

Solid Converter 70

**Mikael Gustavsson, J. Jacob Wikner, Nianxiong
Tan**

Scientific and Technical Aerospace Reports ,1971

Low-Power High-Resolution Analog to Digital

Converters Amir Zjajo, José Pineda de

Gyvez, 2010-10-29 With the fast advancement of CMOS fabrication technology, more and more signal-processing functions are implemented in the digital domain for a lower cost, lower power consumption, higher yield, and higher re-configurability. This has recently generated a great demand for low-power, low-voltage A/D converters that can be realized in a mainstream deep-submicron CMOS technology. However, the discrepancies between lithography wavelengths and circuit feature sizes are increasing. Lower power supply voltages significantly reduce noise margins and increase variations in process, device and design parameters. Consequently, it is steadily more difficult to control the fabrication process precisely enough to maintain uniformity. The inherent randomness of materials used in fabrication at nanoscopic scales means that performance will be increasingly variable, not only from die-to-die but also within each individual die. Parametric variability will be compounded by degradation in nanoscale integrated circuits resulting in instability of parameters over time, eventually leading to the development of faults. Process variation cannot be solved by improving manufacturing tolerances; variability must be reduced by new device technology or managed by design in order for scaling to continue. Similarly, within-die performance variation also imposes new challenges for test

methods. In an attempt to address these issues, Low-Power High-Resolution Analog-to-Digital Converters specifically focus on: i) improving the power efficiency for the high-speed, and low spurious spectral A/D conversion performance by exploring the potential of low-voltage analog design and calibration techniques, respectively, and ii) development of circuit techniques and algorithms to enhance testing and debugging potential to detect errors dynamically, to isolate and confine faults, and to recover errors continuously. The feasibility of the described methods has been verified by measurements from the silicon prototypes fabricated in standard 180nm, 90nm and 65nm CMOS technology.

The Everyday Internet All-in-One Desk Reference For Dummies Peter Weverka, 2005-04-29 The Internet made its way into everyday life as a tool people used occasionally to keep in touch with friends and gather information for personal or business needs. Now, thanks to high-speed connections, wireless access, and safe and powerful Web sites, the Internet has become the main means for handling personal finance, shopping for big-ticket items, and communicating with people around the world. It's to the point where many people can't get through the day without turning to the Internet to get things accomplished. The Everyday Internet All-in-One Desk Reference For Dummies is the complete resource for casual Internet users who are looking to make the jump to becoming experienced navigators of the wired world. Written by Internet guru Peter Weverka, this book walks readers through the basics of going online before

heading into the realms of online bargain shopping, bill paying, personal finance, keeping up with hobbies, and even setting up an online business. * The material is broken into mini-books that make it easier to find an answer and keep moving along the online highway * This book clarifies all the mysteries of how to use the Internet to make everyday life simpler * Covers key Internet properties like eBay, Google, and Yahoo! as well as favorite tasks like playing games, tracing family roots, and keeping a diary online

Research on Particle Imaging Detectors Georges Charpak, 1995-07-07 Much instrumentation has been developed for imaging the trajectories of elementary particles produced in high energy collisions. Since 1968, gaseous detectors, beginning with multiwire chambers and drift chambers, have been used for the visualisation of particle trajectories and the imaging of X-rays, neutrons, hard gamma rays, beta rays and ultraviolet photons. This book commemorates the groundbreaking research leading to the evolution of such detectors carried out at CERN by Georges Charpak, Nobel Prizewinner for Physics in 1992. Besides collecting his key papers, the book also includes original linking commentary which sets his work in the context of other worldwide research. Contents: Low Energy Nuclear Physics with Gaseous Detectors: 1950–1959 Detectors with Optical Imaging: 1956–1992 Filmless Spark Chambers: 1962–1967 Wire Chambers and Drift Chambers: 1968–1994 Multistep Avalanche Chambers: 1978–1990 Imaging of Vacuum Ultra Violet Photons.

Application to Cerenkov and Gamma
ImagingMiscellaneous Detectors:
1962–1994Applications of Detectors to Biology or
Medicine: 1970–1992 Readership: Nuclear
physicists, particle physicists and applied
physicists. keywords:Detecting Ionization
Radiations;Imaging Ionizing Radiations;Low Dose
Radiography;Gaseous Detectors;High Energy Physics
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Ham Radio Magazine ,1979-07

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Administration. Scientific and Technical
Information Program,1992

CMOS Data Converters for Communications Mikael
Gustavsson,J. Jacob Wikner,Nianxiong
Tan,2006-04-18 CMOS Data Converters for
Communications distinguishes itself from other
data converter books by emphasizing system-related
aspects of the design and frequency-domain
measures. It explains in detail how to derive data
converter requirements for a given communication
system (baseband, passband, and multi-carrier
systems). The authors also review CMOS data
converter architectures and discuss their
suitability for communications. The rest of the
book is dedicated to high-performance CMOS data
converter architecture and circuit design.
Pipelined ADCs, parallel ADCs with an improved
passive sampling technique, and oversampling ADCs
are the focus for ADC architectures, while
current-steering DAC modeling and implementation
are the focus for DAC architectures. The

principles of the switched-current and the switched-capacitor techniques are reviewed and their applications to crucial functional blocks such as multiplying DACs and integrators are detailed. The book outlines the design of the basic building blocks such as operational amplifiers, comparators, and reference generators with emphasis on the practical aspects. To operate analog circuits at a reduced supply voltage, special circuit techniques are needed. Low-voltage techniques are also discussed in this book. CMOS Data Converters for Communications can be used as a reference book by analog circuit designers to understand the data converter requirements for communication applications. It can also be used by telecommunication system designers to understand the difficulties of certain performance requirements on data converters. It is also an excellent resource to prepare analog students for the new challenges ahead.

Circuit Techniques for Low-Voltage and High-Speed A/D Converters Mikko E. Waltari, Kari A.I. Halonen, 2006-04-18 This useful monograph presents a total of seven prototypes: two double-sampled S/H circuits, a time-interleaved ADC, an IF-sampling self-calibrated pipelined ADC, a current steering DAC with a deglitcher, and two pipelined ADCs employing the S0 techniques.

NASA Tech Brief, 1970

NANO-CHIPS 2030 Boris Murmann, Bernd Hoefflinger, 2020-06-08 In this book, a global team of experts from academia, research institutes and industry presents their vision on how new nano-chip architectures will enable the performance and

energy efficiency needed for AI-driven advancements in autonomous mobility, healthcare, and man-machine cooperation. Recent reviews of the status quo, as presented in CHIPS 2020 (Springer), have prompted the need for an urgent reassessment of opportunities in nanoelectronic information technology. As such, this book explores the foundations of a new era in nanoelectronics that will drive progress in intelligent chip systems for energy-efficient information technology, on-chip deep learning for data analytics, and quantum computing. Given its scope, this book provides a timely compendium that hopes to inspire and shape the future of nanoelectronics in the decades to come.

NASA Patent Abstracts Bibliography United States. National Aeronautics and Space Administration. Scientific and Technical Information Office, 1972

Modular Low-Power, High-Speed CMOS Analog-to-Digital Converter of Embedded Systems Keh-La Lin, Armin Kemna, Bedrich J. Hosticka, 2003-04-30 One of the main trends of microelectronics is toward design for integrated systems, i.e., system-on-a-chip (SoC) or system-on-silicon (SoS). Due to this development, design techniques for mixed-signal circuits become more important than before. Among other devices, analog-to-digital and digital-to-analog converters are the two bridges between the analog and the digital worlds. Besides, low-power design technique is one of the main issues for embedded systems, especially for hand-held applications. **Modular Low-Power, High-Speed CMOS Analog-to-Digital Converter for Embedded Systems**

aims at design techniques for low-power, high-speed analog-to-digital converter processed by the standard CMOS technology. Additionally this book covers physical integration issues of A/D converter integrated in SoC, i.e., substrate crosstalk and reference voltage network design.

Metals Abstracts ,1970

High-Performance D/A-Converters Martin

Clara,2012-12-22 This book deals with modeling and implementation of high performance, current-steering D/A-converters for digital transceivers in nanometer CMOS technology. In the first part, the fundamental performance limitations of current-steering DACs are discussed. Based on simplified models, closed-form expressions for a number of basic non-ideal effects are derived and tested. With the knowledge of basic performance limits, the converter and system architecture can be optimized in an early design phase, trading off circuit complexity, silicon area and power dissipation for static and dynamic performance. The second part describes four different current-steering DAC designs in standard 130 nm CMOS. The converters have a resolution in the range of 12-14 bits for an analog bandwidth between 2.2 MHz and 50 MHz and sampling rates from 100 MHz to 350 MHz. Dynamic-Element-Matching (DEM) and advanced dynamic current calibration techniques are employed to minimize the required silicon area.

Analog-to-Digital Conversion Marcel J.M.

Pelgrom,2010-07-24 A book is like a window that allows you to look into the world. The window is shaped by the author and that makes that every window presents a unique view of the world. This

is certainly true for this book. It is shaped by the topics and the projects throughout my career. Even more so, this book reflects my own style of working and thinking. That starts already in Chap. 2. When I joined Philips Research in 1979, many of my colleagues used little paper notebooks to keep track of the most used equations and other practical things. This notebook was the beginning for Chap. 2: a collection of topics that form the basis for much of the other chapters. Chapter 2 is not intended to explain these topics, but to refresh your knowledge and help you when you need some basics to solve more complex issues. In the chapters discussing the fundamental processes of conversion, you will recognize my preoccupation with mathematics. I really enjoy finding an equation that properly describes the underlying mechanism. Nevertheless mathematics is not a goal in itself: the equations help to understand the way the variables are connected to the result. Real insight comes from understanding the physics and electronics. In the chapters on circuit design I have tried to reduce the circuit diagrams to the simplest form, but not simpler. . . I do have private opinions on what works and what should not be applied.

Delta-Sigma A/D-Converters Richard

Gaggl, 2012-12-17 The emphasis of this book is on practical design aspects for broadband A/D converters for communication systems. The embedded designs are employed for transceivers in the field of ADSL solutions and WLAN applications. An area- and power-efficient realization of a converter is mandatory to remain competitive in the market. The

right choice for the converter topology and architecture needs to be done very carefully to result in a competitive FOM. The book begins with a brief overview of basic concepts about ADSL and WLAN to understand the ADC requirements. At architectural level, issues on different modulator topologies are discussed employing the provided technology node. The design issues are pointed out in detail for modern digital CMOS technologies, beginning with 180nm followed by 130nm and going down to 65nm feature size. Beside practical aspects, challenges to mixed-signal design level are addressed to optimize the converters in terms of consumed chip area, power consumption and design for high yield in volume production. Thus, careful considerations on circuit- and architectural- level are performed by introducing a dynamic-biasing technique, a feed-forward approach and a resolution in time instead of amplitude resolution.

Extractive Metallurgy of Copper Mark E.

Schlesinger, Matthew J. King, Kathryn C.

Sole, William G. Davenport, 2011-07-26 This multi-author new edition revises and updates the classic reference by William G. Davenport et al (winner of, among other awards, the 2003 AIME Mineral Industry Educator of the Year Award for inspiring students in the pursuit of clarity), providing fully updated coverage of the copper production process, encompassing topics as diverse as environmental technology for wind and solar energy transmission, treatment of waste by-products, and recycling of electronic scrap for potential alternative technology implementation. The authors

examine industrially grounded treatments of process fundamentals and the beneficiation of raw materials, smelting and converting, hydrometallurgical processes, and refining technology for a mine-to-market perspective - from primary and secondary raw materials extraction to shipping of rod or billet to customers. The modern coverage of the work includes both smelting processes such as Ausmelt and Isasmelt, which have become state-of-the-art in sulfide concentrate smelting and converting. Drawing on extensive international industrial consultancies within working plants, this work describes in depth the complete copper production process, starting from both primary and secondary raw materials and ending with rod or billet being shipped to customers. The work focuses particularly on currently-used industrial processes used to turn raw materials into refined copper metal rather than ideas working 'only on paper'. New areas of coverage include the environmentally appropriate uses of copper cables in power transmission for wind and solar energy sources; the recycling of electronic scrap as an important new feedstock to the copper industry, and state-of-the-art Ausmelt and Isasmelt both smelting processes for sulfide concentrate smelting and converting.

Analog Circuit Design Michiel Steyaert, Arthur van Roermund, Andrea Baschirotto, 2011-09-15 *Analog Circuit Design* contains the contribution of 18 tutorials of the 20th workshop on Advances in Analog Circuit Design. Each part discusses a specific to-date topic on new and valuable design ideas in the area of analog circuit design. Each

part is presented by six experts in that field and state of the art information is shared and overviewed. This book is number 20 in this successful series of Analog Circuit Design, providing valuable information and excellent overviews of: Topic 1 : Low Voltage Low Power, chairman: Andrea Baschirotto Topic 2 : Short Range Wireless Front-Ends, chairman: Arthur van Roermund Topic 3 : Power Management and DC-DC, chairman : Michiel Steyaert. Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field. The tutorial coverage also makes it suitable for use in an advanced design course.

Performance of a Pressurized Multi-wire Proportional Chamber for Neutron Imaging Bruce Arthur Director, 1979

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Solid Converter 70 Introduction

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