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**National Institute of Standards and
Technology (U.S.)**

Cumulated Index Medicus ,1997

Chinese Criminal Entrepreneurs in Canada, Volume I

Alex Chung,2019-03-09 This book explores the historical origins, activities, and structure of the archetypal 'new' Asian criminal entrepreneurs in Canada, known as The Big Circle Boys (BCB). It traces their illegal immigration abroad from Guangzhou, the extent to which they are organised and violent, and what the future holds for them in Canada. The BCB's organisational features are examined against theories and legislation of organised crime to understand how they compare to other criminal entities. For the first time, a unique glimpse is provided into the workings of an elusive cellular network comprised of BCB dai lo (bosses). Through interviews and official documents, their criminal undertakings and structural dimensions are pieced together to show how their interdependent and collaborative cells enabled them to form a dynamic criminal community. This book speaks to those interested in how a collective of ethnic-Chinese career criminals have replaced traditional criminal organisations in transnational criminal markets, particularly for scholars and students of social sciences disciplines.

Mobile Point-of-Care Monitors and Diagnostic Device

Design Walter Karlen,2018-09-03 Efficient mobile systems that allow for vital sign monitoring and disease diagnosis at the point of care can help combat issues such as rising healthcare costs, treatment delays in remote and resource-poor areas, and the global shortage of skilled medical personnel. Covering everything from sensors, systems, and software to integration, usability, and regulatory challenges, Mobile Point-of-Care Monitors and Diagnostic Device Design offers valuable insight into state-of-the-art technologies, research, and methods for designing

personal diagnostic and ambulatory healthcare devices. Presenting the combined expertise of contributors from various fields, this multidisciplinary text: Gives an overview of the latest mobile health and point-of-care technologies Discusses portable diagnostics devices and sensors, including mobile-phone-based health systems Explores lab-on-chip systems as well as energy-efficient solutions for mobile point-of-care monitors Addresses computer vision and signal processing for real-time diagnostics Considers interface design for lay healthcare providers and home users Mobile Point-of-Care Monitors and Diagnostic Device Design provides important background information about the design process of mobile health and point-of-care devices, using practical examples to illustrate key aspects related to instrumentation, information processing, and implementation.

Virtual Environments and Scientific Visualization '96

Martin Göbel, Jaques David, Pavel Slavik, Jarke van Wijk, 2012-12-06 Selected papers from this year's Workshops on Virtual Environments and on Visualization in Scientific Computing are included in this volume. The papers on VE discuss Virtual Environment System architecture, communication requirements, synthetic actors, crowd simulations and modeling aspects, application experience in surgery support, geographic information systems, and engineering and virtual housing systems. Contributions from the Visualization workshop are presented in four groups: volume rendering, user interfaces in scientific visualization, architecture of scientific visualization systems and flow visualization.

Index of Patents Issued from the United States Patent and Trademark Office ,1995

Top-Down Design of High-Performance Sigma-Delta Modulators Fernando Medeiro, Belén Pérez

Verdú, Angel Rodríguez-Vázquez, 2013-04-18 The interest for ΔI -modulation-based NO converters has significantly increased in the last years. The reason for that is twofold. On the one hand, unlike other converters that need accurate building blocks to obtain high resolution, ΔI -converters show low sensitivity to the imperfections of their building blocks. This is achieved through extensive use of digital signal processing - a desirable feature regarding the implementation of NO interfaces in mainstream CMOS technologies which are better suited for implementing fast, dense, digital circuits than accurate analog circuits. On the other hand, the number of applications with industrial interest has also grown. In fact, starting from the earliest in the audio band, today we can find ΔI -converters in a large variety of NO interfaces, ranging from instrumentation to communications. These advances have been supported by a number of research works that have led to a considerably large amount of published papers and books covering different sub-topics: from purely theoretical aspects to architecture and circuit optimization. However, so much material is often difficultly digested by those unexperienced designers who have been committed to developing a ΔI -converter, mainly because there is a lack of methodology. In our view, a clear methodology is necessary in ΔI -modulator design because all related tasks are rather hard.

Applied Science & Technology Index, 1997

Stochastic Process Variation in Deep-Submicron CMOS Amir Zjajo, 2013-11-19 One of the most notable features of nanometer scale CMOS technology is the increasing magnitude of variability of the key device parameters affecting performance of integrated circuits. The growth of variability can be attributed to multiple factors, including the difficulty of manufacturing control,

the emergence of new systematic variation-generating mechanisms, and most importantly, the increase in atomic-scale randomness, where device operation must be described as a stochastic process. In addition to wide-sense stationary stochastic device variability and temperature variation, existence of non-stationary stochastic electrical noise associated with fundamental processes in integrated-circuit devices represents an elementary limit on the performance of electronic circuits. In an attempt to address these issues, *Stochastic Process Variation in Deep-Submicron CMOS: Circuits and Algorithms* offers unique combination of mathematical treatment of random process variation, electrical noise and temperature and necessary circuit realizations for on-chip monitoring and performance calibration. The associated problems are addressed at various abstraction levels, i.e. circuit level, architecture level and system level. It therefore provides a broad view on the various solutions that have to be used and their possible combination in very effective complementary techniques for both analog/mixed-signal and digital circuits. The feasibility of the described algorithms and built-in circuitry has been verified by measurements from the silicon prototypes fabricated in standard 90 nm and 65 nm CMOS technology.

Advances in Database Technology EDBT '96

Mokrane Bouzeghoub, Georges Gardarin, 1996-03-18 This book presents the refereed proceedings of the Fifth International Conference on Extending Database Technology, EDBT'96, held in Avignon, France in March 1996. The 31 full revised papers included were selected from a total of 178 submissions; also included are some industrial-track papers, contributed by partners of several ESPRIT projects. The volume is organized in topical sections on data mining, active databases, design tools,

advanced DBMS, optimization, warehousing, system issues, temporal databases, the web and hypermedia, performance, workflow management, database design, and parallel databases.

Analog Layout Generation for Performance and Manufacturability Koen Lampaert, Georges Gielen, Willy M.C. Sansen, 2013-04-18 Analog integrated circuits are very important as interfaces between the digital parts of integrated electronic systems and the outside world. A large portion of the effort involved in designing these circuits is spent in the layout phase. Whereas the physical design of digital circuits is automated to a large extent, the layout of analog circuits is still a manual, time-consuming and error-prone task. This is mainly due to the continuous nature of analog signals, which causes analog circuit performance to be very sensitive to layout parasitics. The parasitic elements associated with interconnect wires cause loading and coupling effects that degrade the frequency behaviour and the noise performance of analog circuits. Device mismatch and thermal effects put a fundamental limit on the achievable accuracy of circuits. For successful automation of analog layout, advanced place and route tools that can handle these critical parasitics are required. In the past, automatic analog layout tools tried to optimize the layout without quantifying the performance degradation introduced by layout parasitics. Therefore, it was not guaranteed that the resulting layout met the specifications and one or more layout iterations could be needed. In *Analog Layout Generation for Performance and Manufacturability*, the authors propose a performance driven layout strategy to overcome this problem. In this methodology, the layout tools are driven by performance constraints, such that the final layout, with parasitic effects, still satisfies the specifications of the circuit. The

performance degradation associated with an intermediate layout solution is evaluated at runtime using predetermined sensitivities. In contrast with other performance driven layout methodologies, the tools proposed in this book operate directly on the performance constraints, without an intermediate parasitic constraint generation step. This approach makes a complete and sensible trade-off between the different layout alternatives possible at runtime and therefore eliminates the possible feedback route between constraint derivation, placement and layout extraction. Besides its influence on the performance, layout also has a profound impact on the yield and testability of an analog circuit. In *Analog Layout Generation for Performance and Manufacturability*, the authors outline a new criterion to quantify the detectability of a fault and combine this with a yield model to evaluate the testability of an integrated circuit layout. They then integrate this technique with their performance driven routing algorithm to produce layouts that have optimal manufacturability while still meeting their performance specifications. *Analog Layout Generation for Performance and Manufacturability* will be of interest to analog engineers, researchers and students.

Systematic Design of CMOS Switched-Current Bandpass Sigma-Delta Modulators for Digital Communication Chips José M. de la Rosa, Belén Pérez-Verdú, Ángel Rodríguez-Vázquez, 2007-05-08 This very detailed book discusses architectures, circuits and procedures for the optimum design of bandpass sigma-delta A/D interfaces for mixed-signal chips in standard CMOS technologies. It provides uniquely in-depth coverage of switched-current errors, which supports the design of high performance SI chips.

Mastering Autodesk Revit MEP 2016 Simon

Whitbread,2015-09-01 Get up and running on Autodesk Revit MEP 2016 with this detailed, hands-on guide Mastering Autodesk Revit MEP 2016 provides perfectly paced coverage of all core concepts and functionality, with tips, tricks, and hands-on exercises that help you optimize productivity. With a focus on real-world uses and workflows, this detailed reference explains Revit MEP tools and functionality in the context of professional design and provides the practical insight that can only come from years of experience. Coverage includes project setup, work sharing, building loads, ductwork, electrical and plumbing, and much more, with clear explanation every step of the way. The companion website features downloadable tutorials that reinforce the material presented, allowing you to jump in at any point and compare your work to the pros. This is your guide to master the capabilities of this essential productivity-enhancing tool. Generate schedules that show quantities, materials, design dependencies, and more Evaluate building loads, and design logical air, water, and fire protection systems Create comprehensive electrical and plumbing plans tailored to the project Model your design with custom parameters, symbols, fixtures, devices, and more If you're ready to get on board this emerging design, collaboration, and documentation paradigm, Mastering Autodesk Revit MEP 2016 is the one-stop resource you need.

Index to IEEE Publications Institute of Electrical and Electronics Engineers,1997

Advanced Automotive Technologies: Annual Report to Congress, Fiscal Year 1996 ,1998

OOIS'96 Dilipkumar Patel,Yuan Sun,Shushmaben Patel,2012-12-06 This volume contains the papers presented at the Third International Conference on Object Oriented Information Systems (OOIS'96) which was held at

South Bank University, London. The keynote addresses, by Professor Colette Roland and Mr Ian Graham, are also included. The acceptance rate for papers was around 47%. The papers for the Industry Day were invited papers. The keynote paper by Professor Roland analyses the challenges in object modelling, particularly the impact of requirements engineering for conceptual modelling. She suggests innovative research perspectives to enhance and extend object oriented approaches in order to deal with the emerging area of requirements engineering. The keynote paper presented by Mr. Graham focuses on the problems and solutions for adopting use cases. In his paper, Graham illustrates the theoretical issues and practical problems of use cases, and highlights them using examples. The papers included in this volume cover different aspects of object modelling, object oriented software development, object databases, and interoperability. In the modelling session, Ram, et al. outline an extended object model to tackle the problems of capturing complex requirements of office information systems. Simons' paper concentrates on core object modelling concepts and presents a mathematical theory of class.

Static and Dynamic Performance Limitations for High Speed D/A Converters Anne van den Bosch, Michiel Steyaert, Willy M.C. Sansen, 2013-06-29 Static and Dynamic Performance Limitations for High Speed D/A Converters discusses the design and implementation of high speed current-steering CMOS digital-to-analog converters. Starting from the definition of the basic specifications for a D/A converter, the elements determining the static and dynamic performance are identified. Different guidelines based on scientific derivations are suggested to optimize this performance. Furthermore, a new closed formula has been derived to account for the influence of the transistor

mismatch on the achievable resolution of the current-steering D/A converter. To allow a thorough understanding of the dynamic behavior, a new factor has been introduced. Moreover, the frequency dependency of the output impedance introduces harmonic distortion components which can limit the maximum attainable spurious free dynamic range. Finally, the last part of the book gives an overview on different existing transistor mismatch models and the link with the static performance of the D/A converter.

IAS '96 IEEE Industry Applications Society.
Meeting, 1996

A Guide to the Use and Calibration of Detector Array Equipment Gordon R. Hopkinson, Teresa M.

Goodman, Stuart R. Prince, 2004 The guide arose from a survey of UK users and manufacturers/developers to assess detector equipment usage and calibration, in order to identify the factors influencing the accuracy of measurements obtained with detector array equipment, and thus develop calibration techniques and establish best practice procedures. The text contains both a review of the existing literature and a large amount of new experimental data obtained during the course of the study. The main emphasis has been on UV, visible, and near-infrared systems that use silicon detector technology, but the issues arising in thermal imaging with infrared detector arrays have also been addressed, along with brief sections on EM CCDs for low-light-level imaging and on lag effects in CMOS active pixel sensors.

International Aerospace Abstracts , 1996

Publications of the National Institute of Standards and Technology ... Catalog National Institute of Standards and Technology (U.S.), 1994

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