

Multitone Generator 19

Guillaume de la Roche,Andrés Alayón-Glazunov,Ben Allen

Official Gazette of the United States Patent and Trademark Office ,2002

Multi-Mode / Multi-Band RF Transceivers for Wireless Communications Gernot Hueber,Robert Bogdan Staszewski,2011-02-22 Summarizes cutting-edge physical layer technologies for multi-mode wireless RF transceivers. Includes original contributions from distinguished researchers and professionals. Covers cutting-edge physical layer technologies for multi-mode wireless RF transceivers. Contributors are all leading researchers and professionals in this field.

LTE-Advanced and Next Generation Wireless Networks Guillaume de la Roche,Andrés Alayón-Glazunov,Ben Allen,2012-11-05 LTE- A and Next Generation Wireless Networks: ChannelModeling and Performance describes recent advances inpropagation and channel modeling necessary for simulating nextgeneration wireless systems. Due to the radio spectrum scarcity,two fundamental changes are anticipated compared to the currentstatus. Firstly, the strict reservation of a specific band for aunique standard could evolve toward a priority policy allowing theco-existence of secondary users in a band allocated to a primarysystem. Secondly, a huge increase of the number of cells isexpected by combining outdoor base stations with smaller cells suchas pico/femto cells and relays. This evolution is accompanied withthe emergence of cognitive radio that becomes a reality interminals together with the development of self-organizationcapabilities and distributed cooperative behaviors. The book is divided into three parts: Part I addresses the fundamentals (e.g. technologies, channelmodeling principles etc.) Part II addresses propagation and modeling discussing topicssuch as indoor propagation, outdoor propagation, etc. Part III explores system performance and applications (e.g.MIMO Over-the-air testing, electromagnetic safety, etc).

Digital Waveform Generation Pete Symons,2013-10-17 This overview introduces powerful, flexible and practical digital waveform generation techniques, enabling the design of bespoke digital waveform generation systems from scratch. Including detailed hardware design examples and downloadable Mathcad models created for 'what if?' design scenarios, this is essential reading for professional members of the digital signal processing community.

Official Gazette of the United States Patent Office United States. Patent Office,1970

19th IEEE VLSI Test Symposium ,2001 Collects 58 papers from the April/May 2001 symposium that explore new approaches in the testing of electronic circuits and systems. Key areas in testing are discussed, such as BIST, analog measurement, fault tolerance, diagnosis methods, scan chain design, memory test and diagnosis, and test data compression and compaction. Also on the program are sessions on emerging areas that are gaining prominence, including low power testing, testing high speed circuits on low cost testers, processor based self test techniques, and core- based system-on-chip testing. Some of the topics are robust and low cost BIST architectures for sequential fault testing in datapath multipliers, a method for measuring the cycle-to-cycle period jitter of high-frequency clock signals, fault equivalence identification using redundancy information and static and dynamic extraction, and test scheduling for minimal energy consumption under power constraints. No subject index. c. Book News Inc.

Analog Test Signal Generation Using Periodic $\Sigma\Delta$ -Encoded Data Streams Benoit Dufort,G.W. Roberts,2012-12-06 Analog Test Signal Generation Using Periodic SigmaDelta-Encoded Data Streams presents a new method to generate high quality analog signals with low hardware complexity. The theory of periodic SigmaDelta-encoded bitstreams is presented along with a set of empirical tables to help select the appropriate parameters of a bitstream. An optimization procedure is also outlined to help select a bit sequence with the desired attributes. A large variety of signals can be generated using this approach. Silicon implementation issues are discussed with a specific emphasis on area overhead and ease of design. One FPGA circuit and three different silicon implementations are presented along with experimental results. It is shown that simple designs are capable of generating very high precision signals-on-chip. The technique is further extended to multi-bit signal generation where it is shown how to increase the performance of arbitrary waveform, generators commonly found in past and present-day mixed-signal testers. No hardware modifications are required, only the numbers in memory are changed. Three different calibration techniques to reduce the effects of the AWG's non-linearities are also introduced, together with supporting experimental evidence. The main focus of this text is to describe an area-efficient technique for analog signal generation using SigmaDelta-encoded data stream. The main characteristics of the technique are: High quality signals (SFDR of 110 dB observed); Large variety of signals generated; Bitstreams easily obtained with a fast optimization program; Good frequency resolution, compatible with coherent sampling; Simple and fast hardware implementation; Mostly digital, except an easily testable 1-bit DAC and possibly a reconstruction filter; Memory already available on-chip can be reused, reducing area overhead; Designs can be incorporated into existing CAD tools; High frequency generation.

The New International Encyclopædia Frank Moore Colby,Talcott Williams,1917

The New International Encyclopaedia Frank Moore Colby,Talcott Williams,1923

The New International Encyclopedia Frank Moore Colby,Talcott Williams,1922

Index of Patents Issued from the United States Patent Office United States. Patent Office,1968

Index of Patents Issued from the United States Patent and Trademark Office ,1990

Fast Hopping Frequency Generation in Digital CMOS Mohammad Farazian,Lawrence E. Larson,Prasad S. Gudem,2012-10-12 Overcoming the agility limitations of conventional frequency synthesizers in multi-band OFDM ultra wideband is a key research goal in digital technology. This volume outlines a frequency plan that can generate all the required frequencies from a single fixed frequency, able to implement center frequencies with no more than two levels of SSB mixing. It recognizes the need for future synthesizers to bypass on-chip inductors and operate at low voltages to enable the increased integration and efficiency of networked appliances. The author examines in depth the architecture of the dividers that generate the necessary frequencies from a single base frequency and are capable of establishing a fractional division ratio. Presenting the first CMOS inductorless single PLL 14-band frequency synthesizer for MB-OFDMUWB makes this volume a key addition to the literature, and with the synthesizer capable of arbitrary band-hopping in less than two nanoseconds, it operates well within the desired range on a 1.2-volt power supply. The author's close analysis of the operation, stability, and phase noise of injection-locked regenerative frequency dividers will provide researchers and technicians with much food for developmental thought.

Conference Proceedings, 19th European Microwave Conference 89 ,1989

Practical Applications Circuits Handbook Anne Fischer Lent,2013-07-19 Practical Applications Circuits Handbook focuses on the various circuit designs and applications collected from manufacturer data. This book describes the overall design of each circuit and provides background information on its concepts and components. Organized into 23 chapters, this book starts with an overview of the various types and general designs of several audio amplifiers, including high power audio amplifier, gain-controlled stereo amplifier, and ceramic pickup amplifier. This text then explores several automotive circuits and explains their practical applications, including the speed warning device, auto burglar alarm, tachometer, automobile voltage regulator, and car radio. Other chapters describe the wind-powered battery charger, which can be used as a remote source of power where wind energy is abundant. This book discusses as well the general design of automatic light control wherein the control turns on a lamp when the input to the photodiode falls below a particular value. This book is a valuable resource for engineers, students, and hobbyists.

Approval Guide ,1985

Information Technology Ricardo Reis,2004-07-27 This book contains a selection of tutorials on hot topics in information technology, which were presented at the IFIP World Computer Congress. WCC2004 took place at the Centre de Congrès Pierre Baudis, in Toulouse, France, from 22 to 27 August 2004. The 11 chapters included in the book were chosen from tutorials proposals submitted to WCC2004. These papers report on several important and state-of-the-art topics on information technology such as: Quality of Service in Information Networks Risk-Driven Development of Security-Critical Systems Using UMLsec Developing Portable Software Formal Reasoning About Systems, Software and Hardware Using Functionals, Predicates and Relations The Problematic of Distributed Systems Supervision Software Rejuvenation - Modeling and Analysis Test and Design-for-Test of Mixed-Signal Integrated Circuits Web Services Applications of Multi-Agent Systems Discrete Event Simulation Human-Centered Automation

We hereby would like to thank IFIP and more specifically WCC2004 Tutorials Committee and the authors for their contribution. We also would like to thank the congress organizers who have done a great job. Ricardo Reis Editor QUALITY OF SERVICE IN INFORMATION NETWORKS Augusto Casaca IST/INESC, R. Alves Redol, 1000-029, Lisboa, Portugal. Abstract: This article introduces the problems concerned with the provision of end-end quality of service in IP networks, which are the basis of information networks, describes the existing solutions for that provision and presents some of the current research items on the subject. Key words: Information networks, IP networks, Integrated Services, Differentiated Services, Multiprotocol Label Switching, UMTS.

EDN. ,1982

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar,Georges G. E. Gielen,2002-05-06 The tools and techniques you need to break the analog design bottleneck! Ten years ago, analog seemed to be a dead-end technology. Today, System-on-Chip (SoC) designs are increasingly mixed-signal designs. With the advent of application-specific integrated circuits (ASIC) technologies that can integrate both analog and digital functions on a single chip, analog has become more crucial than ever to the design process. Today, designers are moving beyond hand-crafted, one-transistor-at-a-time methods. They are using new circuit and physical synthesis tools to design practical analog circuits; new modeling and analysis tools to allow rapid exploration of system level alternatives; and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago. To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field, this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools. Areas covered are: * Analog synthesis * Symbolic analysis * Analog layout * Analog modeling and analysis * Specialized analog simulation * Circuit centering and yield optimization * Circuit testing Computer-Aided Design of Analog Integrated Circuits and Systems is the cutting-edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck.

Optical Modulation Le Nguyen Binh,2017-11-22 This books aims to present fundamental aspects of optical communication techniques and advanced modulation techniques and extensive applications of optical communications systems and networks employing single-mode optical fibers as the transmission system. New digital techqniues such as chromatic dispersion, polarization mode dispersion, nonlinear phase distortion effects, etc. will be discussed. Practical models for practice and understanding the behavior and dynamics of the devices and systems will be included.

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