

Logiccircuit

Shimon P. Vingron

CMOS Logic Circuit Design John P. Uyemura,2007-05-08 This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

Logic Circuit Design Shimon P. Vingron,2012-03-28 In three main divisions the book covers combinational circuits, latches, and asynchronous sequential circuits. Combinational circuits have no memorising ability, while sequential circuits have such an ability to various degrees. Latches are the simplest sequential circuits, ones with the shortest memory. The presentation is decidedly non-standard. The design of combinational circuits is discussed in an orthodox manner using normal forms and in an unorthodox manner using set-theoretical evaluation formulas relying heavily on Karnaugh maps. The latter approach allows for a new design technique called composition. Latches are covered very extensively. Their memory functions are expressed mathematically in a time-independent manner allowing the use of (normal, non-temporal) Boolean logic in their calculation. The theory of latches is then used as the basis for calculating asynchronous circuits. Asynchronous circuits are specified in a tree-representation, each internal node of the tree representing an internal latch of the circuit, the latches specified by the tree itself. The tree specification allows solutions of formidable problems such as algorithmic state assignment, finding equivalent states non-recursively, and verifying asynchronous circuits.

An Introduction to Logic Circuit Testing Parag K. Lala,2022-06-01 An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits/systems. The material covered in the book should be sufficient for a course, or part of a course, in digital circuit testing for senior-level undergraduate and first-year graduate students in Electrical Engineering and Computer Science. The book will also be a valuable resource for engineers working in the industry. This book has four chapters. Chapter 1 deals with various types of faults that may occur in very large scale integration (VLSI)-based digital circuits. Chapter 2 introduces the major concepts of all test generation techniques such as redundancy, fault coverage, sensitization, and backtracking. Chapter 3 introduces the key concepts of testability, followed by some ad hoc design-for-testability rules that can be used to enhance testability of combinational circuits. Chapter 4 deals with test generation and response evaluation techniques used in BIST (built-in self-test) schemes for VLSI chips. Table of Contents: Introduction / Fault Detection in Logic Circuits / Design for Testability / Built-in Self-Test / References

An Introduction to Logic Circuit Testing Parag K. Lala,2009 An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits/systems. The material covered in the book should be sufficient for a course, or part of a course, in digital circuit testing for senior-level undergraduate and first-year graduate students in Electrical Engineering and Computer Science. The book will also be a valuable resource for engineers working in the industry. This book has four chapters. Chapter 1 deals with various types of faults that may occur in very large scale integration (VLSI)-based digital circuits. Chapter 2 introduces the major concepts of all test generation techniques such as redundancy, fault coverage, sensitization, and backtracking. Chapter 3 introduces the key concepts of testability, followed by some ad hoc design-for-testability rules that can be used to enhance testability of combinational circuits. Chapter 4 deals with test generation and response evaluation techniques used in BIST (built-in self-test) schemes for VLSI chips. Table of Contents: Introduction / Fault Detection in Logic Circuits / Design for Testability / Built-in Self-Test / References

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Digital Electronics: A Primer - Introductory Logic Circuit Design Mark S Nixon,2015-01-27 This practical introduction explains exactly how digital circuits are designed, from the basic circuit to the advanced system. It covers combinational logic circuits, which collect logic signals, to sequential logic circuits, which embody time and memory to progress through sequences of states. The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions. Based on the author's extensive experience in teaching digital electronics to undergraduates, the book translates theory directly into practice and presents the essential information in a compact, digestible style. Worked problems and examples are accompanied by abbreviated solutions, with demonstrations to ensure that the design material and the circuits' operation are fully understood. This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction.

Logic Circuit Design Shimon P. Vingron,2023-12-29 The 2nd edition has been thoroughly revised and is intended as a wakeup call in the stagnant and dormant field of switching algebra and logic circuit design. It presents the material in a concise but thorough way. The topics selected are an in-depth presentation of switching algebra, a theory of memory circuits (sometimes called flop flops), a new approach to asynchronous circuits, and a newly added part presenting a unique programming technique (or language) for programmable logic controllers (PLCs). Be ready for the unorthodox and controversial.

Digital Logic Circuit Analysis and Design Victor Peter Nelson,1995 For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore- or junior-level. Many recent texts place instructors in the difficult position of choosing between authoritative, state-of-the art coverage and an approach that is highly supportive of student learning. This carefully developed text was widely praised by reviewers for both its great clarity and its rigor. The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language and has abundant coverage of current topics of interest, such as programmable devices, computer-aided design, and testability. An unusually large number of illustrations, examples, and problems help students gain a solid sense of how theory underlies practice.

Fluid Power Logic Circuit Design Peter Rohner,1979

Introduction to Logic Circuit Design Preliminary Edition Brock J. Lamer,2015-08-31

Introduction to VLSI Systems Ming-Bo Lin,2011-11-28 With the advance of semiconductors and ubiquitous computing, the use of system-on-a-chip (SoC) has become an essential technique to reduce product cost. With this progress and continuous reduction of feature sizes, and the development of very large-scale integration (VLSI) circuits, addressing the harder problems requires fundamental understanding of circuit and layout design issues. Furthermore, engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer-aided design (CAD) tools. Introduction to VLSI Systems: A Logic, Circuit, and System Perspective addresses the need for teaching such a topic in terms of a logic, circuit, and system design perspective. To achieve the above-mentioned goals, this classroom-tested book focuses on: Implementing a digital system as a full-custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal-oxide-semiconductor (CMOS) VLSI Important issues of modern CMOS processes, including

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Introduction to Logic Circuit Design Brock J. Lamerer,2015-10-12

Electronic Logic Circuits J. Gibson,2013-01-11 Most branches of organizing utilize digital electronic systems. This book introduces the design of such systems using basic logic elements as the components. The material is presented in a straightforward manner suitable for students of electronic engineering and computer science. The book is also of use to engineers in related disciplines who require a clear introduction to logic circuits. This third edition has been revised to encompass the most recent advances in technology as well as the latest trends in components and notation. It includes a wide coverage of application specific integrated circuits (ASICs), many worked examples and a step-by-step logical and practical approach.

Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,1999

Introduction to Logic Circuit Theory Igor Aleksander,1970

Digital Logic Circuit Analysis and Design [rental Edition] Victor P Nelson,Bill D Carroll,H Troy Nagle,David Irwin,2020-02-18

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Logic Circuit Design Alan W. Shaw,1993 With Shaw's conversational writing style, sophomore engineering and computer science majors will find this text's coverage of combinational and sequential logic design easy to understand and a pleasure to read. The text is remarkably clear and provides extensive examples. Shaw maintains an ongoing relationship with industry, which is reflected in the text's primary goal of preparing students for entry into the workplace--ready to design. The text's design-first organization allows students to master a simple systematic design process, then move to design and analysis of more complex circuits. The use of polarized notation offers students an easy-to-learn notation that clarifies the thought process in design, allows a simplification of the sign process, and improves documentation. Software simulation is stressed in all designed circuits, allowing students to test circuits before committing them to hardware. An early introduction to programmable logic devices reflects their importance in design.

Official Gazette of the United States Patent Office United States. Patent Office,1964

Introduction to Logic Circuits & Logic Design with Verilog Brock J. LaMeres,2019-04-10 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers. Coverage includes both the classical approach to digital system design (i.e., pen and paper) in addition to the modern hardware description language (HDL) design approach (computer-based). Using this textbook enables readers to design digital systems using the modern HDL approach, but they have a broad foundation of knowledge of the underlying hardware and theory of their designs. This book is designed to match the way the material is actually taught in the classroom. Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics. The author has designed the presentation with learning goals and assessment at its core. Each section addresses a specific learning outcome that the student should be able to "do" after its completion. The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome.

Reversible Logic Circuit Ri-Gui Zhou,Naihuan Jing,2015 In the conventional combination of logic circuits, energy loss is an important consideration. Research on reversible logic circuits are of interest to power minimization having applications in low power CMOS design, DNA computing, bioinformatics, nanotechnology, information security and so on. In this book, a novel reversible quantum full adder, reversible BCD adder, subtraction and quantum No-Wait-Carry adder and a novel reversible quantum array multiplier is introduced. At the same time, the model of this array multiplier based on CMOS technology and pass-transistor is also discussed. Reversible arithmetic logic unit and a novel 1-bit reversible comparator and another novel 4-bit reversible comparator are described as well. Finally, this book devotes itself to the theory and simulation of QCA, introduces a study of quantum states in semi-classical simulation and the genetic simulated annealing simulation method based on the polarized rule.

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