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Institute of Electrical and Electronics Engineers,IEEE Computer Society. Design Automation Technical Committee

Cambridge International AS and A Level Computer Science Coursebook Sylvia Langfield,Dave Duddell,2015-12-17 Cambridge International AS and A Level Computer Science Coursebook delivers an accessible guide to theoretical and practical skills in Computer Science, with a clear progression of tasks that help to consolidate and develop knowledge. Cambridge International AS and A Level Computer Science Coursebook offers students detailed descriptions of the concepts, reinforced with examples that outline complex subject matter in a clear way. Alongside fundamental definitions, higher level programming skills are developed through the explanation of processes and consolidated by practical exam-type questions for students to attempt.-- Publisher description.

Application-Specific Integrated Circuits Michael Smith,Professor of European Politics Department of European Studies Michael Smith,1997-06-10 This comprehensive book on application-specific integrated circuits (ASICs) describes the latest methods in VLSI-systems design. ASIC design, using commercial tools and pre-designed cell libraries, is the fastest, most cost-effective, and least error-prone method of IC design. As a consequence, ASICs and ASIC-design methods have become increasingly popular in industry for a wide range of applications. The book covers both semicustom and programmable ASIC types. After describing the fundamentals of digital logic design and the physical features of each ASIC type, the book turns to ASIC logic design - design entry, logic synthesis, simulation, and test - and then to physical design - partitioning, floorplanning, placement, and routing. You will find here, in practical well-explained detail, everything you need to know to understand the design of an ASIC, and everything you must do to begin and to complete your own design. Features Broad coverage includes, in one information-packed volume, cell-based ICs, gate arrays, field-programmable gate arrays (FPGAs), and complex programmable logic devices (PLDs). Examples throughout the book have been checked with a wide range of commercial tools to ensure their accuracy and utility. Separate chapters and appendixes on both Verilog and VHDL, including material from IEEE standards, serve as a complete reference for high-level, ASIC-design entry. As in other landmark VLSI books published by Addison-Wesley - from Mead and Conway to Weste and Eshraghian - the author's teaching expertise and industry experience illuminate the presentation of useful design methods. Any engineer, manager, or student who is working with ASICs in a design project, or who is simply interested in knowing more about the different ASIC types and design styles, will find this book to be an invaluable resource, reference, and guide.

1076-2019 - IEEE Standard for VHDL Language Reference Manual ,

The Design and Analysis of VLSI Circuits Lance A. Glasser,Daniel W. Dobberpuhl,1985

The Verilog® Hardware Description Language Donald Thomas,Philip Moorby,2008-09-11 XV From the Old to the New xvii Acknowledgments xx| Verilog A Tutorial Introduction Getting Started 2 A Structural Description 2 Simulating the binaryToESeg Driver 4 Creating Ports For the Module 7 Creating a Testbench For a Module 8 Behavioral Modeling of Combinational Circuits 11 Procedural Models 12 Rules for Synthesizing Combinational Circuits 13 Procedural Modeling of Clocked Sequential Circuits 14 Modeling Finite State Machines 15 Rules for Synthesizing Sequential Systems 18 Non-Blocking Assignment (

IEEE Standards Interpretations Institute of Electrical and Electronics Engineers,IEEE Computer Society. Design Automation Technical Committee,1992

Principles of CMOS VLSI Design Neil West,Kamran Eshraghian,Michael J. S. Smith,2000-12 This book conveys an understanding of CMOS technology, circuit design, layout, and system design sufficient to the designer. The book deals with the technology down to the layout level of detail, thereby providing a bridge from a circuit to a form that may be fabricated. The early chapters provide a circuit view of the CMOS IC design, the middle chapters cover a sub-system view of CMOS VLSI, and the final section illustrates these techniques using a real-world case study.

Microelectronic Systems Albert Heuberger,Günter Elst,Randolf Hanke,Janina Heppner,Karlheinz Kirsch,2011-12-27 This book is dedicated to Prof. Dr. Heinz Gerhäuser on the occasion of his retirement both from the position of Executive Director of the Fraunhofer Institute for Integrated Circuits IIS and from the Endowed Chair of Information Technologies with a Focus on Communication Electronics (LIKE) at the Friedrich-Alexander-Universität Erlangen-Nürnberg. Heinz Gerhäuser's vision and entrepreneurial spirit have made the Fraunhofer IIS one of the most successful and renowned German research institutions. He has been Director of the Fraunhofer IIS since 1993, and under his leadership it has grown to become the largest of Germany's 60 Fraunhofer Institutes, a position it retains to this day, currently employing over 730 staff. Likely his most important scientific as well as application-related contribution was his pivotal role in the development of the mp3 format, which would later become a worldwide success. The contributions to this Festschrift were written by both Fraunhofer IIS staff and external project team members in appreciation of Prof. Dr. Gerhäuser's lifetime academic achievements and his inspiring leadership at the Fraunhofer IIS. The papers reflect the broad spectrum of the institute's research activities and are grouped into sections on circuits, information systems, visual computing, and audio and multimedia. They provide academic and industrial researchers in fields like signal processing, sensor networks, microelectronics, and integrated circuits with an up-to-date overview of research results that have a huge potential for cutting-edge industrial applications.

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