

Efficient 8x8 Discrete Cosine Transform

Kevin Jeffay, Hong Jiang Zhang

Error-Free Implementation of the Discrete Cosine Transform Khan Wahid,2010-11 The advancement in multimedia-based applications has sustained the need for more efficient ways to encode signals and images. Over the years, the Discrete Cosine Transform (DCT) has emerged as the most popular transform for many image and video coding applications. While implementing the DCT using finite-precision, approximation of the real-valued transform coefficients is required. It not only results in computational error, but also limits the quality of the reconstruction. An error-free encoding using algebraic integers has been shown to be an effective way of resolving the issue. The mapping technique - referred as Algebraic Integer Quantization (AIQ) - encodes the irrational basis functions using algebraic integers. This research presents novel AIQ-based architectures for the fast implementation of several 8x8 2-D scaled DCT and Inverse scaled DCTs. A new Integer-like 8x8 2-D DCT for H.264 encoder is also presented. Apart from the multiplication-free nature, the presented scheme eliminates any quantization errors during the transform stage resulting in area-efficient and high-speed designs suitable for real-time image or video compression and HDTV applications.

Integrated Circuit and System Design Enrico Macii,Vassilis Paliouras,Odyseas Koufopavlou,2004-08-24 Welcometothe proceedings of PATMOS 2004, the fourteenth in a series of international workshops. PATMOS 2004 was organized by the University of Patras with technical co-sponsorship from the IEEE Circuits and Systems Society. Over the years, the PATMOS meeting has evolved into an important European event, where industry and academia meet to discuss power and timing aspects in modern integrated circuit and system design. PATMOS provides a forum for researchers to discuss and investigate the emerging challenges in design methodologies and tools required to develop the upcoming generations of integrated circuits and systems. We realized this vision this year by providing a technical program that contained state-of-the-art technical contributions, a keynote speech, three invited talks and two embedded tutorials. The technical program focused on timing, performance and power consumption, as well as architectural aspects, with particular emphasis on modelling, design, characterization, analysis and optimization in the nanometer era. This year a record 152 contributions were received to be considered for possible presentation at PATMOS. Despite the choice for an intense three-day meeting, only 51 lecture papers and 34 poster papers could be accommodated in the single-track technical program. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 85 papers to be presented at PATMOS and organized them into 13 technical sessions. As was the case with the PATMOS workshops, the review process was anonymous, full papers were required, and several reviews were received per manuscript.

Discrete Cosine Transform, Second Edition Humberto Ochoa-Dominguez,K. R. Rao,2019-04-18 Many new DCT-like transforms have been proposed since the first edition of this book. For example, the integer DCT that yields integer transform coefficients, the directional DCT to take advantage of several directions of the image and the steerable DCT. The advent of higher dimensional frames such as UHD TV and 4K-TV demand for small and large transform blocks to encode small or large similar areas respectively in an efficient way. Therefore, a new updated book on DCT, adapted to the modern days, considering the new advances in this area and targeted for students, researchers and the industry is a necessity.

Ultra-Low-Voltage Design of Energy-Efficient Digital Circuits Nele Reynders,Wim Dehaene,2015-04-14 This book focuses on increasing the energy-efficiency of electronic devices so that portable applications can have a longer stand-alone time on the same battery. The authors explain the energy-efficiency

benefits that ultra-low-voltage circuits provide and provide answers to tackle the challenges which ultra-low-voltage operation poses. An innovative design methodology is presented, verified, and validated by four prototypes in advanced CMOS technologies. These prototypes are shown to achieve high energy-efficiency through their successful functionality at ultra-low supply voltages.

Multidimensional Discrete Unitary Transforms Artyom M. Grigoryan, Sos S. Aghaian, 2003-07-31 This reference presents a more efficient, flexible, and manageable approach to unitary transform calculation and examines novel concepts in the design, classification, and management of fast algorithms for different transforms in one-, two-, and multidimensional cases. Illustrating methods to construct new unitary transforms for best algorithm selection and development in real-world applications, the book contains a wide range of examples to compare the efficacy of different algorithms in a variety of one-, two-, and three-dimensional cases. Multidimensional Discrete Unitary Transforms builds progressively from simple representative cases to higher levels of generalization.

Readings in Multimedia Computing and Networking Kevin Jeffay, Hong Jiang Zhang, 2001-08-10 Readings in Multimedia Computing and Networking captures the broad areas of research and developments in this burgeoning field, distills the key findings, and makes them accessible to professionals, researchers, and students alike. For the first time, the most influential and innovative papers on these topics are presented in a cohesive form, giving shape to the diverse area of multimedia computing. The seminal moments are recorded by a dozen visionaries in the field and each contributing editor provides a context for their area of research by way of a thoughtful, focused chapter introduction. The volume editors, Kevin Jeffay and Hong Jiang Zhang, offer further incisive interpretations of past and present developments in this area, including those within media and content processing, operating systems, and networking support for multimedia. This book will provide you with a sound understanding of the theoretical and practical issues at work in the field's continuing evolution. * Offers an in-depth look at the technical challenges in multimedia and provides real and potential solutions that promise to expand the role of multimedia in business, entertainment, and education. * Examines in Part One issues at the heart of multimedia processes: the means by which multimedia data are coded, compressed, indexed, retrieved, and otherwise manipulated. * Examines in Part Two the accommodation of these processes by storage systems, operating systems, network protocols, and applications. * Written by leading researchers, the introductions give shape to a field that is continually defining itself and place the key research findings in context to those who need to understand the state-of-the-art developments.

Effective Video Coding for Multimedia Applications Sudhakar Radhakrishnan, 2011-04-26 Information has become one of the most valuable assets in the modern era. Within the last 5-10 years, the demand for multimedia applications has increased enormously. Like many other recent developments, the materialization of image and video encoding is due to the contribution from major areas like good network access, good amount of fast processors e.t.c. Many standardization procedures were carried out for the development of image and video coding. The advancement of computer storage technology continues at a rapid pace as a means of reducing storage requirements of an image and video as most situation warrants. Thus, the science of digital video compression/coding has emerged. This storage capacity seems to be more impressive when it is realized that the intent is to deliver very high quality video to the end user with as few visible artifacts as possible. Current methods of video compression such as Moving Pictures Experts Group (MPEG) standard provide good performance in terms of retaining video quality while reducing the storage requirements. Many books are available for video coding fundamentals. This book is the research outcome of various Researchers and Professors who have contributed a might in this field. This book suits researchers doing their research in the area of video

coding. The understanding of fundamentals of video coding is essential for the reader before reading this book. The book revolves around three different challenges namely (i) Coding strategies (coding efficiency and computational complexity), (ii) Video compression and (iii) Error resilience. The complete efficient video system depends upon source coding, proper inter and intra frame coding, emerging newer transform, quantization techniques and proper error concealment. The book gives the solution of all the challenges and is available in different sections.

Circuits and Systems Tutorials Chris Toumazou, Nick Battersby, Sonia Porta, 1995-12-11 Available for the first time in paperback, this ground-breaking industry textbook is heralded as a first in its state-of-the-art coverage of the most important areas emerging in circuits and systems. It is compiled from course material used in a suite of one-day tutorials on circuits and systems designed expressly for engineers and research scientists who want to explore subjects outside, but related to, their immediate fields. Authored by 50 circuits and systems experts, this volume fosters a fundamental and authoritative understanding of each subject.

Parallel Processing and Applied Mathematics Roman Wyrzykowski, Ewa Deelman, Jack Dongarra, Konrad Karczewski, 2020-03-19 The two-volume set LNCS 12043 and 12044 constitutes revised selected papers from the 13th International Conference on Parallel Processing and Applied Mathematics, PPAM 2019, held in Bialystok, Poland, in September 2019. The 91 regular papers presented in these volumes were selected from 161 submissions. For regular tracks of the conference, 41 papers were selected from 89 submissions. The papers were organized in topical sections named as follows: Part I: numerical algorithms and parallel scientific computing; emerging HPC architectures; performance analysis and scheduling in HPC systems; environments and frameworks for parallel/distributed/cloud computing; applications of parallel computing; parallel non-numerical algorithms; soft computing with applications; special session on GPU computing; special session on parallel matrix factorizations. Part II: workshop on language-based parallel programming models (WLPP 2019); workshop on models algorithms and methodologies for hybrid parallelism in new HPC systems; workshop on power and energy aspects of computations (PEAC 2019); special session on tools for energy efficient computing; workshop on scheduling for parallel computing (SPC 2019); workshop on applied high performance numerical algorithms for PDEs; minisymposium on HPC applications in physical sciences; minisymposium on high performance computing interval methods; workshop on complex collective systems. Chapters Parallel Adaptive Cross Approximation for the Multi-trace Formulation of Scattering Problems and A High-Order Discontinuous Galerkin Solver with Dynamic Adaptive Mesh Refinement to Simulate Cloud Formation Processes are available open access under a Creative Commons Attribution 4.0 International License via link.springer.com.

Design and Evaluation of a Data-dependent Low-power 8x8 DCT/IDCT Cheng-Yu Pai, 2000 Traditional fast Discrete Cosine Transform (DCT)/Inverse DCT (IDCT) algorithms have focused on reducing arithmetic complexity and have fixed run-time complexities regardless of the input. Recently, data-dependent signal processing has been applied to the DCT/IDCT. These algorithms have variable run-time complexities. A new two-dimensional 8 x 8 low-power DCT/IDCT design is implemented using VHDL by applying the data-dependent signal-processing concept onto the traditional fixed-complexity fast DCT/IDCT algorithm. To reduce power, the design is based on Loeffler's fast algorithm, which uses a low number of multiplications. On top of that, zero bypassing, data segmentation, input truncation, and hardwired canonical sign-digit (CSD) multipliers are used to reduce the run-time computation, hence reduce the switching activities and the power. When synthesized using Canadian Microelectronic Corporation 3-V 0.35 μ m CMOS technology, this FDCT/IDCT design consumes

122.7/124.9 mW with clock frequency of 40MHz and processing rate of 320M sample/sec. With technology scaling to 0.35 μ m technology, the proposed design features lower switching capacitance per sample, i.e. more power-efficient, than other previously reported high-performance FDCT/IDCT designs.* *This work is supported by National Sciences and Engineering Research Council of Canada (NSERC) post-graduate scholarship, and NSERC research grants.

Space and Earth Science Data Compression Workshop, 1993

Wireless Communications Andreas F. Molisch, 2012-02-06 Professor Andreas F. Molisch, renowned researcher and educator, has put together the comprehensive book, *Wireless Communications*. The second edition, which includes a wealth of new material on important topics, ensures the role of the text as the key resource for every student, researcher, and practitioner in the field. —Professor Moe Win, MIT, USA Wireless communications has grown rapidly over the past decade from a niche market into one of the most important, fast moving industries. Fully updated to incorporate the latest research and developments, *Wireless Communications, Second Edition* provides an authoritative overview of the principles and applications of mobile communication technology. The author provides an in-depth analysis of current treatment of the area, addressing both the traditional elements, such as Rayleigh fading, BER in flat fading channels, and equalisation, and more recently emerging topics such as multi-user detection in CDMA systems, MIMO systems, and cognitive radio. The dominant wireless standards; including cellular, cordless and wireless LANs; are discussed. Topics featured include: wireless propagation channels, transceivers and signal processing, multiple access and advanced transceiver schemes, and standardised wireless systems. Combines mathematical descriptions with intuitive explanations of the physical facts, enabling readers to acquire a deep understanding of the subject. Includes new chapters on cognitive radio, cooperative communications and relaying, video coding, 3GPP Long Term Evolution, and WiMax; plus significant new sections on multi-user MIMO, 802.11n, and information theory. Companion website featuring: supplementary material on 'DECT', solutions manual and presentation slides for instructors, appendices, list of abbreviations and other useful resources.

Discrete Cosine and Sine Transforms Vladimir Britanak, Patrick C. Yip, K. R. Rao, 2010-07-28 The Discrete Cosine Transform (DCT) is used in many applications by the scientific, engineering and research communities and in data compression in particular. Fast algorithms and applications of the DCT Type II (DCT-II) have become the heart of many established international image/video coding standards. Since then other forms of the DCT and Discrete Sine Transform (DST) have been investigated in detail. This new edition presents the complete set of DCT and DST discrete trigonometric transforms, including their definitions, general mathematical properties, and relations to the optimal Karhunen-Loève transform (KLT), with the emphasis on fast algorithms (one-dimensional and two-dimensional) and integer approximations of DCTs and DSTs for their efficient implementations in the integer domain. DCTs and DSTs are real-valued transforms that map integer-valued signals to floating-point coefficients. To eliminate the floating-point operations, various methods of integer approximations have been proposed to construct and flexibly generate a family of integer DCT and DST transforms with arbitrary accuracy and performance. The integer DCTs/DSTs with low-cost and low-powered implementation can replace the corresponding real-valued transforms in wireless and satellite communication systems as well as portable computing applications. The book is essentially a detailed excursion on orthogonal/orthonormal DCT and DST matrices, their matrix factorizations and integer approximations. It is hoped that the book will serve as a valuable reference for industry, academia and research institutes in developing integer DCTs and DSTs as well as an inspiration source for further advanced research. Presentation of the complete set of DCTs and

DSTs in context of entire class of discrete unitary sinusoidal transforms: the origin, definitions, general mathematical properties, mutual relationships and relations to the optimal Karhunen-Loève transform (KLT) Unified treatment with the fast implementations of DCTs and DSTs: the fast rotation-based algorithms derived in the form of recursive sparse matrix factorizations of a transform matrix including one- and two-dimensional cases Detailed presentation of various methods and design approaches to integer approximation of DCTs and DSTs utilizing the basic concepts of linear algebra, matrix theory and matrix computations leading to their efficient multiplierless real-time implementations, or in general reversible integer-to-integer implementations Comprehensive list of additional references reflecting recent/latest developments in the efficient implementations of DCTs and DSTs mainly one-, two-, three- and multi-dimensional fast DCT/DST algorithms including the recent active research topics for the time period from 1990 up to now

Pattern Recognition Jose Francisco Martinez-Trinidad, Jesús Ariel Carrasco-Ochoa, Jose Arturo Olvera-López, Joaquín Salas-Rodríguez, Ching Y. Suen, 2014-06-20 This book constitutes the refereed proceedings of the 6th Mexican Conference on Pattern Recognition, MCPR 2014, held in Cancun, Mexico, in June 2014. The 39 revised full papers presented were carefully reviewed and selected from 68 submissions and are organized in topical sections on pattern recognition and artificial intelligence; computer vision; image processing and analysis; animal biometric recognition and applications of pattern recognition.

Recent Advances in Computer Science and Information Engineering Zhihong Qian, Lei Cao, Weilian Su, Tingkai Wang, Huamin Yang, 2012-01-25 CSIE 2011 is an international scientific Congress for distinguished scholars engaged in scientific, engineering and technological research, dedicated to build a platform for exploring and discussing the future of Computer Science and Information Engineering with existing and potential application scenarios. The congress has been held twice, in Los Angeles, USA for the first and in Changchun, China for the second time, each of which attracted a large number of researchers from all over the world. The congress turns out to develop a spirit of cooperation that leads to new friendship for addressing a wide variety of ongoing problems in this vibrant area of technology and fostering more collaboration over the world. The congress, CSIE 2011, received 2483 full paper and abstract submissions from 27 countries and regions over the world. Through a rigorous peer review process, all submissions were refereed based on their quality of content, level of innovation, significance, originality and legibility. 688 papers have been accepted for the international congress proceedings ultimately.

System-on-Chip for Real-Time Applications Wael Badawy, Graham A. Julien, 2012-12-06 System-on-Chip for Real-Time Applications will be of interest to engineers, both in industry and academia, working in the area of SoC VLSI design and application. It will also be useful to graduate and undergraduate students in electrical and computer engineering and computer science. A selected set of papers from the 2nd International Workshop on Real-Time Applications were used to form the basis of this book. It is organized into the following chapters: -Introduction; -Design Reuse; -Modeling; -Architecture; -Design Techniques; -Memory; -Circuits; -Low Power; -Interconnect and Technology; -MEMS. System-on-Chip for Real-Time Applications contains many signal processing applications and will be of particular interest to those working in that community.

Discrete Cosine Transform K. Ramamohan Rao, P. Yip, 2014-06-28 This is the first comprehensive treatment of the theoretical aspects of the discrete cosine transform (DCT), which is being recommended by various standards organizations, such as the CCITT, ISO etc., as the primary compression tool in digital image coding. The main purpose of the book is to provide a complete source for the user of this signal processing tool, where both the basics and the applications are detailed. An extensive bibliography covers both the theory and applications of the DCT. The novice will find the book useful in its self-contained treatment of

the theory of the DCT, the detailed description of various algorithms supported by computer programs and the range of possible applications, including codecs used for teleconferencing, videophone, progressive image transmission, and broadcast TV. The more advanced user will appreciate the extensive references. Tables describing ASIC VLSI chips for implementing DCT, and motion estimation and details on image compression boards are also provided.

MPEG Video Compression Standard Joan L. Mitchell, 1997 This book gives the professional and student one place to look for answers and guidance on MPEG. It incorporates low-level as well as sophisticated reviews of the major technical elements, and includes sufficient tutorial material that even naive users should be able to understand the answers to their MPEG questions. It contains many pictorial examples so that the benefits and limits of MPEG can be estimated from the data in the book. The full functionality of the entire MPEG standard is explained. Guidance is given about the purpose behind some of the choices and implementers. Particular emphasis is placed on the image compression of the MPEG standard.

Integrated Circuit and System Design, 2004

Advances in Face Image Analysis: Techniques and Technologies Zhang, Yu-Jin, 2010-07-31 More than 30 leading experts from around the world provide comprehensive coverage of various branches of face image analysis, making this text a valuable asset for students, researchers, and practitioners engaged in the study, research, and development of face image analysis techniques.

Immerse yourself in the artistry of words with its expressive creation, Discover the Artistry of **Efficient 8x8 Discrete Cosine Transform**. This ebook, presented in a PDF format (PDF Size: *), is a masterpiece that goes beyond conventional storytelling. Indulge your senses in prose, poetry, and knowledge. Download now to let the beauty of literature and artistry envelop your mind in a unique and expressive way.

Table of Contents Efficient 8x8 Discrete Cosine Transform

1. Understanding the eBook Efficient 8x8 Discrete Cosine Transform
 - The Rise of Digital Reading Efficient 8x8 Discrete Cosine Transform
 - Advantages of eBooks Over Traditional Books
2. Identifying Efficient 8x8 Discrete Cosine Transform
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals

3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Efficient 8x8 Discrete Cosine Transform
 - User-Friendly Interface
4. Exploring eBook Recommendations from Efficient 8x8 Discrete Cosine Transform
 - Personalized Recommendations
 - Efficient 8x8 Discrete Cosine Transform User Reviews and Ratings

- Efficient 8x8 Discrete Cosine Transform and Bestseller Lists
- 5. Accessing Efficient 8x8 Discrete Cosine Transform Free and Paid eBooks
 - Efficient 8x8 Discrete Cosine Transform Public Domain eBooks
 - Efficient 8x8 Discrete Cosine Transform eBook Subscription Services
 - Efficient 8x8 Discrete Cosine Transform Budget-Friendly Options
- 6. Navigating Efficient 8x8 Discrete Cosine Transform eBook Formats
 - ePub, PDF, MOBI, and More
 - Efficient 8x8 Discrete Cosine Transform Compatibility with Devices
 - Efficient 8x8 Discrete Cosine Transform Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Efficient 8x8 Discrete Cosine Transform
 - Highlighting and Note-Taking Efficient 8x8 Discrete Cosine Transform
 - Interactive Elements Efficient 8x8 Discrete Cosine Transform
- 8. Staying Engaged with Efficient 8x8 Discrete Cosine Transform
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Efficient 8x8 Discrete Cosine Transform
- 9. Balancing eBooks and Physical Books Efficient 8x8 Discrete Cosine Transform
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Efficient 8x8 Discrete Cosine Transform

- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Efficient 8x8 Discrete Cosine Transform
 - Setting Reading Goals Efficient 8x8 Discrete Cosine Transform
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Efficient 8x8 Discrete Cosine Transform
 - Fact-Checking eBook Content of Efficient 8x8 Discrete Cosine Transform
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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