

Circuit Graphs 1

Cam Nguyen

Circuit Double Cover of Graphs Cun-Quan Zhang,2012-04-26 Contains all the techniques, methods and results developed so far in a bid to solve the famous CDC conjecture.

Discrete Mathematics Oscar Levin,2018-07-30 Note: This is a custom edition of Levin's full Discrete Mathematics text, arranged specifically for use in a discrete math course for future elementary and middle school teachers. (It is NOT a new and updated edition of the main text.)This gentle introduction to discrete mathematics is written for first and second year math majors, especially those who intend to teach. The text began as a set of lecture notes for the discrete mathematics course at the University of Northern Colorado. This course serves both as an introduction to topics in discrete math and as the introduction to proof course for math majors. The course is usually taught with a large amount of student inquiry, and this text is written to help facilitate this.Four main topics are covered: counting, sequences, logic, and graph theory. Along the way proofs are introduced, including proofs by contradiction, proofs by induction, and combinatorial proofs.While there are many fine discrete math textbooks available, this text has the following advantages: - It is written to be used in an inquiry rich course.- It is written to be used in a course for future math teachers.- It is open source, with low cost print editions and free electronic editions.

Computational Ecology: Graphs, Networks And Agent-based Modeling Wenjun Zhang,2012-05-04 Graphs, networks and agent-based modeling are the most thriving and attracting sciences used in ecology and environmental sciences. As such, this book is the first comprehensive treatment of the subject in the areas of ecology and environmental sciences.From this integrated and self-contained book, researchers, university teachers and students will be provided with an in-depth and complete insight on knowledge, methodology and recent advances of graphs, networks and agent-based-modeling in ecology and environmental sciences.Java codes and a standalone software package will be presented in the book for easy use for those not familiar with mathematical details.

Electronic Circuit Analysis: Rao,2011 Electronic Circuit Analysis is designed to serve students of a two semester undergraduate course on electronic circuit analysis. It builds on the subject from its basic principles over fifteen chapters, providing detailed coverage on the design and analysis of electronic circuits.

Circuit Analysis for Power Engineering Handbook Arie L. Shenkman,Moses Zarudi,2012-12-06 The study of circuits is the foundation on which most other courses in the electrical engineering curriculum are based. For this reason the first course in circuit analysis must be appropriate to the succeeding specializations, which may be classified into two groups. One is a specialization in electronics, microelectronics, communications, computers etc. , or so-called low current, low-voltage engineering. The other is in power electronics, power systems, energy conversion devices etc. , or so-called high-current, high voltage engineering. It is evident that although there are many common teaching topics in the basic course of circuit analysis, there are also certain differences. Unfortunately most of the textbooks in this field are written from the 'electronic engineer's viewpoint', i. e. with the emphasis on low current systems. This brought the author to the conclusion that there is a definite disadvantage in not having a more appropriate book for the specializations in high-current, high-voltage engineering. Thus the idea for this book came into being. The major feature distinguishing this book from others on circuit analysis is in delivering the material with a very strong connection to the specializations in the field of power systems, i. e. in high-current and high voltage engineering. The author believes that this emphasis gives the reader more opportunity for a better understanding and practice of the material which is relevant for power system network analysis, and to prepare students for their further specializations.

Handbook of Graph Theory Jonathan L. Gross, Jay Yellen, 2003-12-29 The Handbook of Graph Theory is the most comprehensive single-source guide to graph theory ever published. Best-selling authors Jonathan Gross and Jay Yellen assembled an outstanding team of experts to contribute overviews of more than 50 of the most significant topics in graph theory-including those related to algorithmic and optimization approach

Circuit Analysis and Feedback Amplifier Theory Wai-Kai Chen, 2018-10-03 Culled from the pages of CRC's highly successful, best-selling The Circuits and Filters Handbook, Second Edition, Circuit Analysis and Feedback Amplifier Theory presents a sharply focused, comprehensive review of the fundamental theory behind professional applications of circuits and feedback amplifiers. It supplies a concise, convenient reference to the key concepts, models, and equations necessary to analyze, design, and predict the behavior of large-scale circuits and feedback amplifiers, illustrated by frequent examples. Edited by a distinguished authority, this book emphasizes the theoretical concepts underlying the processes, behavior, and operation of these devices. It includes guidance on the design of multiple-loop feedback amplifiers. More than 350 figures and tables illustrate the concepts, and where necessary, the theories, principles, and mathematics of some subjects are reviewed. Expert contributors discuss analysis in the time and frequency domains, symbolic analysis, state-variable techniques, feedback amplifier configurations, general feedback theory, and network functions and feedback, among many other topics. Circuit Analysis and Feedback Amplifier Theory builds a strong theoretical foundation for the design and analysis of advanced circuits and feedback amplifiers while serving as a handy reference for experienced engineers, making it a must-have for both beginners and seasoned experts.

Electronic Circuit Analysis B. Visvesvara Rao, 2012

Model Reduction for Circuit Simulation Peter Benner, Michael Hinze, E. Jan W. ter Maten, 2011-03-25 Simulation based on mathematical models plays a major role in computer aided design of integrated circuits (ICs). Decreasing structure sizes, increasing packing densities and driving frequencies require the use of refined mathematical models, and to take into account secondary, parasitic effects. This leads to very high dimensional problems which nowadays require simulation times too large for the short time-to-market demands in industry. Modern Model Order Reduction (MOR) techniques present a way out of this dilemma in providing surrogate models which keep the main characteristics of the device while requiring a significantly lower simulation time than the full model. With Model Reduction for Circuit Simulation we survey the state of the art in the challenging research field of MOR for ICs, and also address its future research directions. Special emphasis is taken on aspects stemming from miniturisations to the nano scale. Contributions cover complexity reduction using e.g., balanced truncation, Krylov-techniques or POD approaches. For semiconductor applications a focus is on generalising current techniques to differential-algebraic equations, on including design parameters, on preserving stability, and on including nonlinearity by means of piecewise linearisations along solution trajectories (TPWL) and interpolation techniques for nonlinear parts. Furthermore the influence of interconnects and power grids on the physical properties of the device is considered, and also top-down system design approaches in which detailed block descriptions are combined with behavioral models. Further topics consider MOR and the combination of approaches from optimisation and statistics, and the inclusion of PDE models with emphasis on MOR for the resulting partial differential algebraic systems. The methods which currently are being developed have also relevance in other application areas such as mechanical multibody systems, and systems arising in chemistry and to biology. The current number of books in the area of MOR for ICs is very limited, so that this volume helps to fill a gap in providing the state of the art material, and to stimulate further research in this area of MOR. Model Reduction for Circuit Simulation also reflects

and documents the vivid interaction between three active research projects in this area, namely the EU-Marie Curie Action ToK project O-MOORE-NICE (members in Belgium, The Netherlands and Germany), the EU-Marie Curie Action RTN-project COMSON (members in The Netherlands, Italy, Germany, and Romania), and the German federal project System reduction in nano-electronics (SyreNe).

Fundamentals of Electric Circuit Theory D Chattopadhyay | PC Rakshit, 2000-11 This book presents the subject matter in a clear and concise manner with numerous diagrams and examples

Circuit Design: Know It All Darren Ashby, Bonnie Baker, Ian Hickman, Walt Kester, Robert Pease, Tim Williams, Bob Zeidman, 2011-04-19 The Newnes Know It All Series takes the best of what our authors have written to create hard-working desk references that will be an engineer's first port of call for key information, design techniques and rules of thumb. Guaranteed not to gather dust on a shelf! Electronics Engineers need to master a wide area of topics to excel. The Circuit Design Know It All covers every angle including semiconductors, IC Design and Fabrication, Computer-Aided Design, as well as Programmable Logic Design. • A 360-degree view from our best-selling authors • Topics include fundamentals, Analog, Linear, and Digital circuits • The ultimate hard-working desk reference; all the essential information, techniques and tricks of the trade in one volume

Asynchronous Circuit Design for VLSI Signal Processing Teresa H. Meng, Sharad Malik, 2011-06-27 Asynchronous Circuit Design for VLSI Signal Processing is a collection of research papers on recent advances in the area of specification, design and analysis of asynchronous circuits and systems. This interest in designing digital computing systems without a global clock is prompted by the ever growing difficulty in adopting global synchronization as the only efficient means to system timing. Asynchronous circuits and systems have long held interest for circuit designers and researchers alike because of the inherent challenge involved in designing these circuits, as well as developing design techniques for them. The frontier research in this area can be traced back to Huffman's publications 'The Synthesis of Sequential Switching Circuits' in 1954 followed by Unger's book, 'Asynchronous Sequential Switching Circuits' in 1969 where a theoretical foundation for handling logic hazards was established. In the last few years a growing number of researchers have joined force in unveiling the mystery of designing correct asynchronous circuits, and better yet, have produced several alternatives in automatic synthesis and verification of such circuits. This collection of research papers represents a balanced view of current research efforts in the design, synthesis and verification of asynchronous systems.

Intl Std Ed-a Mathematical View of Our World Harold R. Parks, 2007

The Hamiltonian Circuit Algorithm Ashay Dharwadkar, 2004-08-08 We present a new polynomial-time algorithm for finding Hamiltonian circuits in graphs. It is shown that the algorithm always finds a Hamiltonian circuit in graphs that have at least three vertices and minimum degree at least half the total number of vertices. In the process, we also obtain a constructive proof of Dirac's famous theorem of 1952, for the first time. The algorithm finds a Hamiltonian circuit (respectively, tour) in all known examples of graphs that have a Hamiltonian circuit (respectively, tour). In view of the importance of the P versus NP question, we ask: does there exist a graph that has a Hamiltonian circuit (respectively, tour) but for which this algorithm cannot find a Hamiltonian circuit (respectively, tour)? The algorithm is implemented in C++ and the program is demonstrated with several examples.

Topological Principles in Cartography James P. Corbett, 1979

Radio-Frequency Integrated-Circuit Engineering Cam Nguyen, 2015-03-16 Die Technologie komplementärer Metalloxid-Halbleiter (Complementary Metal-Oxide Semiconductor, CMOS) kommt bei der Fertigung integrierter Schaltkreise zum Einsatz. In diesem Fachbuch werden Theorie, Analyse, Eigenschaften (Hochfrequenz/Hochgeschwindigkeit) und Anwendungen von Leiterplatten-Übertragungsleitungen, die in integrierten Schaltkreisen und Systemen verwendet werden, ausführlich behandelt. Weitere Themen sind Anwendungen in allen Bereichen der Hochfrequenztechnik, einschließlich drahtlose Kommunikation, Optik und Computer. Das Fachbuch ist durch das Lösungshandbuch ideal für Studenten im höheren Grundstudium, Ingenieure für Hochfrequenz-Mikrowellentechnik, Optikingenieure, Ingenieure für Festkörperbauelemente und für Computeringenieure.

Resources for Teaching Discrete Mathematics Brian Hopkins, 2009 Hopkins collects the work of 35 instructors who share their innovations and insights about teaching discrete mathematics at the high school and college level. The book's 9 classroom-tested projects, including building a geodesic dome, come with student handouts, solutions, and notes for the instructor. The 11 history modules presented draw on original sources, such as Pascal's Treatise on the Arithmetical Triangle, allowing students to explore topics in their original contexts. Three articles address extensions of standard discrete mathematics content. Two other articles explore pedagogy specifically related to discrete mathematics courses: adapting a group discovery method to larger classes, and using logic in encouraging students to construct proofs.

Graph Theory Applications L.R. Foulds, 1995-01-20 The first part of this text covers the main graph theoretic topics: connectivity, trees, traversability, planarity, colouring, covering, matching, digraphs, networks, matrices of a graph, graph theoretic algorithms, and matroids. These concepts are then applied in the second part to problems in engineering, operations research, and science as well as to an interesting set of miscellaneous problems, thus illustrating their broad applicability. Every effort has been made to present applications that use not merely the notation and terminology of graph theory, but also its actual mathematical results. Some of the applications, such as in molecular evolution, facilities layout, and traffic network design, have never appeared before in book form. Written at an advanced undergraduate to beginning graduate level, this book is suitable for students of mathematics, engineering, operations research, computer science, and physical sciences as well as for researchers and practitioners with an interest in graph theoretic modelling.

Surveys in Combinatorics, 1993 Keith Walker, 1993-07-29 This volume contains the invited papers presented at the 14th British Combinatorial Conference, held at the University of Keele in July 1993.

Applied Graph Theory Wai-Kai Chen, 2014-11-28 Applied Graph Theory: Graphs and Electrical Networks, Second Revised Edition provides a concise discussion of the fundamentals of graph and its application to the electrical network theory. The book emphasizes the mathematical precision of the concepts and principles involved. The text first covers the basic theory of graph, and then proceeds to tackling in the next three chapters the various applications of graph to electrical network theory. These chapters also discuss the foundations of electrical network theory; directed-graph solutions of linear algebraic equations; and topological analysis of linear systems. Next, the book covers trees and their generation. Chapter 6 deals with the realizability of directed graphs with prescribed degrees, while Chapter 7 talks about state equations of networks. The book will be of great use to researchers of network topology, linear systems, and circuitries.

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