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Christian G. Meyer

Dedicated Digital Processors F. Mayer-Lindenberg, 2004-04-02 The recent evolution of digital technology has resulted in the design of digital processors with increasingly complex capabilities. The implementation of hardware/software co-design methodologies provides new opportunities for the development of low power, high speed DSPs and processor networks. Dedicated digital processors are digital processors with an application specific computational task. Dedicated Digital Processors presents an integrated and accessible approach to digital processor design principles, processes, and implementations based upon the author's considerable experience in teaching digital systems design and digital signal processing. Emphasis is placed on presentation of hardware/software co-design methods, with examples and illustrations provided throughout the text. System-on-a-chip and embedded systems are described and examples of high speed real-time processing are given. Coverage of standard and emerging DSP architectures enable the reader to make an informed selection when undertaking their own designs. Presents readers with the elementary building blocks for the design of digital hardware systems and processor networks Provides a unique evaluation of standard DSP architectures whilst providing up-to-date information on the latest architectures, including the TI 55x and TigerSharc chip families and the Virtex FPGA (field-programmable gate array) Introduces the concepts and methodologies for describing and designing hardware VHDL is presented and used to illustrate the design of a simple processor A practical overview of hardware/software codesign with design techniques and considerations illustrated with examples of real-world designs Fundamental reading for graduate and senior undergraduate students of computer and electronic engineering, and Practicing engineers developing DSP applications.

EISA System Architecture Tom Shanley, Don Anderson, MindShare, Inc, 1995 A building-block approach to a bedrock PC standard.

EDN, 1994

Official Gazette of the United States Patent and Trademark Office, 2000

Parallel Processing Techniques for Simulation Madan Singh, A.Y. Allidina, B.K. Daniels, 2013-11-21 This volume provides the proceedings of the First European Workshop on Parallel Processing Techniques for Simulation which was held at the end of October 1985. The Workshop was organized within the framework of a joint project sponsored by the Commission of the European Communities under the research part of the multiannual programme in the field of Data Processing aiming at promoting collaborative research work in the Community. The project involved collaborative work between the Complex Systems Group of the Control Systems Centre at UMIST, the Systems Reliability Service of the United Kingdom Atomic Energy Authority and the University of Bergamo, Italy. The aim of this project was to develop decomposition coordination techniques which would be of help in the simulation of complex dynamical systems on parallel processing facilities. One of the major aims of the Workshop was to report on the results produced within the project and to try to relate these to the leading work going on in this field in other centres of excellence. With this in mind, the Proceedings Volume is split up into a number of parts corresponding to the main sessions within the Workshop programme.

Enhanced IP Services for Cisco Networks Donald C. Lee, 1999 Learn how to manage and deploy the latest IP services in Cisco-centric networks. Understand VPN security concepts: confidentiality, integrity, origin authentication, non-repudiation, anti-replay, perfect forward secrecy Deploy quality of service technologies to protect your mission-critical applications Find out how IPsec technology works and how to configure it in IOS Learn how to set up a router as a firewall and intrusion detection system Gain efficient use of your IP address space with NAT, VLSM, IP unnumbered Solve real-world routing problems with redistribution, route filtering, summarization, policy routing Enable authentication, authorization, and accounting (AAA) security services with RADIUS and TACACS+ servers Enhanced IP Services for Cisco Networks is a guide to the new enabling and advanced IOS services that build more scalable, intelligent, and secure networks. You will learn the technical details necessary to deploy quality of service and VPN technologies, as well as improved security and advanced routing features. These services will allow you to securely extend the network to new frontiers, protect your network from attacks, and enhance network transport with application-level prioritization. This book offers a practical guide to implementing IPsec, the IOS Firewall, and IOS Intrusion Detection System. Also included are advanced routing principles and quality of service features that focus on improving the capability of your network. A good briefing on cryptography fully explains the science that makes VPNs possible. Rather than being another routing book, this is a guide to improving your network's capabilities by understanding and using the sophisticated features available to you in Cisco's IOS software

An Open-Source Research Platform for Heterogeneous Systems on Chip Andreas Dominic Kurth, 2022-10-05 Heterogeneous systems on chip (HeSoCs) combine general-purpose, feature-rich multi-core host processors with domain-specific programmable many-core accelerators (PMCA)s to unite versatility with energy efficiency and peak performance. By virtue of their heterogeneity, HeSoCs hold the promise of increasing performance and energy efficiency compared to homogeneous multiprocessors, because applications can be executed on hardware that is designed for them. However, this heterogeneity also increases system complexity substantially. This thesis presents the first research platform for HeSoCs where all components, from accelerator cores to application programming interface, are available under permissive open-source licenses. We begin by identifying the hardware and software components that are required in HeSoCs and by designing a representative hardware and software architecture. We then design, implement, and evaluate four critical HeSoC components that have not been discussed in research at the level required for an open-source implementation: First, we present a modular, topology-agnostic, high-performance on-chip communication platform, which adheres to a state-of-the-art industry-standard protocol. We show that the platform can be used to build high-bandwidth (e.g., 2.5 GHz and 1024 bit data width) end-to-end communication fabrics with high degrees of concurrency (e.g., up to 256 independent concurrent transactions). Second, we present a modular and efficient solution for implementing atomic memory operations in highly-scalable many-core processors, which demonstrates near-optimal linear throughput scaling for various synthetic and real-world workloads and requires only 0.5 kGE per core. Third, we present a hardware-software solution for shared virtual memory that avoids the majority of translation lookaside buffer misses with prefetching, supports parallel burst transfers without additional buffers, and can be scaled with the workload and number of parallel processors. Our work improves accelerator performance for memory-intensive kernels by up to 4x. Fourth, we present a software toolchain for mixed-data-model heterogeneous compilation and OpenMP offloading. Our work enables transparent memory sharing between a 64-bit host processor and a 32-bit accelerator at overheads below 0.7 % compared to 32-bit-only execution. Finally, we combine our contributions to a research platform for state-of-the-art HeSoCs and demonstrate its performance and flexibility.

LPI Linux Certification in a Nutshell Jeffrey Dean, 2001 The Linux Professional Institute (LPI) is the leader in obtaining the independent, vendor-neutral certification that provides proof of the necessary skills in demand by IT departments. LPI Linux Certification in a Nutshell is written with the LPI exams in mind by including information on the required Topics and Objectives. Beyond preparing to pass the LPIC Level 1 exams, this book provides an excellent understanding of Linux concepts and functions. LPI Linux Certification in a Nutshell prepares system administrators for both of the General Linux LPIC Level 1 exams (101 and 102). The book is divided into two parts (one for each of the LPIC Level 1 exams), and each part features a summary of the exam, a Highlighter's Index, labs, suggested exercises, and practice exams. Part 1 covers Exam 101: GNU and Unix commands; devices, Linux filesystems, and the filesystem hierarchy standard; boot, initialization, shutdown, and run levels; documentation; and administrative tasks. Part 2 covers Exam 102: hardware and architecture; Linux installation and package management; the Linux kernel; text editing, processing, and printing; shells, scripting, programming, and compiling; the X Window System; networking fundamentals; network services; and security. While this book is designed to help system administrators prepare for the LPI certification exams, the tutorial-style approach will help newbies learn more about their Linux system. For those preparing to take the LPI certification exams, this book will prove to be invaluable in its

scope and breadth.

High Performance Switches and Routers H. Jonathan Chao,Bin Liu,2007-04-06 As Internet traffic grows and demands for quality of service become stringent, researchers and engineers can turn to this go-to guide for tested and proven solutions. This text presents the latest developments in high performance switches and routers, coupled with step-by-step design guidance and more than 550 figures and examples to enable readers to grasp all the theories and algorithms used for design and implementation.

RFID Handbook Syed A. Ahson,Mohammad Ilyas,2017-12-19 Radio Frequency Identification (RFID) tagging is now used by the department of defense and many of the world's largest retailers including Wal-Mart. As RFID continues to infiltrate industries worldwide, organizations must harness a clear understanding of this technology in order to maximize its potential and protect against the potential risks it poses. The RFID Handbook provides an overview of RFID technology, its associated security and privacy risks, and recommended practices that will enable organizations to realize productivity improvements while also protecting sensitive information and the privacy of individuals. Expert contributors present a host of applications including RFID enabled automated receiving, triage with RFID for massive incidents, RFID and NFC in relation to mobile phones, and RFID technologies for communication robots and a privacy preserving video surveillance system. The unprecedented coverage also includes detailed descriptions of adaptive splitting protocols as well as tree-based and probabilistic anti-collision protocols. Drawing on its distinguished editors and world-renowned contributors, this one-of-a-kind handbook serves as the ultimate reference on RFID, from basic research concepts to future applications.

Intelligent Robotics and Applications Chun-Yi Su,Subhash Rakheja,Liu Honghai,2012-09-28 The three volume set LNAI 7506, LNAI 7507 and LNAI 7508 constitutes the refereed proceedings of the 5th International Conference on Intelligent Robotics and Applications, ICIRA 2012, held in Montreal, Canada, in October 2012. The 197 revised full papers presented were thoroughly reviewed and selected from 271 submissions. They present the state-of-the-art developments in robotics, automation and mechatronics. This volume covers the topics of robot actuators and sensors; robot design, development and control; robot intelligence, learning and linguistics; robot mechanism and design; robot motion analysis and planning; robotic vision, recognition and reconstruction; and planning and navigation.

Parallel Evolution of Parallel Processors G. Lerman,L. Rudolph,2013-03-07 Study the past, if you would divine the future. - CONFUCIUS A well written, organized, and concise survey is an important tool in any newly emerging field of study. This present text is the first of a new series that has been established to promote the publications of such survey books. A survey serves several needs. Virtually every new research area has its roots in several diverse areas and many of the initial fundamental results are dispersed across a wide range of journals, books, and conferences in many different sub fields. A good survey should bring together these results. But just a collection of articles is not enough. Since terminology and notation take many years to become standardized, it is often difficult to master the early papers. In addition, when a new research field has its foundations outside of computer science, all the papers may be difficult to read. Each field has its own view of elegance and its own method of presenting results. A good survey overcomes such difficulties by presenting results in a notation and terminology that is familiar to most computer scientists. A good survey can give a feel for the whole field. It helps identify trends, both successful and unsuccessful, and it should point new researchers in the right direction.

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CompTIA Network+ Review Guide Jon Buhagiar,2018-04-10 Essential last-minute review aid for the updated CompTIA Network+ Exam N10-007 CompTIA Network+ Review Guide Exam N10-007, 4th Edition, is your ideal study companion for preparing for the CompTIA Network+ exam (N10-007). Organized by exam objectives, this is a focused, concise review guide that works hand-in-hand with any learning tool, including the Sybex CompTIA Network+ Study Guide, CompTIA Network+ Deluxe Study Guide, and CompTIA Network+ Practice Tests. The book is broken into 5 parts, each part corresponding to one of the 5 objective domain areas of the Network+ exam: Network Architecture; Network Operations; Network Security; Troubleshooting; and Industry Standards, Practices, and Network Theory. Readers will also be given access to the comprehensive online Sybex test bank, which includes two bonus practice tests, electronic flashcards, and a glossary of terms that you'll need to know come exam day. CompTIA's Network+ certification covers advances in networking technology, and reflects changes in associated job tasks. The exam places greater emphasis on network implementation and support, and includes expanded coverage of wireless networking topics. This review guide gives you the opportunity to identify your level of knowledge while there's still time to study, and avoid exam-day surprises. Review network architecture and security Understand network operations and troubleshooting Gain insight into industry standards and best practices Get a firmer grasp of network theory fundamentals If you're looking for a beginning, vendor-neutral networking certification, look no further than CompTIA Network+.

The Ocean Engineering Handbook Ferial El-Hawary,2000-12-28 Compiled with the help of an internationally acclaimed panel of experts, the Ocean Engineering Handbook is the most complete reference available for professionals. It offers you comprehensive coverage of important areas of the theory and practice of oceanic/coastal engineering and technology. This well organized text includes five major sections: M

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Bithost 160 Introduction

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