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Software Diagnostics Institute,Dmitry Vostokov

Troubleshooting Cisco Nexus Switches and NX-OS Vinit

Jain, Brad Edgeworth, Richard Furr, 2018-05-22 The definitive deep-dive guide to hardware and software troubleshooting on Cisco Nexus switches The Cisco Nexus platform and NX-OS switch operating system combine to deliver unprecedented speed, capacity, resilience, and flexibility in today's data center networks. *Troubleshooting Cisco Nexus Switches and NX-OS* is your single reference for quickly identifying and solving problems with these business-critical technologies. Three expert authors draw on deep experience with large Cisco customers, emphasizing the most common issues in real-world deployments, including problems that have caused major data center outages. Their authoritative, hands-on guidance addresses both features and architecture, helping you troubleshoot both control plane forwarding and data plane/data path problems and use NX-OS APIs to automate and simplify troubleshooting. Throughout, you'll find real-world configurations, intuitive illustrations, and practical insights into key platform-specific behaviors. This is an indispensable technical resource for all Cisco network consultants, system/support engineers, network operations professionals, and CCNP/CCIE certification candidates working in

the data center domain. · Understand the NX-OS operating system and its powerful troubleshooting tools · Solve problems with cards, hardware drops, fabrics, and CoPP policies · Troubleshoot network packet switching and forwarding · Properly design, implement, and troubleshoot issues related to Virtual Port Channels (VPC and VPC+) · Optimize routing through filtering or path manipulation · Optimize IP/IPv6 services and FHRP protocols (including HSRP, VRRP, and Anycast HSRP) · Troubleshoot EIGRP, OSPF, and IS-IS neighbor relationships and routing paths · Identify and resolve issues with Nexus route maps · Locate problems with BGP neighbor adjacencies and enhance path selection · Troubleshoot high availability components (BFD, SSO, ISSU, and GIR) · Understand multicast protocols and troubleshooting techniques · Identify and solve problems with OTV · Use NX-OS APIs to automate troubleshooting and administrative tasks

The Design and Implementation of the RT-Thread Operating System Qiu Yi,Xiong Puxiang,Tianlong Zhu,2020-11-12 Since the release of V0.01 in 2006, to the present V4.0 version, RT-Thread has developed a reputation among developers for its open source strategy. RT-Thread has gained a large following among members of the embedded open source community in China with hundreds

of thousands of enthusiasts. RT-Thread is widely used in energy, automotive, medical, consumer electronics, among other applications, making it a mature and stable open source embedded operating system. The purpose of RT-Thread RTOS Design and Implementation is to create an easy learning curve for mastering RT-Thread, so that more developers can participate in the development of RT-Thread and work together to create an open source, tiny, and beautiful Internet of Things operating system. The book's first part introduces the RT-Thread kernel and starts with an overview of RT-Thread before covering thread management, clock management, inter-thread synchronization, inter-thread communication, memory management, and interrupt management. The second part begins with RT-Thread kernel porting and explains how to port RT-Thread to a hardware board to run it. The second part also introduces RT-Thread components and discusses the Env development environment, FinSH console, device management, and network framework. Additional topics covered include: The I/O device framework Virtual file systems Peripheral interfaces Devices including the PIN device, UART device, and ADC device, among others. Each chapter features code samples, as well as helpful tables and graphs, so you can practice as you learn as well as

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The Design and Implementation of the RT-Thread Operating System Qiu Yi,Xiong Puxiang,Tianlong Zhu,2020-11-13 Since the release of V0.01 in 2006, to the present V4.0 version, RT-Thread has developed a reputation among developers for its open source strategy. RT-Thread has gained a large following among members of the embedded open source community in China with hundreds of thousands of enthusiasts. RT-Thread is widely used in energy, automotive, medical, consumer electronics, among other applications, making it a mature and stable open source embedded operating system. The purpose of RT-Thread RTOS Design and Implementation is to create an easy learning curve for mastering RT-Thread, so that more developers can participate in the development of RT-Thread and work together to create an open source, tiny, and beautiful Internet of Things operating system. The book's first part introduces the RT-Thread kernel and starts with an overview of RT-Thread before covering thread management, clock management, inter-thread synchronization, inter-thread communication, memory management, and interrupt management. The second part begins with RT-Thread kernel porting and explains how to port RT-Thread to a hardware board to run it. The second

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Advanced Windows Debugging Mario Hewardt, Daniel Pravat, 2007-10-29 The First In-Depth, Real-World, Insider's Guide to Powerful Windows Debugging For Windows developers, few tasks are more challenging than debugging—or more crucial. Reliable and realistic information about Windows debugging has always been scarce. Now, with over 15 years of experience two of Microsoft's system-level developers present a thorough and practical guide to Windows debugging ever written. Mario Hewardt and Daniel Pravat cover debugging throughout the entire application lifecycle and show how to make the most of the tools currently available—including Microsoft's powerful native debuggers and third-party solutions. To help you find real solutions fast, this book is organized around real-world debugging scenarios. Hewardt

and Pravat use detailed code examples to illuminate the complex debugging challenges professional developers actually face. From core Windows operating system concepts to security, Windows® Vista™ and 64-bit debugging, they address emerging topics head-on—and nothing is ever oversimplified or glossed over!

Digital Audio Signal Processing Udo Zölzer, 2022-02-24 Digital Audio Signal Processing The fully revised new edition of the popular textbook, featuring additional MATLAB exercises and new algorithms for processing digital audio signals Digital Audio Signal Processing (DASP) techniques are used in a variety of applications, ranging from audio streaming and computer-generated music to real-time signal processing and virtual sound processing. Digital Audio Signal Processing provides clear and accessible coverage of the fundamental principles and practical applications of digital audio processing and coding. Throughout the book, the authors explain a wide range of basic audio processing techniques and highlight new directions for automatic tuning of different algorithms and discuss state- of-the-art DASP approaches. Now in its third edition, this popular guide is fully updated with the latest signal processing algorithms for audio processing. Entirely new chapters cover nonlinear processing, Machine Learning (ML)

for audio applications, distortion, soft/hard clipping, overdrive, equalizers and delay effects, sampling and reconstruction, and more. Covers the fundamentals of quantization, filters, dynamic range control, room simulation, sampling rate conversion, and audio coding Describes DASP techniques, their theoretical foundations, and their practical applications Discusses modern studio technology, digital transmission systems, storage media, and home entertainment audio components Features a new introductory chapter and extensively revised content throughout Provides updated application examples and computer-based activities supported with MATLAB exercises and interactive JavaScript applets via an author-hosted companion website Balancing essential concepts and technological topics, Digital Audio Signal Processing, Third Edition remains the ideal textbook for advanced music technology and engineering students in audio signal processing courses. It is also an invaluable reference for audio engineers, hardware and software developers, and researchers in both academia and industry.

Real-Time Bluetooth Networks Jonathan W.

Valvano, 2016-11-14 Welcome to Real-Time Bluetooth Networks - Shape the World. This book, now in its second printing December

2017, offers a format geared towards hands-on self-paced learning. The overarching goal is to give you the student an experience with real-time operating systems that is based on the design and development of a simplified RTOS that exercises all the fundamental concepts. To keep the discourse grounded in practice we have refrained from going too deep into any one topic. We believe this will equip the student with the knowledge necessary to explore more advanced topics on their own. In essence, we will teach you the skills of the trade, but mastery is the journey you will have to undertake on your own. An operating system (OS) is layer of software that sits on top of the hardware. It manages the hardware resources so that the applications have the illusion that they own the hardware all to themselves. A real-time system is one that not only gets the correct answer but gets the correct answer at the correct time. Design and development of an OS therefore requires both, understanding the underlying architecture in terms of the interface (instruction set architecture, ISA) it provides to the software, and organizing the software to exploit this interface and present it to user applications. The decisions made in effectively managing the underlying architecture becomes more crucial in real-time systems as the performance (specifically timing) demands go

beyond simple logical correctness. The architecture we will focus on is the ARM ISA, which is a very popular architecture in the embedded device ecosystem where real-time systems proliferate. A quick introduction to the ISA will be followed by specifics of TI's offering of this ISA as the Tiva and MSP432 Launchpad microcontroller. To make the development truly compelling we need a target application that has real-time constraints and multi-threading needs. To that end you will incrementally build a personal fitness device with Bluetooth connectivity. The Bluetooth connectivity will expose you to the evolving domain of Internet-of-things (IoT) where our personal fitness device running a custom RTOS will interact with a smartphone.

Memory Dump Analysis Anthology, Volume 13 Software Diagnostics Institute, Dmitry Vostokov, 2021-12-30 This reference volume consists of revised, edited, cross-referenced, and thematically organized selected articles from Software Diagnostics Institute (DumpAnalysis.org + TraceAnalysis.org) and Software Diagnostics Library (former Crash Dump Analysis blog, DumpAnalysis.org/blog) about software diagnostics, root cause analysis, debugging, crash and hang dump analysis, software trace and log analysis written in December 2019 - July 2020 for software

engineers developing and maintaining products on Windows and Linux platforms, quality assurance engineers testing software, technical support, escalation and site reliability engineers dealing with complex software issues, security and vulnerability researchers, reverse engineers, malware and memory forensics analysts. This volume is fully cross-referenced with volumes 1 - 12 and features: - 9 new crash dump analysis patterns with selected downloadable example memory dumps; - 15 new software trace and log analysis patterns; - Introduction to diagnostic analysis gestures; - Introduction to the category-theoretic view of debugging; - Lists of recommended category theory, number theory, and cybersecurity books.

Embedded Microprocessor System Design using FPGAs Uwe Meyer-Baese, 2021-04-16 This textbook for courses in Embedded Systems introduces students to necessary concepts, through a hands-on approach. It gives a great introduction to FPGA-based microprocessor system design using state-of-the-art boards, tools, and microprocessors from Altera/Intel® and Xilinx®. HDL-based designs (soft-core), parameterized cores (Nios II and MicroBlaze), and ARM Cortex-A9 design are discussed, compared and explored using many hand-on designs projects. Custom IP for HDMI coder,

Floating-point operations, and FFT bit-swap are developed, implemented, tested and speed-up is measured. Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze, MicroBlaze, Nios II and ARMv7 architectures in VHDL and Verilog code, as well as the custom IP projects. Each Chapter has a substantial number of short quiz questions, exercises, and challenging projects. Explains soft, parameterized, and hard core systems design tradeoffs; Demonstrates design of popular KCPSM6 8 Bit microprocessor step-by-step; Discusses the 32 Bit ARM Cortex-A9 and a basic processor is synthesized; Covers design flows for both FPGA Market leaders Nios II Altera/Intel and MicroBlaze Xilinx system; Describes Compiler-Compiler Tool development; Includes a substantial number of Homework's and FPGA exercises and design projects in each chapter.

Memory Dump Analysis Anthology Dmitry Vostokov,2008-04

This revised, cross-referenced, and thematically organized volume of selected DumpAnalysis.org blog posts targets software engineers developing and maintaining products on Windows platforms, technical support, and escalation engineers.

The Design and Implementation of the RT-Thread Operating

System Qiu Yi,Xiong Puxiang,Tianlong Zhu,2020-11-13 Since the release of V0.01 in 2006, to the present V4.0 version, RT-Thread has developed a reputation among developers for its open source strategy. RT-Thread has gained a large following among members of the embedded open source community in China with hundreds of thousands of enthusiasts. RT-Thread is widely used in energy, automotive, medical, consumer electronics, among other applications, making it a mature and stable open source embedded operating system. The purpose of RT-Thread RTOS Design and Implementation is to create an easy learning curve for mastering RT-Thread, so that more developers can participate in the development of RT-Thread and work together to create an open source, tiny, and beautiful Internet of Things operating system. The book's first part introduces the RT-Thread kernel and starts with an overview of RT-Thread before covering thread management, clock management, inter-thread synchronization, inter-thread communication, memory management, and interrupt management. The second part begins with RT-Thread kernel porting and explains how to port RT-Thread to a hardware board to run it. The second part also introduces RT-Thread components and discusses the Env development environment, FinSH console, device management,

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Embedded Microcomputer Systems Jonathan W.

Valvano, 2012 *Embedded Microcomputer Systems: Real Time Interfacing* provides an in-depth discussion of the design of real-time embedded systems using 9S12 microcontrollers. This book covers the hardware aspects of interfacing, advanced software topics (including interrupts), and a systems approach to typical embedded applications. This text stands out from other microcomputer systems books because of its balanced, in-depth treatment of both hardware and software issues important in real time embedded systems design. It features a wealth of detailed case studies that demonstrate basic concepts in the context of actual working examples of systems. It also features a unique simulation software package on the bound-in CD-ROM (called Test Execute and Simulate, or TexaS, for short) – that provides a self-contained software environment for designing, writing,

implementing, and testing both the hardware and software components of embedded systems.

The Export of Goods (Control) Order 1970 Great Britain,
Enabling power:The Import, Export and Customs Powers (Defence)
Act 1939 s. 1. Made:01.09.70. Coming into force:28.09.70.
Effect:S.R. & O. 1931/413, SI 1951/1016, 1964/704, 1968/1020
Amended

Troubleshooting BGP Vinit Jain,Brad Edgeworth,2016-12-23
The definitive guide to troubleshooting today's complex BGP networks This is today's best single source for the techniques you need to troubleshoot BGP issues in modern Cisco IOS, IOS XR, and NxOS environments. BGP has expanded from being an Internet routing protocol and provides a scalable control plane for a variety of technologies, including MPLS VPNs and VXLAN. Bringing together content previously spread across multiple sources, Troubleshooting BGP describes BGP functions in today's blended service provider and enterprise environments. Two expert authors emphasize the BGP-related issues you're most likely to encounter in real-world deployments, including problems that have caused massive network outages. They fully address convergence and scalability, as well as common concerns such as BGP slow

peer, RT constraint filtering, and missing BGP routes. For each issue, key concepts are presented, along with basic configuration, detailed troubleshooting methods, and clear illustrations. Wherever appropriate, OS-specific behaviors are described and analyzed. Troubleshooting BGP is an indispensable technical resource for all consultants, system/support engineers, and operations professionals working with BGP in even the largest, most complex environments.

- Quickly review the BGP protocol, configuration, and commonly used features
- Master generic troubleshooting methodologies that are relevant to BGP networks
- Troubleshoot BGP peering issues, flapping peers, and dynamic BGP peering
- Resolve issues related to BGP route installation, path selection, or route policies
- Avoid and fix convergence problems
- Address platform issues such as high CPU or memory usage
- Scale BGP using route reflectors, diverse paths, and other advanced features
- Solve problems with BGP edge architectures, multihoming, and load balancing
- Secure BGP inter-domain routing with RPKI
- Mitigate DDoS attacks with RTBH and BGP Flowspec
- Understand common BGP problems with MPLS Layer 3 or Layer 2 VPN services
- Troubleshoot IPv6 BGP for service providers, including 6PE and 6VPE
- Overcome problems with VXLAN BGP EVPN data

center deployments · Fully leverage BGP High Availability features, including GR, NSR, and BFD · Use new BGP enhancements for link-state distribution or tunnel setup This book is part of the Networking Technology Series from Cisco Press, which offers networking professionals valuable information for constructing efficient networks, understanding new technologies, and building successful careers.

Hybrid Membrane Systems for Water Purification Rajindar Singh, 2006-02-08 Membrane systems are finding increasing application worldwide in the purification of potable and industrial water, and their design and use is set to grow considerably in years to come. This comprehensive book is written in a practical style with emphasis on process description, key unit operations, plant equipment description, equipment installation, safety and maintenance, process control, plant start-up, operation and troubleshooting. It is supplemented by case studies and useful engineering rules-of-thumb. The author is a chemical engineer with many years experience in the field and his technical knowledge and practical know-how in the water purification industry are summarised succinctly in this volume. This book... * Will ensure your system design is fit for its purpose * Informs readers of which

membranes to use; why, where and when * Will help readers to trouble-shoot and improve performance * Provides case studies help understanding through real-life situations This book... * Will ensure your system design is fit for its purpose * Informs readers of which membranes to use; why, where and when * Will help readers to trouble-shoot and improve performance * Provides case studies help understanding through real-life situations

The Definitive Guide to the ARM Cortex-M0 Joseph

Yiu,2011-04-04 The Definitive Guide to the ARM Cortex-M0 is a guide for users of ARM Cortex-M0 microcontrollers. It presents many examples to make it easy for novice embedded-software developers to use the full 32-bit ARM Cortex-M0 processor. It provides an overview of ARM and ARM processors and discusses the benefits of ARM Cortex-M0 over 8-bit or 16-bit devices in terms of energy efficiency, code density, and ease of use, as well as their features and applications. The book describes the architecture of the Cortex-M0 processor and the programmers model, as well as Cortex-M0 programming and instruction set and how these instructions are used to carry out various operations. Furthermore, it considers how the memory architecture of the Cortex-M0 processor affects software development; Nested Vectored Interrupt

Controller (NVIC) and the features it supports, including flexible interrupt management, nested interrupt support, vectored exception entry, and interrupt masking; and Cortex-M0 features that target the embedded operating system. It also explains how to develop simple applications on the Cortex-M0, how to program the Cortex-M0 microcontrollers in assembly and mixed-assembly languages, and how the low-power features of the Cortex-M0 processor are used in programming. Finally, it describes a number of ARM Cortex-M0 products, such as microcontrollers, development boards, starter kits, and development suites. This book will be useful to both new and advanced users of ARM Cortex devices, from students and hobbyists to researchers, professional embedded-software developers, electronic enthusiasts, and even semiconductor product designers. The first and definitive book on the new ARM Cortex-M0 architecture targeting the large 8-bit and 16-bit microcontroller market Explains the Cortex-M0 architecture and how to program it using practical examples Written by an engineer at ARM who was heavily involved in its development

The Definitive Guide to ARM® Cortex®-M3 and Cortex®-M4 Processors Joseph Yiu, 2013-10-06 This new edition has been fully revised and updated to include extensive information on the ARM

Cortex-M4 processor, providing a complete up-to-date guide to both Cortex-M3 and Cortex-M4 processors, and which enables migration from various processor architectures to the exciting world of the Cortex-M3 and M4. This book presents the background of the ARM architecture and outlines the features of the processors such as the instruction set, interrupt-handling and also demonstrates how to program and utilize the advanced features available such as the Memory Protection Unit (MPU). Chapters on getting started with IAR, Keil, gcc and CooCox ColIDE tools help beginners develop program codes. Coverage also includes the important areas of software development such as using the low power features, handling information input/output, mixed language projects with assembly and C, and other advanced topics. Two new chapters on DSP features and CMSIS-DSP software libraries, covering DSP fundamentals and how to write DSP software for the Cortex-M4 processor, including examples of using the CMSIS-DSP library, as well as useful information about the DSP capability of the Cortex-M4 processor A new chapter on the Cortex-M4 floating point unit and how to use it A new chapter on using embedded OS (based on CMSIS-RTOS), as well as details of processor features to support OS operations Various debugging techniques as well as

a troubleshooting guide in the appendix topics on software porting from other architectures A full range of easy-to-understand examples, diagrams and quick reference appendices

Mastering Windows Server 2012 R2 Mark Minasi, Kevin Greene, Christian Booth, Robert Butler, John McCabe, Robert Panek, Michael Rice, Stefan Röth, 2013-12-03 Check out the new Hyper-V, find new and easier ways to remotely connect back into the office, or learn all about Storage Spaces—these are just a few of the features in Windows Server 2012 R2 that are explained in this updated edition from Windows authority Mark Minasi and a team of Windows Server experts led by Kevin Greene. This book gets you up to speed on all of the new features and functions of Windows Server, and includes real-world scenarios to put them in perspective. If you're a system administrator upgrading to, migrating to, or managing Windows Server 2012 R2, find what you need to do the job in this complete resource. Learn all about:

- Installing or upgrading to and managing Windows Server 2012 R2
- Understanding Microsoft NIC teams 2012 and PowerShell Setting up via GUI or updated Server Core 2012
- Migrating, merging, and modifying your Active Directory Managing address spaces with IPAM
- Understanding new shared storage, storage spaces, and

better tools Controlling access to file shares—a new and improved approach Using and administering Remote Desktop, Virtual Desktop, and Hyper-V®

Receptor – Based Drug Design Paul Leff, 1998-04-10 Employing a wide range of examples from G-protein-coupled receptors and ligand-gated ion channels, this detailed, single-source reference illustrates the principles of pharmacological analysis and receptor classification that are the basis of rational drug design. Explains the experimental and theoretical methods used to characterize interactions between ligands and receptors-providing the pharmacological information needed to solve treatment problems and facilitate the drug design process! Demonstrating the achievements of the receptor-based approach in therapeutics and indicating future directions, **Receptor-Based Drug Design** introduces novel computer-assisted strategies for the design of new agonists, antagonists, and inverse agonists for G-protein-coupled receptors shows how to assess agonist concentration-effect curve data discusses radioligand binding assays presents new in vitro multiarray assays for G-protein-coupled receptors explains the use of individual second messenger signaling responses in analyzing drug-receptor interactions examines the role of electrophysiology in

finding new drugs and drug targets describes selectively acting b-adrenoceptor agonists and glucocorticoid steroids for asthma treatment outlines the rationale for using angiotensin receptor antagonists and more! Written by over 25 international authorities and containing nearly 1200 bibliographic citations, *Receptor-Based Drug Design* is a practical resource for pharmacologists, pharmacists, and pharmaceutical scientists; organic and medicinal chemists and biochemists; molecular biologists; biomedical researchers; and upper-level undergraduate and graduate students in these disciplines.

Mastering Windows Server 2003 Mark Minasi,Rhonda Layfield,Lisa Justice,2006-10-09 Provides information on planning, installing, configuring, and maintaining the Microsoft Windows Server 2003 R2, SP1 system.

Digital Signal Processing with Field Programmable Gate Arrays
Uwe Meyer-Baese,2013-03-09 Starts with an overview of today's FPGA technology, devices, and tools for designing state-of-the-art DSP systems. A case study in the first chapter is the basis for more than 30 design examples throughout. The following chapters deal with computer arithmetic concepts, theory and the implementation of FIR and IIR filters, multirate digital signal

processing systems, DFT and FFT algorithms, and advanced algorithms with high future potential. Each chapter contains exercises. The VERILOG source code and a glossary are given in the appendices, while the accompanying CD-ROM contains the examples in VHDL and Verilog code as well as the newest Altera Baseline software. This edition has a new chapter on adaptive filters, new sections on division and floating point arithmetics, an up-date to the current Altera software, and some new exercises.

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