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LP Steffe

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input 3 0 i2 assign y i1 i2
endmodule i thought of the
following test module

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multiplier truth table is given
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adder 2 bit multiplier using
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in reg a in reg 1 b in reg b in
reg 1 end endcase if bits 0
begin display multiplication
completed finish reg 1 b1
conversion of binary to bcd for
i 0 i n 2 i i 1 begin if 3 n 2 3 1 4
1 bcd reg 3 0 5 bcd reg 3 0 bcd
reg 3 0 3 if 7

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using just half and full adders -
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to create a modules that
simulates 4 bit multiplier
without using multiplication
need just to use half and full
adders so i succeeded to
program the solution from

some instance this is the code
module ha sout cout a b output
sout cout input a b assign sout
a b assign cout a b endmodule
module fa

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representation

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