Intel

Alexander Supalov, Andrey Semin, Christopher Dahnken, Michael Klemm

Inside Intel Tim Jackson,1998 The first book on 'the most profitable company on earth', by the bestselling author of Virgin King. Intel Trinity, The Michael S. Malone,2014-07-15 Based on unprecedented access to the corporation's archives, The Intel Trinity is the first full history of Intel Corporation—the essential company of the digital age—told through the lives of the three most important figures in the company's history: Robert Noyce, Gordon Moore, and Andy Grove. Often hailed the "most important company in the world," Intel remains, more than four decades after its inception, a defining company of the global digital economy. The legendary inventors of the microprocessor-the single most important product in the modern world-Intel today builds the tiny "engines" that power almost every intelligent electronic device on the planet. But the true story of Intel is the human story of the trio of geniuses behind it. Michael S. Malone reveals how each brought different things to Intel, and at different times. Noyce, the most respected high tech figure of his generation, brought credibility (and money) to the company's founding; Moore made Intel the world's technological leader; and Grove, has relentlessly driven the company to ever-higher levels of success and competitiveness. Without any one of these figures, Intel would never have achieved its historic success; with them, Intel made possible the personal computer, Internet, telecommunications, and the personal electronics revolutions. The Intel Trinity is not just the story of Intel's legendary past; it also offers an analysis of the formidable challenges that lie ahead as the company struggles to maintain its dominance, its culture, and its legacy. With eight pages of black-and-white photos.

<u>Product Guide</u> Intel Corporation,1985 Microcomputer components; Microprocessors; Development tools; Peripherals; Microcommunications components; Embedded controllers; Automotive products; Express (Extended reliability); Application specific integrated circuits (ASIC). Microcomputer systems; Single board computers; Personal computer enhacement products; OEM systems; Microcommunications systems; Parallel supercomputers; Software.

Platform Embedded Security Technology Revealed Xiaoyu Ruan,2014-08-28 Platform Embedded Security Technology Revealed is an in-depth introduction to Intel's platform embedded solution: the security and management engine. The engine is shipped inside most Intel platforms for servers, personal computers, tablets, and smartphones. The engine realizes advanced security and management functionalities and protects applications' secrets and users' privacy in a secure, light-weight, and inexpensive way. Besides native built-in features, it allows third-party software vendors to develop applications that take advantage of the security infrastructures offered by the engine. Intel's security and management engine is technologically unique and significant, but is largely unknown to many members of the tech communities who could potentially benefit from it. Platform Embedded Security Technology Revealed reveals technical details of the engine. The engine provides a new way for the computer security industry to resolve critical problems resulting from booming mobile technologies, such as increasing threats against confidentiality and privacy. This book describes how this advanced level of protection is made possible by the engine, how it can improve users' security experience, and how third-party vendors can make use of it. It's written for computer security professionals and researchers; embedded system engineers; and software engineers and vendors who are interested in developing new security applications on top of Intel's security and management engine. It's also written for advanced users who are interested in understanding how the security features of Intel's platforms work.

Intel Threading Building Blocks James Reinders, 2007-07-12 Book explains how to maximize the benefits of Intel's new dual-core and multi-core processors through a portable C++ library that works on Windows, Linux, Macintosh, and Unix systems.

Inside Intel Tim Jackson,1998-11-01 Named a Best Business Book by Business Week, Inside Intel is the gripping business saga of a company that rose to dominance through technological innovation, and maintained its leadership against competitors through aggressive marketing, tough business tactics, and liberal use of legal firepower. In his in-depth portrait of Intel, the first history/expose of the company, former Financial Times columnist Tim Jackson reveals that Intel's corporate culture is determinedly secretive and authoritarian, and the company retains its own force of private investigators to prevent its employees from going astray. Intel routinely uses the threat of lawsuits against workers and rivals. At the center of this story is Andy Grove, Intel's high-profile CEO and chairman, once a penniless immigrant who waited tables to put himself through college. It is Grove who has made the unpopular decisions which have kept Intel at the top of the chip market. Exhaustively researched from court records, unpublished documents, and interviews with Intel's competitors, partners, and past and present employees, Jackson traces the company's spectacular failures and successes, as well as the powerful human struggles that have made Intel one of the most competitive players in a high-stakes game.

Intel® Trusted Execution Technology for Server Platforms William Futral, James Greene, 2013-09-23 This book guides the server administrator / datacenter manager in enabling the technology as well as establishing a launch control policy that he can use to customize the server's boot process to fit the datacenter's requirements. This book explains how the OS (typically a Virtual Machine Monitor or Hypervisor) and supporting software can build on the secure facilities afforded by Intel TXT to provide additional security features and functions. It provides examples how the datacenter can create and use trusted pools.

The Intel Trinity Michael S. Malone, 2014-07-15 The definitive history of the Intel Corporation—the essential company of the digital age—told through the lives of its three preeminent figures: Robert Noyce, Gordon Moore, and Andy Grove Intel has often been hailed as the most important company in the world, and with good reason: While technology companies come and go, Intel remains, more than four decades after its inception, a defining company of the global digital economy. The legendary inventor of the microprocessor—the single most significant product in the modern world—Intel today builds the tiny engines that power almost every intelligent electronic device on the planet. But the true story of Intel is the human story of the three geniuses behind it. In The Intel Trinity, Michael S. Malone takes an unflinching look at the strengths and weaknesses each member of the trio has brought to Intel, and how, without the perfect balance, the company would never have reached its current level of success. Robert Noyce, the most respected high-tech figure of his generation, brought credibility (and money) to the company's founding; Gordon Moore made Intel the world's technological leader; and Andy Grove relentlessly drove the company to ever-higher levels of success and competitiveness. Without any one of these figures, Intel would never have achieved its historic success; with them, Intel made possible the personal computer, the Internet, telecommunications, and the personal electronics revolution. Based on unprecedented access to corporate archives, The Intel Trinity reveals the fascinating stories behind the company's ubiquitous products and the unique business practices—including a willingness to commit to new ideas, an initiative to make bold investments in lean times, and a devotion to upholding Gordon Moore's namesake law—that led Intel to consistent success unheard of elsewhere in the tech world. The Intel Trinity is not just the story of Intel's legendary past; it is also an analysis of the formidable challenges that lie ahead as the company struggles to maintain its dominance, its culture,

Android Application Development for the Intel Platform Ryan Cohen, Tao Wang, 2014-09-17 The number of Android devices running on Intel processors has increased since Intel and Google announced, in late 2011, that they would be working together to optimize future versions of Android for Intel Atom processors. Today, Intel processors can be found in Android smartphones and tablets made by some of the top manufacturers of Android devices, such as Samsung, Lenovo, and Asus. The increase in Android devices featuring Intel processors has created a demand for Android applications optimized for Intel Architecture: Android Application Development for the Intel® Platform is the perfect introduction for software engineers and mobile app developers. Through well-designed app samples, code samples and case studies, the book teaches Android application development based on the Intel platform—including for smartphones, tablets, and embedded devices—covering performance tuning, debugging and optimization. This

book is jointly developed for individual learning by Intel Software College and China Shanghai JiaoTong University.

Summary: Inside Intel BusinessNews Publishing,2013-02-15 The must-read summary of Tim Jackson's book: Inside Intel: How Andy Grove Built the World's Most Successful Chip Company. This complete summary of the ideas from Tim Jackson's book Inside Intel shows how Robert Noyce and Gordon Moore built their company thanks to their reputations and their dedication to their goals. In his book, the author tells their compelling story and analyses what made their business decisions so effective. According to Jackson, Intel's success was based on three things: expertise, persistence and hiring the right people. This summary explores the actions behind the company's success and is sure to inspire you to overcome obstacles and stay on the path to achieving your goal no matter what. Added-value of this summary: • Save time • Understand key concepts • Expand your business knowledge To learn more, read Inside Intel and discover the secrets behind the success of the global computing company and the journey taken by its founders to get there.

Parallel Programming with Intel Parallel Studio XE Stephen Blair-Chappell, Andrew Stokes, 2012-04-19 Optimize code for multicore processors with Intel's Parallel Studio Parallel programming is rapidly becoming a must-know skill for developers. Yet, where to start? This teach-yourself tutorial is an ideal starting point for developers who already know Windows C and C++ and are eager to add parallelism to their code. With a focus on applying tools, techniques, and language extensions to implement parallelism, this essential resource teaches you how to write programs for multicore and leverage the power of multicore in your programs. Sharing hands-on case studies and real-world examples, the authors examine the challenges of each project and show you how to overcome them. Explores conversion of serial code to parallel Focuses on implementing Intel Parallel Studio Highlights the benefits of using parallel code Addresses error and performance optimization of code Includes real-world scenarios that illustrate the techniques of advanced parallel programming situations Parallel Programming with Intel Parallel Studio dispels any concerns of difficulty and gets you started creating faster code with Intel Parallel Studio.

Intel Xeon Phi Coprocessor High Performance Programming James Jeffers, James Reinders, 2013-02-11 Authors Jim Jeffers and James Reinders spent two years helping educate customers about the prototype and pre-production hardware before Intel introduced the first Intel Xeon Phi coprocessor. They have distilled their own experiences coupled with insights from many expert customers, Intel Field Engineers, Application Engineers and Technical Consulting Engineers, to create this authoritative first book on the essentials of programming for this new architecture and these new products. This book is useful even before you ever touch a system with an Intel Xeon Phi coprocessor. To ensure that your applications run at maximum efficiency, the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors, Intel Xeon Phi coprocessors, or other high performance microprocessors. Applying these techniques will generally increase your program performance on any system, and better prepare you for Intel Xeon Phi coprocessors and the Intel MIC architecture. A practical guide to the essentials of the Intel Xeon Phi coprocessor Presents best practices for portable, high-performance computing and a familiar and proven threaded, scalar-vector programming model Includes simple but informative code examples that explain the unique aspects of this new highly parallel and high performance computational product Covers wide vectors, many cores, many threads and high bandwidth cache/memory architecture

Optimizing HPC Applications with Intel Cluster Tools Alexander Supalov, Andrey Semin, Christopher Dahnken, Michael Klemm, 2014-10-09 Optimizing HPC Applications with Intel® Cluster Tools takes the reader on a tour of the fast-growing area of high performance computing and the optimization of hybrid programs. These programs typically combine distributed memory and shared memory programming models and use the Message Passing Interface (MPI) and OpenMP for multi-threading to achieve the ultimate goal of high performance at low power consumption on enterprise-class workstations and compute clusters. The book focuses on optimization for clusters consisting of the Intel® Xeon processor, but the optimization methodologies also apply to the Intel® Xeon Phi™ coprocessor and heterogeneous clusters mixing both architectures. Besides the tutorial and reference content, the authors address and refute many myths and misconceptions surrounding the topic. The text is augmented and enriched by descriptions of real-life situations.

High-Performance Computing on the Intel® Xeon PhiTM Endong Wang,Qing Zhang,Bo Shen,Guangyong Zhang,Xiaowei Lu,Qing Wu,Yajuan Wang,2014-06-26 The aim of this book is to explain to high-performance computing (HPC) developers how to utilize the Intel® Xeon PhiTM series products efficiently. To that end, it introduces some computing grammar, programming technology and optimization methods for using many-integrated-core (MIC) platforms and also offers tips and tricks for actual use, based on the authors' first-hand optimization experience. The material is organized in three sections. The first section, "Basics of MIC", introduces the fundamentals of MIC architecture and programming, including the specific Intel MIC programming environment. Next, the section on "Performance Optimization" explains general MIC optimization techniques, which are then illustrated step-by-step using the classical parallel programming example of matrix multiplication. Finally, "Project development" presents a set of practical and experience-driven methods for using parallel computing in application projects, including how to determine if a serial or parallel CPU program is suitable for MIC and how to transplant a program onto MIC. This book appeals to two main audiences: First, software developers for HPC applications – it will enable them to fully exploit the MIC architecture and thus achieve the extreme performance usually required in biological genetics, medical imaging, aerospace, meteorology and other areas of HPC. Second, students and researchers engaged in parallel and high-performance computing – it will guide them on how to push the limits of system performance for HPC applications.

Intel Xeon Phi Coprocessor Architecture and Tools Rezaur Rahman,2013-09-26 Intel® Xeon PhiTM Coprocessor Architecture and Tools: The Guide for Application Developers provides developers a comprehensive introduction and in-depth look at the Intel Xeon Phi coprocessor architecture and the corresponding parallel data structure tools and algorithms used in the various technical computing applications for which it is suitable. It also examines the source code-level optimizations that can be performed to exploit the powerful features of the processor. Xeon Phi is at the heart of world's fastest commercial supercomputer, which thanks to the massively parallel computing capabilities of Intel Xeon Phi processors coupled with Xeon Phi coprocessors attained 33.86 teraflops of benchmark performance in 2013. Extracting such stellar performance in real-world applications requires a sophisticated understanding of the complex interaction among hardware components, Xeon Phi cores, and the applications running on them. In this book, Rezaur Rahman, an Intel leader in the development of the Xeon Phi coprocessor and the optimization of its applications, presents and details all the features of Xeon Phi core design that are relevant to the practice of application developers, such as its vector units, hardware multithreading, cache hierarchy, and host-to-coprocessor communication channels. Building on this foundation, he shows developers how to solve real-world technical computing problems by selecting, deploying, and optimizing the available algorithms and data structure alternatives matching Xeon Phi's hardware characteristics. From Rahman's practical descriptions and extensive code examples, the reader will gain a working knowledge of the Xeon Phi vector instruction set and the Xeon Phi microarchitecture whereby cores execute 512-bit instruction streams in parallel.

Summary of The Intel Trinity - [Review Keypoints and Take-aways] PenZen Summaries,2022-11-27 The summary of The Intel Trinity - How Robert Noyce, Gordon Moore and Andy Grove Built the World's Most Important Company presented here include a short review of the book at the start followed by quick overview of main points and a list of important take-aways at the end of the summary. The Summary of The Intel Trinity, which was published in 2014, is the first comprehensive history of Intel, which is widely considered to be the most important company of the digital age. The personalities of these three important people, Robert Noyce, Gordon Moore, and Andy Grove, formed a formidable business trinity, which contributed significantly to the success of the company. The Intel Trinity summary includes the key points and important takeaways from the book The Intel Trinity by Michael S. Malone.

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Intel Galileo Blueprints Marco Schwartz,2015-06-25 The Intel Galileo board was designed to add the power of an Intel processor to the simplicity of the Arduino platform. Intel Galileo gives you the freedom to create a wide range of DIY projects. Intel Galileo Blueprints will be a detailed guide that covers several projects based on the Intel Galileo board, exploiting the full potential of the board. You will first go through how to set up the development environment for the Galileo board. Next, you will connect different kinds of sensors to the Galileo board, and learn how to use the SD card reader of the board. You will then connect actuators to the Galileo board, like a relay and a servomotor, and write simple software to control these components. Later, you will access the Galileo board remotely in order to monitor the measurements done by the board and send the measured data to a Twitter feed at regular intervals. Finally, you will move on to more advanced topics, such as building a complete home automation system, building a mobile robot controlled by the Intel Galileo board and computer vision applications such as face recognition.

Intel Trusted Execution Technology for Server Platforms William Futral, James Greene, 2013-10-08 This book is a must have resource guide for anyone who wants to ... implement TXT within their environments. I wish we had this guide when our engineering teams were implementing TXT on our solution platforms!" John McAuley, EMC Corporation This book details innovative technology that provides significant benefit to both the cloud consumer and the cloud provider when working to meet the ever increasing requirements of trust and control in the cloud." Alex Rodriguez, Expedient Data Centers This book is an invaluable reference for understanding enhanced server security, and how to deploy and leverage computing environment trust to reduce supply chain risk." Pete Nicoletti. Virtustream Inc. Intel® Trusted Execution Technology (Intel TXT) is a new security technology that started appearing on Intel server platforms in 2010. This book explains Intel Trusted Execution Technology for Servers, its purpose, application, advantages, and limitations. This book guides the server administrator / datacenter manager in enabling the technology as well as establishing a launch control policy that he can use to customize the server's boot process to fit the datacenter's requirements. This book explains how the OS (typically a Virtual Machine Monitor or Hypervisor) and supporting software can build on the secure facilities afforded by Intel TXT to provide additional security features and functions. It provides examples how the datacenter can create and use trusted pools. With a foreword from Albert Caballero, the CTO at Trapezoid.

Intel Corporation Patent Landscape Analysis – January 1, 1994 to December 31, 2013 Reiner E. Jargosch, Joseph Jurich, 2014-06-30 The following analysis illustrates the underlying trends and relationships of U.S. issued patents of the subject company. The analysis employs two frequently used patent classification methods: US Patent Classification (UPC) and International Patent Classification (IPC). Aside from assisting patent examiners in determining the field of search for newly submitted patent applications, the two classification methods play a pivotal role in the characterization and analysis of technologies contained in collections of patent data. The analysis also includes the company's most prolific inventors, top cited patents as well as foreign filings by technology area.

Intel Xeon Phi Processor High Performance Programming James Jeffers, James Reinders, Avinash Sodani, 2016-05-31 This book is an all-in-one source of information for programming the Second-Generation Intel Xeon Phi product family also called Knights Landing. The authors provide detailed and timely Knights Landingspecific details, programming advice, and real-world examples. The authors distill their years of Xeon Phi programming experience coupled with insights from many expert customers — Intel Field Engineers, Application Engineers, and Technical Consulting Engineers — to create this authoritative book on the essentials of programming for Intel Xeon Phi products. Intel® Xeon PhiTM Processor High-Performance Programming is useful even before you ever program a system with an Intel Xeon Phi processor. To help ensure that your applications run at maximum efficiency, the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors, Intel Xeon Phi processors, or other high-performance microprocessors. Applying these techniques will generally increase your program performance on any system and prepare you better for Intel Xeon Phi processors. A practical guide to the essentials for programming Intel Xeon Phi processors Definitive coverage of the Knights Landing architecture Presents best practices for portable, high-performance computing and a familiar and proven threads and vectors programming model Includes real world code examples that highlight usages of the unique aspects of this new highly parallel and high-performance computational product Covers use of MCDRAM, AVX-512, Intel® Omni-Path fabric, many-cores (up to 72), and many threads (4 per core) Covers software developer tools, libraries and programming models Covers using Knights Landing as a processor and a coprocessor

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