

X86

Jitender Narula

Modern X86 Assembly Language Programming Daniel Kusswurm, 2018-12-06 Gain the fundamentals of x86 64-bit assembly language programming and focus on the updated aspects of the x86 instruction set that are most relevant to application software development. This book covers topics including x86 64-bit programming and Advanced Vector Extensions (AVX) programming. The focus in this second edition is exclusively on 64-bit base programming architecture and AVX programming. Modern X86 Assembly Language Programming's structure and sample code are designed to help you quickly understand x86 assembly language programming and the computational capabilities of the x86 platform. After reading and using this book, you'll be able to code performance-enhancing functions and algorithms using x86 64-bit assembly language and the AVX, AVX2 and AVX-512 instruction set extensions. What You Will Learn Discover details of the x86 64-bit platform including its core architecture, data types, registers, memory addressing modes, and the basic instruction set Use the x86 64-bit instruction set to create performance-enhancing functions that are callable from a high-level language (C++) Employ x86 64-bit assembly language to efficiently manipulate common data types and programming constructs including integers, text strings, arrays, and structures Use the AVX instruction set to perform scalar floating-point arithmetic Exploit the AVX, AVX2, and AVX-512 instruction sets to significantly accelerate the performance of computationally-intense algorithms in problem domains such as image processing, computer graphics, mathematics, and statistics Apply various coding strategies and techniques to optimally exploit the x86 64-bit, AVX, AVX2, and AVX-512 instruction sets for maximum possible performance Who This Book Is For Software developers who want to learn how to write code using x86 64-bit assembly language. It's also ideal for software developers who already have a basic understanding of x86 32-bit or 64-bit assembly language programming and are interested in learning how to exploit the SIMD capabilities of AVX, AVX2 and AVX-512.

Modern X86 Assembly Language Programming Daniel Kusswurm, 2014-11-29 Modern X86 Assembly Language Programming shows the fundamentals of x86 assembly language programming. It focuses on the aspects of the x86 instruction set that are most relevant to application software development. The book's structure and sample code are designed to help the reader quickly understand x86 assembly language programming and the computational capabilities of the x86 platform. Please note: Book appendixes can be downloaded here: <http://www.apress.com/9781484200650> Major topics of the book include the following: 32-bit core architecture, data types, internal registers, memory addressing modes, and the basic instruction set

X87 core architecture, register stack, special purpose registers, floating-point encodings, and instruction set MMX technology and instruction set Streaming SIMD extensions (SSE) and Advanced Vector Extensions (AVX) including internal registers, packed integer arithmetic, packed and scalar floating-point arithmetic, and associated instruction sets 64-bit core architecture, data types, internal registers, memory addressing modes, and the basic instruction set 64-bit extensions to SSE and AVX technologies X86 assembly language optimization strategies and techniques

X86 Assembly Language and C Fundamentals Joseph Cavanagh, 2013-01-22 The predominant language used in embedded microprocessors, assembly language lets you write programs that are typically faster and more compact than programs written in a high-level language and provide greater control over the program applications. Focusing on the languages used in X86 microprocessors, X86 Assembly Language and C Fundamentals explains how to write programs in the X86 assembly language, the C programming language, and X86 assembly language modules embedded in a C program. A wealth of program design examples, including the complete code and outputs, help you grasp the concepts more easily. Where needed, the book also details the theory behind the design. Learn the X86 Microprocessor Architecture and Commonly Used Instructions Assembly language programming requires knowledge of number representations, as well as the architecture of the computer on which the language is being used. After covering the binary, octal, decimal, and hexadecimal number systems, the book presents the general architecture of the X86 microprocessor, individual addressing modes, stack operations, procedures, arrays, macros, and input/output operations. It highlights the most commonly used X86 assembly language instructions, including data transfer, branching and looping, logic, shift and rotate, and string instructions, as well as fixed-point, binary-coded decimal (BCD), and floating-point arithmetic instructions. Get a Solid Foundation in a Language Commonly Used in Digital Hardware Written for students in computer science and electrical, computer, and software engineering, the book assumes a basic background in C programming, digital logic design, and computer architecture. Designed as a tutorial, this comprehensive and self-contained text offers a solid foundation in assembly language for anyone working with the design of digital hardware.

The Art of 64-Bit Assembly, Volume 1 Randall Hyde, 2021-11-30 A new assembly language programming book from a well-loved master. Art of 64-bit Assembly Language capitalizes on the long-lived success of Hyde's seminal The Art of Assembly Language. Randall Hyde's The Art of Assembly Language has been the go-to book for learning assembly language for decades. Hyde's latest work, Art of 64-bit Assembly Language is the 64-bit

version of this popular text. This book guides you through the maze of assembly language programming by showing how to write assembly code that mimics operations in High-Level Languages. This leverages your HLL knowledge to rapidly understand x86-64 assembly language. This new work uses the Microsoft Macro Assembler (MASM), the most popular x86-64 assembler today. Hyde covers the standard integer set, as well as the x87 FPU, SIMD parallel instructions, SIMD scalar instructions (including high-performance floating-point instructions), and MASM's very powerful macro facilities. You'll learn in detail: how to implement high-level language data and control structures in assembly language; how to write parallel algorithms using the SIMD (single-instruction, multiple-data) instructions on the x86-64; and how to write stand alone assembly programs and assembly code to link with HLL code. You'll also learn how to optimize certain algorithms in assembly to produce faster code.

The X86 Microprocessors: Architecture And Programming (8086 To Pentium) Das Lyla B,2010-09

Introduction to Computer Organization Robert G. Plantz,2022-01-25 This hands-on tutorial is a broad examination of how a modern computer works. Classroom tested for over a decade, it gives readers a firm understanding of how computers do what they do, covering essentials like data storage, logic gates and transistors, data types, the CPU, assembly, and machine code. Introduction to Computer Organization gives programmers a practical understanding of what happens in a computer when you execute your code. You may never have to write x86-64 assembly language or design hardware yourself, but knowing how the hardware and software works will give you greater control and confidence over your coding decisions. We start with high level fundamental concepts like memory organization, binary logic, and data types and then explore how they are implemented at the assembly language level. The goal isn't to make you an assembly programmer, but to help you comprehend what happens behind the scenes between running your program and seeing "Hello World" displayed on the screen. Classroom-tested for over a decade, this book will demystify topics like: How to translate a high-level language code into assembly language How the operating system manages hardware resources with exceptions and interrupts How data is encoded in memory How hardware switches handle decimal data How program code gets transformed into machine code the computer understands How pieces of hardware like the CPU, input/output, and memory interact to make the entire system work Author Robert Plantz takes a practical approach to the material, providing examples and exercises on every page, without sacrificing technical details. Learning how to think like a computer will help you write better programs, in any language, even if you never look at another line of assembly code again.

Android on x86 Iggý Krajci, Darren Cummings, 2014-01-23 Android on x86: an Introduction to Optimizing for Intel® Architecture serves two main purposes. First, it makes the case for adapting your applications onto Intel's x86 architecture, including discussions of the business potential, the changing landscape of the Android marketplace, and the unique challenges and opportunities that arise from x86 devices. The fundamental idea is that extending your applications to support x86 or creating new ones is not difficult, but it is imperative to know all of the technicalities. This book is dedicated to providing you with an awareness of these nuances and an understanding of how to tackle them. Second, and most importantly, this book provides a one-stop detailed resource for best practices and procedures associated with the installation issues, hardware optimization issues, software requirements, programming tasks, and performance optimizations that emerge when developers consider the x86 Android devices. Optimization discussions dive into native code, hardware acceleration, and advanced profiling of multimedia applications. The authors have collected this information so that you can use the book as a guide for the specific requirements of each application project. This book is not dedicated solely to code; instead it is filled with the information you need in order to take advantage of x86 architecture. It will guide you through installing the Android SDK for Intel Architecture, help you understand the differences and similarities between processor architectures available in Android devices, teach you to create and port applications, debug existing x86 applications, offer solutions for NDK and C++ optimizations, and introduce the Intel Hardware Accelerated Execution Manager. This book provides the most useful information to help you get the job done quickly while utilizing best practices.

Modern Computer Architecture and Organization Jim Ledin, Dave Farley, 2022-05-04 A no-nonsense, practical guide to current and future processor and computer architectures that enables you to design computer systems and develop better software applications across a variety of domains
Key Features
Understand digital circuitry through the study of transistors, logic gates, and sequential logic
Learn the architecture of x86, x64, ARM, and RISC-V processors, iPhones, and high-performance gaming PCs
Study the design principles underlying the domains of cybersecurity, bitcoin, and self-driving cars
Book Description Are you a software developer, systems designer, or computer architecture student looking for a methodical introduction to digital device architectures, but are overwhelmed by the complexity of modern systems? This step-by-step guide will teach you how modern computer systems work with the help of practical examples and exercises. You'll gain insights into the internal behavior of processors down to the circuit level and will understand how the hardware executes code developed in high-level languages. This book will teach you the fundamentals of computer systems

including transistors, logic gates, sequential logic, and instruction pipelines. You will learn details of modern processor architectures and instruction sets including x86, x64, ARM, and RISC-V. You will see how to implement a RISC-V processor in a low-cost FPGA board and write a quantum computing program and run it on an actual quantum computer. This edition has been updated to cover the architecture and design principles underlying the important domains of cybersecurity, blockchain and bitcoin mining, and self-driving vehicles. By the end of this book, you will have a thorough understanding of modern processors and computer architecture and the future directions these technologies are likely to take. What you will learn

- Understand the fundamentals of transistor technology and digital circuits
- Explore the concepts underlying pipelining and superscalar processing
- Implement a complete RISC-V processor in a low-cost FPGA
- Understand the technology used to implement virtual machines
- Learn about security-critical computing applications like financial transaction processing
- Get up to speed with blockchain and the hardware architectures used in bitcoin mining
- Explore the capabilities of self-navigating vehicle computing architectures
- Write a quantum computing program and run it on a real quantum computer

Who this book is for This book is for software developers, computer engineering students, system designers, reverse engineers, and anyone looking to understand the architecture and design principles underlying modern computer systems: ranging from tiny, embedded devices to warehouse-size cloud server farms. A general understanding of computer processors is helpful but not required.

Computer Architecture and Programming of the Intel X86 Family Patrick Stakem, 2012-02-01 This book is an introduction to computer architecture hardware and software, presented in the context of the Intel x86 family. The x86 describes not only a line of microprocessor chips dating back to 1978, but also an instruction set architecture (ISA) that the chips implement. The chip families were built by Intel and other manufacturers, and execute the same instructions, but in different manners. The results are the same, arithmetically and logically, but may differ in their timing. This book covers the Intel ISA-16 and ISA-32 architectures from the 8086/8088 to the Pentium, including the math coprocessors. A chart of ISA processors is included.

Modern Computer Architecture and Organization Jim Ledin, 2020-04-30 A no-nonsense, practical guide to current and future processor and computer architectures, enabling you to design computer systems and develop better software applications across a variety of domains

Key Features

- Understand digital circuitry with the help of transistors, logic gates, and sequential logic
- Examine the architecture and instruction sets of x86, x64, ARM, and RISC-V processors
- Explore the architecture of modern devices such as the iPhone X and high-performance gaming PCs

Book Description Are you a

software developer, systems designer, or computer architecture student looking for a methodical introduction to digital device architectures but overwhelmed by their complexity? This book will help you to learn how modern computer systems work, from the lowest level of transistor switching to the macro view of collaborating multiprocessor servers. You'll gain unique insights into the internal behavior of processors that execute the code developed in high-level languages and enable you to design more efficient and scalable software systems. The book will teach you the fundamentals of computer systems including transistors, logic gates, sequential logic, and instruction operations. You will learn details of modern processor architectures and instruction sets including x86, x64, ARM, and RISC-V. You will see how to implement a RISC-V processor in a low-cost FPGA board and how to write a quantum computing program and run it on an actual quantum computer. By the end of this book, you will have a thorough understanding of modern processor and computer architectures and the future directions these architectures are likely to take.

What you will learn

- Get to grips with transistor technology and digital circuit principles
- Discover the functional elements of computer processors
- Understand pipelining and superscalar execution
- Work with floating-point data formats
- Understand the purpose and operation of the supervisor mode
- Implement a complete RISC-V processor in a low-cost FPGA
- Explore the techniques used in virtual machine implementation
- Write a quantum computing program and run it on a quantum computer

Who this book is for

This book is for software developers, computer engineering students, system designers, reverse engineers, and anyone looking to understand the architecture and design principles underlying modern computer systems from tiny embedded devices to warehouse-size cloud server farms. A general understanding of computer processors is helpful but not required.

Computer Architecture & Programming of the Intel X86 Family Patrick Stakem, 2016-12-31 This book is an introduction to computer architecture, hardware and software, presented in the context of the Intel x86 family. The x86 describes not only a line of microprocessor chips dating back to 1978, but also an instruction set architecture (ISA) that the chips implement. The chip families were built by Intel and other manufacturers, and execute the same instructions, but in different manners. The results are the same, arithmetically and logically, but may differ in their timing. Why the focus on the Intel x86? It was the basis of the IBM personal computer (PC) family and its spin-offs. It has transitioned from a 16 to a 32 to a 64-bit architecture, keeping compatibility for more than 30 years. It's an de-facto industry standard that has withstood the test of time. This book covers the Intel ISA-16 and ISA-32 architectures from the 8086/8088 to the Pentium, including the math coprocessors. A chart of ISA processors is included. The purpose of this

book is to provide the basic background information for an understanding of the 80x86 family, the IBM Personal Computer (pc), and programming in assembly language as an introduction to the broader field of Computer Architecture. It will stress the pervasiveness of this pc-based technology in everyday things and events. It will provide an introduction to Software System Engineering and the Design for Debugging methodology. This book is a spin-off of a course in Computer Architecture/System Integration, taught in the graduate Engineering Science Program at Loyola College (now, Loyola University in Maryland). If we learn to program in the language c, for example, we can take our skills to any computer with a set of c-based tools. If we learn IA-32 assembly language, we have to relearn a language if we switch to a different architecture. So, why do we learn assembly language? Because it gives us insight into the underlying hardware, how it is organized, and how it operates. This book is dedicated to the graduate students in Engineering Science at Loyola College, Columbia Campus, who took the course EG-611, System Integration I, the x86 Architecture and Assembly Language. The course was given to hundreds of students over a span of 15 years by myself and others. An Extensive bibliography is provided.

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Architecture & Programming of the Intel X86 Family: 40th Anniversary Edition Patrick Stakem, 2018-06-16 This new effort was triggered by the introduction of a new chip for the 40th anniversary of the x86 architecture. Who would have thought the architecture would remain commercially viable at

the time? This gives us a unique opportunity to see how Moore's law has held up over time, and to compare and contrast the 40-year old part with the current one, to understand where we are coming from, and heading to. I removed a lot of the material from the original on the basics, including the number systems, data structures, I/O methods, etc. These are generic, and can be found in many Introduction to Computer Science texts. I have added details on the 40th anniversary chip, did a compare and contrast with the original 8086, and included some material on AMD's rival x86-architecture chips.

Implementing Reverse Engineering Jitender Narula, 2021-08-27 More practical less theory KEY FEATURES □ In-depth practical demonstration with multiple examples of reverse engineering concepts. □ Provides a step-by-step approach to reverse engineering, including assembly instructions. □ Helps security researchers to crack application code and logic using reverse engineering open source tools. □ Reverse engineering strategies for simple-to-complex applications like Wannacry ransomware and Windows calculator. DESCRIPTION The book 'Implementing Reverse Engineering' begins with a step-by-step explanation of the fundamentals of reverse engineering. You will learn how to use reverse engineering to find bugs and hacks in real-world applications. This book is divided into three sections. The first section is an exploration of the reverse engineering process. The second section explains reverse engineering of applications, and the third section is a collection of real-world use-cases with solutions. The first section introduces the basic concepts of a computing system and the data building blocks of the computing system. This section also includes open-source tools such as CFF Explorer, Ghidra, Cutter, and x32dbg. The second section goes over various reverse engineering practicals on various applications to give users hands-on experience. In the third section, reverse engineering of Wannacry ransomware, a well-known Windows application, and various exercises are demonstrated step by step. In a very detailed and step-by-step manner, you will practice and understand different assembly instructions, types of code calling conventions, assembly patterns of applications with the printf function, pointers, array, structure, scanf, strcpy function, decision, and loop control structures. You will learn how to use open-source tools for reverse engineering such as portable executable editors, disassemblers, and debuggers. WHAT YOU WILL LEARN □ Understand different code calling conventions like CDECL, STDCALL, and FASTCALL with practical illustrations. □ Analyze and break WannaCry ransomware using Ghidra. □ Using Cutter, reconstruct application logic from the assembly code. □ Hack the Windows calculator to modify its behavior. WHO THIS BOOK IS FOR This book is for cybersecurity researchers, bug bounty hunters, software

developers, software testers, and software quality assurance experts who want to perform reverse engineering for advanced security from attacks. Interested readers can also be from high schools or universities (with a Computer Science background). Basic programming knowledge is helpful but not required.

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Assembly Language for X86 Processors Kip R. Irvine, 2017-07-13 Assembly language is as close to writing machine code as you can get without writing in pure hexadecimal. Since it is such a low-level language, it's not practical in all cases, but should definitely be considered when you're looking to maximize performance. With Assembly Language by Chris Rose, you'll learn how to write x64 assembly for modern CPUs, first by writing inline assembly for 32-bit applications, and then writing native assembly for C++ projects. You'll learn the basics of memory spaces, data segments, CISC instructions, SIMD instructions, and much more. Whether you're working with Intel, AMD, or VIA CPUs, you'll find this book a valuable starting point since many of the instructions are shared between processors. This updated and expanded second edition of Book provides a user-friendly introduction to the subject, Taking a clear structural framework, it guides the reader through the subject's core elements. A flowing writing style combines with the use of illustrations and diagrams throughout the text to ensure the reader understands even the most complex of concepts. This succinct and enlightening overview is a required reading for all those interested in the subject. We hope you find this book useful in shaping your future career & Business.

x86 Software Reverse-Engineering, Cracking, and Counter-Measures Stephanie Domas, Christopher Domas, 2024-01-02 A crystal-clear and practical blueprint to software disassembly x86 Software Reverse-Engineering, Cracking, and Counter-Measures is centered around the world of disassembling

software. It will start with the basics of the x86 assembly language, and progress to how that knowledge empowers you to reverse-engineer and circumvent software protections. No knowledge of assembly, reverse engineering, or software cracking is required. The book begins with a bootcamp on x86, learning how to read, write, and build in the assembly that powers a massive amount of the world's computers. Then the book will shift to reverse engineering applications using a handful of industry favorites such as IDA, Ghidra, Olly, and more. Next, we move to cracking with techniques such as patching and key generation, all harnessing the power of assembly and reverse engineering. Lastly, we'll examine cracking from a defensive perspective. Providing learners with techniques to be a better defender of their own software, or knowledge to crack these techniques more effectively.

Assembly: computer Architecture, x86, system calls, building and linking, ASCII, condition codes, GDB, control flow, stack, calling conventions Reverse Engineering: reconnaissance, strings, RE strategy, stripping, linking, optimizations, compilers, industry tools Cracking: patching, key checkers, key generators, resource hacking, dependency walking Defense: anti-debugging, anti-tamper, packing, cryptors/decryptors, whitelist, blacklist, RASP, code signing, obfuscation A practical and hands-on resource for security professionals to hobbyists, this book is for anyone who wants to learn to take apart, understand, and modify black-box software. x86 Software Reverse-Engineering, Cracking, and Counter-Measures is a vital resource for security researchers, reverse engineers and defenders who analyze, research, crack or defend software applications.

Assembly Language Step-by-Step Jeff Duntemann, 2011-03-03 The eagerly anticipated new edition of the bestselling introduction to x86 assembly language The long-awaited third edition of this bestselling introduction to assembly language has been completely rewritten to focus on 32-bit protected-mode Linux and the free NASM assembler. Assembly is the fundamental language bridging human ideas and the pure silicon hearts of computers, and popular author Jeff Dunteman retains his distinctive lighthearted style as he presents a step-by-step approach to this difficult technical discipline. He starts at the very beginning, explaining the basic ideas of programmable computing, the binary and hexadecimal number systems, the Intel x86 computer architecture, and the process of software development under Linux. From that foundation he systematically treats the x86 instruction set, memory addressing, procedures, macros, and interface to the C-language code libraries upon which Linux itself is built. Serves as an ideal introduction to x86 computing concepts, as demonstrated by the only language directly understood by the CPU itself Uses an approachable, conversational style that assumes no prior experience in programming of any kind Presents x86 architecture and assembly concepts through a cumulative tutorial approach that

is ideal for self-paced instruction Focuses entirely on free, open-source software, including Ubuntu Linux, the NASM assembler, the Kate editor, and the Gdb/Insight debugger Includes an x86 instruction set reference for the most common machine instructions, specifically tailored for use by programming beginners Woven into the presentation are plenty of assembly code examples, plus practical tips on software design, coding, testing, and debugging, all using free, open-source software that may be downloaded without charge from the Internet.

The X86 Pc: Assembly Language, Design, And Interfacing, 5/E Mazidi Muhammad Ali,2010-09

Assembly Language for x86 Processors, Global Edition Kip R. Irvine,2015-01-16 Assembly Language for x86 Processors, 7e is suitable for undergraduate courses in assembly language programming and introductory courses in computer systems and computer architecture. Proficiency in one other programming language, preferably Java, C, or C++, is recommended. Written specifically for 32- and 64-bit Intel/Windows platform, this complete and fully updated study of assembly language teaches students to write and debug programs at the machine level. This text simplifies and demystifies concepts that students need to grasp before they can go on to more advanced computer architecture and operating systems courses. Students put theory into practice through writing software at the machine level, creating a memorable experience that gives them the confidence to work in any OS/machine-oriented environment. The full text downloaded to your computer With eBooks you can: search for key concepts, words and phrases make highlights and notes as you study share your notes with friends eBooks are downloaded to your computer and accessible either offline through the Bookshelf (available as a free download), available online and also via the iPad and Android apps. Upon purchase, you'll gain instant access to this eBook. Time limit The eBooks products do not have an expiry date. You will continue to access your digital ebook products whilst you have your Bookshelf installed.

Assembly Language for X86 Processors Kip Irvine,2020-09-04

X86 Instruction Set Architecture Tom Shanley,2009-11

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