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Jürgen Becker,Marco Platzner,Serge Vernalde

VHDL for Engineers Kenneth L. Short,2009 Suitable for use in a one- or two-semester course for computer and electrical engineering majors. VHDL for Engineers, First Edition is perfect for anyone with a basic understanding of logic design and a minimal background in programming who desires to learn how to design digital systems using VHDL. No prior experience with VHDL is required. This text teaches readers how to design and simulate digital systems using the hardware description language, VHDL. These systems are designed for implementation using programmable logic devices (PLDs) such as complex programmable logic devices (CPLDs) and field programmable gate arrays (FPGAs). The book focuses on writing VHDL design descriptions and VHDL testbenches. The steps in VHDL/PLD design methodology are also a key focus. Short presents the complex VHDL language in a logical manner, introducing concepts in an order that allows the readers to begin producing synthesizable designs as soon as possible.

Advanced ASIC Chip Synthesis Himanshu Bhatnagar,2007-05-08 Advanced ASIC Chip Synthesis: Using Synopsys® Design Compiler® Physical Compiler® and PrimeTime®, Second Edition describes the advanced concepts and techniques used towards ASIC chip synthesis, physical synthesis, formal verification and static timing analysis, using the Synopsys suite of tools. In addition, the entire ASIC design flow methodology targeted for VDSM (Very-Deep-Sub-Micron) technologies is covered in detail. The emphasis of this book is on real-time application of Synopsys tools, used to combat various problems seen at VDSM geometries. Readers will be exposed to an effective design methodology for handling complex, sub-micron ASIC designs. Significance is placed on HDL coding styles, synthesis and optimization, dynamic simulation, formal verification, DFT scan insertion, links to layout, physical synthesis, and static timing analysis. At each step, problems related to each phase of the design flow are identified, with solutions and work-around described in detail. In addition, crucial issues related to layout, which includes clock tree synthesis and back-end integration (links to layout) are also discussed at length. Furthermore, the book contains in-depth discussions on the basis of Synopsys technology libraries and HDL coding styles, targeted towards optimal synthesis solution. Target audiences for this book are practicing ASIC design engineers and masters level students undertaking advanced VLSI courses on ASIC chip design and DFT techniques.

Application-Specific Mesh-based Heterogeneous FPGA Architectures Husain Parvez,Habib Mehrez,2010-11-05 This book presents a new exploration environment for mesh-based, heterogeneous FPGA architectures. It describes state-of-the-art techniques for reducing area

requirements in FPGA architectures, which also increase performance and enable reduction in power required. Coverage focuses on reduction of FPGA area by introducing heterogeneous hard-blocks (such as multipliers, adders etc) in FPGAs, and by designing application specific FPGAs. Automatic FPGA layout generation techniques are employed to decrease non-recurring engineering (NRE) costs and time-to-market of application-specific, heterogeneous FPGA architectures.

Computer–Aided Reasoning Matt Kaufmann,Panagiotis Manolios,J Strother Moore,2013-04-17 Computer-Aided Reasoning: ACL2 Case Studies illustrates how the computer-aided reasoning system ACL2 can be used in productive and innovative ways to design, build, and maintain hardware and software systems. Included here are technical papers written by twenty-one contributors that report on self-contained case studies, some of which are sanitized industrial projects. The papers deal with a wide variety of ideas, including floating-point arithmetic, microprocessor simulation, model checking, symbolic trajectory evaluation, compilation, proof checking, real analysis, and several others. Computer-Aided Reasoning: ACL2 Case Studies is meant for two audiences: those looking for innovative ways to design, build, and maintain hardware and software systems faster and more reliably, and those wishing to learn how to do this. The former audience includes project managers and students in survey-oriented courses. The latter audience includes students and professionals pursuing rigorous approaches to hardware and software engineering or formal methods. Computer-Aided Reasoning: ACL2 Case Studies can be used in graduate and upper-division undergraduate courses on Software Engineering, Formal Methods, Hardware Design, Theory of Computation, Artificial Intelligence, and Automated Reasoning. The book is divided into two parts. Part I begins with a discussion of the effort involved in using ACL2. It also contains a brief introduction to the ACL2 logic and its mechanization, which is intended to give the reader sufficient background to read the case studies. A more thorough, textbook introduction to ACL2 may be found in the companion book, Computer-Aided Reasoning: An Approach. The heart of the book is Part II, where the case studies are presented. The case studies contain exercises whose solutions are on the Web. In addition, the complete ACL2 scripts necessary to formalize the models and prove all the properties discussed are on the Web. For example, when we say that one of the case studies formalizes a floating-point multiplier and proves it correct, we mean that not only can you read an English description of the model and how it was proved correct, but you can obtain the entire formal content of the project and replay the proofs, if you wish, with your copy of ACL2. ACL2 may be obtained from its home page. The results reported in each case study, as ACL2 input scripts, as well as exercise solutions for both

books, are available from this page.

Introduction to Physical Integration and Tapeout in VLSIs Patrick Lee,2010-04-27 This book covers issues and solutions in the physical integration and tapeout management for VLSI design. Chapter 1 gives the overview. Chapter 2 shows detailed techniques for physical design. Chapter 3 provides CAD flows. Chapter 4 discusses on-chip interconnects. A glossary of keywords is provided at the end.

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of NLST Stock after reading the book. Everyone who wants to know about the U.S. stock market.

Advances in the Boolean Domain Bernd Steinbach,2022-09-26 This book gathers together the results of research on the Boolean domain related to important real-life applications that will support the reader in solving their scientific and practical tasks. It highlights that ongoing digitalization leads to increasing amounts of complex applications, the digits of which are usually encoded by Boolean variables due to their simplicity as only two values are used. However, as shown here, an exponentially growing number of vectors of Boolean variables can negate this simplicity, which leads to challenges in advancing progress in the Boolean domain and supporting a wide range of applications.

Field Programmable Logic and Application Jürgen Becker,Marco Platzner,Serge Vernalde,2004-08-11 This book contains the papers presented at the 14th International Conference on Field Programmable Logic and Applications (FPL) held during August 30th- September 1st 2004. The conference was hosted by the Interuniversity Micro- Electronics Center (IMEC) in Leuven, Belgium. The FPL series of conferences was founded in 1991 at Oxford University (UK), and has been held annually since: in Oxford (3 times), Vienna, Prague, Darmstadt, London, Tallinn, Glasgow, Villach, Belfast, Montpellier and Lisbon. It is the largest and oldest conference in reconfigurable computing and brings together academic researchers, industry experts, users and newcomers in an informal, welcoming atmosphere that encourages productive exchange of ideas and knowledge between the delegates. The fast and exciting advances in field programmable logic are increasing steadily with more and more application potential and need. New ground has been broken in architectures, design techniques, (partial) run-time reconfiguration and applications of field programmable devices in several different areas. Many of these recent innovations are reported in this volume. The size of the FPL conferences has grown significantly over the years. FPL in 2003 saw 216 papers submitted. The interest and support for FPL in the programmable logic community continued this year with 285 scientific papers submitted, demonstrating a 32% increase when compared to the year before. The technical program was assembled from 78 selected regular papers, 45 additional short papers and 29 posters, resulting in this volume of proceedings. The program also included three invited plenary keynote presentations from Xilinx, Gilder Technology Report and Altera, and three embedded tutorials from Xilinx, the Universit $\Box$ at Karlsruhe (TH) and the University of Oslo.

Reversible Computation Martin Kutrib,Uwe Meyer,2023-07-11 This book constitutes the refereed proceedings of the 15th International Conference on Reversible Computation, RC 2023, held in Giessen, Germany, during July 18–19, 2023. The 11 full papers and 3 short papers included in this

book were carefully reviewed and selected from 19 submissions. They were organized in topical sections as follows: Foundations; Reversible Programming; Quantum Computing; and Quantum Circuits.

Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,2001

A Practical Approach to VLSI System on Chip (SoC) Design Veena S. Chakravarthi,2019-09-25 This book provides a comprehensive overview of the VLSI design process. It covers end-to-end system on chip (SoC) design, including design methodology, the design environment, tools, choice of design components, handoff procedures, and design infrastructure needs. The book also offers critical guidance on the latest UPF-based low power design flow issues for deep submicron SOC designs, which will prepare readers for the challenges of working at the nanotechnology scale. This practical guide will provide engineers who aspire to be VLSI designers with the techniques and tools of the trade, and will also be a valuable professional reference for those already working in VLSI design and verification with a focus on complex SoC designs. A comprehensive practical guide for VLSI designers; Covers end-to-end VLSI SoC design flow; Includes source code, case studies, and application examples.

The Electronic Design Automation Handbook Dirk Jansen,2010-02-23 When I attended college we studied vacuum tubes in our junior year. At that time an average radio had ?ve vacuum tubes and better ones even seven. Then transistors appeared in 1960s. A good radio was judged to be one with more thententransistors. Latergoodradioshad15–20transistors and after that everyone stopped counting transistors. Today modern processors runing personal computers have over 10milliontransistorsandmoremillionswillbeaddedevery year. The difference between 20 and 20M is in complexity, methodology and business models. Designs with 20 tr- sistors are easily generated by design engineers without any tools, whilst designs with 20M transistors can not be done by humans in reasonable time without the help of Prof. Dr. Gajski demonstrates the Y-chart automation. This difference in complexity introduced a paradigm shift which required sophisticated methods and tools, and introduced design automation into design practice. By the decomposition of the design process into many tasks and abstraction levels the methodology of designing chips or systems has also evolved. Similarly, the business model has changed from vertical integration, in which one company did all the tasks from product speci?cation to manufacturing, to globally distributed, client server production in which most of the design and manufacturing tasks are outsourced.

Computational Nanotechnology Sarhan M. Musa,2018-09-03 Applications of nanotechnology continue to fuel significant innovations in areas ranging from electronics, microcomputing, and biotechnology to medicine, consumer supplies, aerospace, and energy production. As progress in nanoscale science and engineering leads to the continued development of advanced materials and new devices, improved methods of modeling and simulation are required to achieve a more robust quantitative understanding of matter at the nanoscale. Computational Nanotechnology: Modeling and Applications with MATLAB® provides expert insights into current and emerging methods, opportunities, and challenges associated with the computational techniques involved in nanoscale research. Written by, and for, those working in the interdisciplinary fields that comprise nanotechnology—including engineering, physics, chemistry, biology, and medicine—this book covers a broad spectrum of technical information, research ideas, and practical knowledge. It presents an introduction to computational methods in nanotechnology, including a closer look at the theory and modeling of two important nanoscale systems: molecular magnets and semiconductor quantum dots. Topics covered include: Modeling of nanoparticles and complex nano and MEMS systems Theory associated with micromagnetics Surface modeling of thin films Computational techniques used to validate hypotheses that may not be accessible through traditional experimentation Simulation methods for various nanotubes and modeling of carbon nanotube and silicon nanowire transistors In regard to applications of computational nanotechnology in biology, contributors describe tracking of nanoscale structures in cells, effects of various forces on cellular behavior, and use of protein-coated gold nanoparticles to better understand protein-associated nanomaterials. Emphasizing the importance of MATLAB for biological simulations in nanomedicine, this wide-ranging survey of computational nanotechnology concludes by discussing future directions in the field, highlighting the importance of the algorithms, modeling software, and computational tools in the development of efficient nanoscale systems.

Basic VLSI Design Technology Cherry Bhargava,Gaurav Mani Khanal,2022-09-01 The current cutting-edge VLSI circuit design technologies provide end-users with many applications, increased processing power and improved cost effectiveness. This trend is accelerating, with significant implications on future VLSI and systems design. VLSI design engineers are always in demand for front-end and back-end design applications.The book aims to give future and current VSLI design engineers a robust understanding of the underlying principles of the subject. It not only focuses on circuit design processes obeying VLSI rules but also on technological aspects of fabrication. The Hardware Description Language (HDL) Verilog is explained along with its modelling style. The book

also covers CMOS design from the digital systems level to the circuit level. The book clearly explains fundamental principles and is a guide to good design practices.The book is intended as a reference book for senior undergraduate, first-year post graduate students, researchers as well as academicians in VLSI design, electronics & electrical engineering and materials science. The basics and applications of VLSI design from digital system design to IC fabrication and FPGA Prototyping are each covered in a comprehensive manner. At the end of each unit is a section with technical questions including solutions which will serve as an excellent teaching aid to all readers.Technical topics discussed in the book include: • Digital System Design• Design flow for IC fabrication and FPGA based prototyping • Verilog HDL• IC Fabrication Technology• CMOS VLSI Design• Miscellaneous (It covers basics of Electronics, and Reconfigurable computing, PLDs, Latest technology etc.).

Embedded Systems Design with Platform FPGAs Ronald Sass,Andrew G. Schmidt,2010-09-10 Embedded Systems Design with Platform FPGAs introduces professional engineers and students alike to system development using Platform FPGAs. The focus is on embedded systems but it also serves as a general guide to building custom computing systems. The text describes the fundamental technology in terms of hardware, software, and a set of principles to guide the development of Platform FPGA systems. The goal is to show how to systematically and creatively apply these principles to the construction of application-specific embedded system architectures. There is a strong focus on using free and open source software to increase productivity. Each chapter is organized into two parts. The white pages describe concepts, principles, and general knowledge. The gray pages provide a technical rendition of the main issues of the chapter and show the concepts applied in practice. This includes step-by-step details for a specific development board and tool chain so that the reader can carry out the same steps on their own. Rather than try to demonstrate the concepts on a broad set of tools and boards, the text uses a single set of tools (Xilinx Platform Studio, Linux, and GNU) throughout and uses a single developer board (Xilinx ML-510) for the examples. Explains how to use the Platform FPGA to meet complex design requirements and improve product performance Presents both fundamental concepts together with pragmatic, step-by-step instructions for building a system on a Platform FPGA Includes detailed case studies, extended real-world examples, and lab exercises

Fundamentals of Layout Design for Electronic Circuits Jens Lienig,Juergen Scheible,2020-03-19 This book covers the fundamental knowledge of layout design from the ground up, addressing both physical design, as generally applied to digital circuits, and analog layout. Such knowledge provides

the critical awareness and insights a layout designer must possess to convert a structural description produced during circuit design into the physical layout used for IC/PCB fabrication. The book introduces the technological know-how to transform silicon into functional devices, to understand the technology for which a layout is targeted (Chap. 2). Using this core technology knowledge as the foundation, subsequent chapters delve deeper into specific constraints and aspects of physical design, such as interfaces, design rules and libraries (Chap. 3), design flows and models (Chap. 4), design steps (Chap. 5), analog design specifics (Chap. 6), and finally reliability measures (Chap. 7). Besides serving as a textbook for engineering students, this book is a foundational reference for today's circuit designers. For Slides and Other Information: https://www.ifte.de/books/pd/index.html

Inside OrCAD Capture for Windows Chris Schroeder,1998 Introduction to Schematic Capture * Installation and Configuration * OrCAD Basics * Hierarchical Design * Post Processing * Library Editor * Advanced Features * Command Reference * Tips and Techniques.

Formal Verification Erik Seligman,Tom Schubert,M. V. Achutha Kiran Kumar,2023-05-26 Formal Verification: An Essential Toolkit for Modern VLSI Design, Second Edition presents practical approaches for design and validation, with hands-on advice to help working engineers integrate these techniques into their work. Formal Verification (FV) enables a designer to directly analyze and mathematically explore the quality or other aspects of a Register Transfer Level (RTL) design without using simulations. This can reduce time spent validating designs and more quickly reach a final design for manufacturing. Building on a basic knowledge of SystemVerilog, this book demystifies FV and presents the practical applications that are bringing it into mainstream design and validation processes. Every chapter in the second edition has been updated to reflect evolving FV practices and advanced techniques. In addition, a new chapter, Formal Signoff on Real Projects, provides guidelines for implementing signoff quality FV, completely replacing some simulation tasks with significantly more productive FV methods. After reading this book, readers will be prepared to introduce FV in their organization to effectively deploy FV techniques that increase design and validation productivity. Covers formal verification algorithms that help users gain full coverage without exhaustive simulation Helps readers understand formal verification tools and how they differ from simulation tools Shows how to create instant testbenches to gain insights into how models work and to find initial bugs Presents insights from Intel insiders who share their hard-won knowledge and solutions to complex design problems

Parasitic Substrate Coupling in High Voltage Integrated Circuits Pietro Buccella,Camillo

Stefanucci,Maher Kayal,Jean-Michel Sallese,2018-03-14 This book introduces a new approach to model and predict substrate parasitic failures in integrated circuits with standard circuit design tools. The injection of majority and minority carriers in the substrate is a recurring problem in smart power ICs containing high voltage, high current switching devices besides sensitive control, protection and signal processing circuits. The injection of parasitic charges leads to the activation of substrate bipolar transistors. This book explores how these events can be evaluated for a wide range of circuit topologies. To this purpose, new generalized devices implemented in Verilog-A are used to model the substrate with standard circuit simulators. This approach was able to predict for the first time the activation of a latch-up in real circuits through post-layout SPICE simulation analysis. Discusses substrate modeling and circuit-level simulation of parasitic bipolar device coupling effects in integrated circuits; Includes circuit back-annotation of the parasitic lateral n-p-n and vertical p-n-p bipolar transistors in the substrate; Uses Spice for simulation and characterization of parasitic bipolar transistors, latch-up of the parasitic p-n-p-n structure, and electrostatic discharge (ESD) protection devices; Offers design guidelines to reduce couplings by adding specific protections.
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