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Léo Pomar

A Hardware Accelerated MP3 Decoder with Bluetooth Streaming Capabilities ,

Understanding MP3 Martin Ruckert,2005-06-14 MPEG audio coding became popular under the name MP3. It is now the most important means of delivering high quality audio over the internet and will play the lead role in digital movie sound as well as in digital audio broadcast. This book explains the ideas, the concepts, and the implementation of MP3. Reading it requires no special prerequisites, but still, the book is detailed enough to include a fully executable highly efficient MP3 decoding engine. Not only understandable but even enjoyable.

Publishing Music Online Paul Sellars,2001 This is an absolute beginner's guide to MP3. Learn how to encode audio and publish your songs on the Internet. The book provides you with all the knowledge and tools you need to get your music online.

Digital Signal Processing System-Level Design Using LabVIEW Nasser Kehtarnavaz,Namjin Kim,2011-04-01 LabVIEW (Laboratory Virtual Instrumentation Engineering Workbench) developed by National Instruments is a graphical programming environment. Its ease of use allows engineers and students to streamline the creation of code visually, leaving time traditionally spent on debugging for true comprehension of DSP. This book is perfect for practicing engineers, as well as hardware and software technical managers who are familiar with DSP and are involved in system-level design. With this text, authors Kehtarnavaz and Kim have also provided a valuable resource for students in conventional engineering courses. The integrated lab exercises create an interactive experience which supports development of the hands-on skills essential for learning to navigate the LabVIEW program. Digital Signal Processing System-Level Design Using LabVIEW is a comprehensive tool that will greatly accelerate the DSP learning process. Its thorough examination of LabVIEW leaves no question unanswered. LabVIEW is the program that will demystify DSP and this is the book that will show you how to master it. * A graphical programming approach (LabVIEW) to DSP system-level design * DSP implementation of appropriate components of a LabVIEW designed system * Providing system-level, hands-on experiments for DSP lab or project courses

Advances in Recent Trends in Communication and Networks ,2010

Practical Design Verification Dhiraj K. Pradhan,Ian G. Harris,2009-06-11 Improve design efficiency & reduce costs with this guide to formal & simulation-based functional verification. Presenting a theoretical & practical understanding of the key issues involved, it explains both formal techniques (model checking, equivalence checking) & simulation-based techniques (coverage metrics, test generation).

MP3 Scot Hacker,2000 You'll find a detailed examination of the codec itself and its development, as well as how MP3 files compare with other common compression formats..

Analytical and Stochastic Modeling Techniques and Applications Khalid Al-Begain,Dieter Fiems,William Knottenbelt,2010-06 This book constitutes the refereed proceedings of the 17th International Conference on Analytical and Stochastic Modeling Techniques and Applications, ASMTA 2010, held in Cardiff, UK, in June 2010. The 28 revised full papers presented were carefully reviewed and selected from numerous submissions for inclusion in the book. The papers are organized in topical sections on queueing theory, specification languages and tools, telecommunication systems, estimation, prediction, and stochastic modelling.

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Vassilis Paliouras,2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation, PATMOS 2005, held in Leuven, Belgium in September 2005. The 74 revised full papers presented were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on low-power processors, code optimization for low-power, high-level design, telecommunications and signal processing, low-power circuits, system-on-chip design, busses and interconnections, modeling, design automation, low-power techniques, memory and register files, applications, digital circuits, and analog and physical design.

QuickTime for Java Chris Adamson,2005 Java developers who need to add audio, video, or interactive media creation and playback to their applications find that QuickTime Java is a powerful toolkit, but one that's not easy to get into. This book offers the first real look at this important software and lets impatient early adopters focus on learning by doing

Cosine-/Sine-Modulated Filter Banks Vladimir Britanak,K. R. Rao,2017-08-02 This book covers various algorithmic developments in the perfect reconstruction cosine/sine-modulated filter banks (TDAC-MDCT/MDST or MLT, MCLT, low delay MDCT, complex exponential/cosine/sine-modulated QMF filter banks), and near-perfect reconstruction QMF banks (pseudo-QMF banks) in detail, including their general mathematical properties, matrix representations, fast algorithms and various methods to integer approximations being recently a new transform technology for lossless audio coding. Each chapter will contain a number of examples and will conclude with problems and exercises. The book reflects the research efforts/activities and achieved results of the authors in the time period over the last 20 years.

Winning the SoC Revolution Grant Martin,Henry Chang,2012-12-06 In 1998-99, at the dawn of the SoC Revolution, we wrote Surviving the SOC Revolution: A Guide to Platform Based Design. In that book, we focused on presenting guidelines and best practices to aid engineers beginning to design complex System-on-Chip devices (SoCs). Now, in 2003, facing the mid-point of that revolution, we believe that it is time to focus on winning. In this book, Winning the SoC Revolution: Experiences in Real Design, we gather the best practical experiences in how to design SoCs from the most advanced design groups, while setting the issues and techniques in the context of SoC design methodologies. As an edited volume, this book has contributions from the leading design houses who are winning in SoCs - Altera, ARM, IBM, Philips, TI, UC Berkeley, and Xilinx. These chapters present the many facets of SoC design - the platform based approach, how to best utilize IP, Verification, FPGA fabrics as an alternative to ASICs, and next generation process technology issues. We also include observations from Ron Wilson of CMP Media on best practices for SoC design team collaboration. We hope that by utilizing this book, you too, will win the SoC Revolution.

Real-Time Digital Signal Processing Sen M. Kuo,Bob H. Lee,Wenshun Tian,2013-08-05 Combines both the DSP principles and real-timeimplementations and applications, and now updated with the newZdsp USB Stick, which is very low cost, portable and widelyemployed at many DSP labs. Real-Time Digital Signal Processing introducesfundamental digital signal processing (DSP) principles and will beupdated to include the latest DSP applications, introduce newsoftware development tools

and adjust the software design process to reflect the latest advances in the field. In the 3rd edition of the book, the key aspect of hands-on experiments will be enhanced to make the DSP principles more interesting and directly interact with the real-world applications. All of the programs will be carefully updated using the most recent version of software development tools and the new TMS320VC5505 eZdsp USB Stick for real-time experiments. Due to its lower cost and portability, the new software and hardware tools are now widely used in university labs and in commercial industrial companies to replace the older and more expensive generation. The new edition will have a renewed focus on real-time applications and will offer step-by-step hands-on experiments for a complete design cycle starting from floating-point C language program to fixed-point C implementation, code optimization using INTRINSICS, and mixed C-and-assembly programming on fixed-point DSP processors. This new methodology enables readers to concentrate on learning DSP fundamentals and innovative applications by relaxing the intensive programming efforts, namely, the traditional DSP assembly coding efforts. The book is organized into two parts; Part One introduces the digital signal processing principles and theories, and Part Two focuses on practical applications. The topics for the applications are the extensions of the theories in Part One with an emphasis placed on the hands-on experiments, systematic design and implementation approaches. The applications provided in the book are carefully chosen to reflect current advances of DSP that are of most relevance for the intended readership. Combines both the DSP principles and real-time implementations and applications using the new eZdsp USB Stick, which is very low cost, portable and widely employed at many DSP labs is now used in the new edition. Places renewed emphasis on C-code experiments and reduces the exercises using assembly coding; effective use of C programming, fixed-point C code and INTRINSICS will become the main focus of the new edition. Updates to application areas to reflect latest advances such as speech coding techniques used for next generation networks (NGN), audio coding with surrounding sound, wideband speech codec (ITU G.722.2 Standard), fingerprint for image processing, and biomedical signal processing examples. Contains new addition of several projects that can be used as semester projects; as well as new many new real-time experiments using TI's binary libraries – the experiments are prepared with flexible interface and modular for readers to adapt and modify to create other useful applications from the provided basic programs. Consists of more MATLAB experiments, such as filter design, algorithm evaluation, proto-typing for C-code architecture, and simulations to aid readers to learn DSP fundamentals. Includes supplementary material of program and data files for examples, applications, and experiments hosted on a companion website. A valuable resource for Postgraduate students enrolled on DSP courses focused on DSP implementation & applications as well as Senior undergraduates studying DSP; engineers and programmers who need to learn and use DSP principles and development tools for their projects.

Embedded DSP Processor Design Dale Liu, 2008-05-30 This book provides design methods for Digital Signal Processors and Application Specific Instruction set Processors, based on the author's extensive, industrial design experience. Top-down and bottom-up design methodologies are presented, providing valuable guidance for both students and practicing design engineers. Coverage includes design of internal-external data types, application specific instruction sets, micro architectures, including designs for datapath and control path, as well as memory sub systems. Integration and verification of a DSP-ASIP processor are discussed and reinforced with extensive examples. Instruction set design for application specific processors based on fast application profiling Micro architecture design methodology Micro architecture design details based on real examples Extendable architecture design protocols Design for efficient memory sub systems (minimizing on chip memory and cost) Real example designs based on extensive, industrial experiences

Compression for Great Digital Video Ben Waggoner, 2002 CD-ROM contains project files and sample media for three tutorials.

Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office, 2001

Parallel Computing Christian Bischof, 2008 ParCo 2007 marks a quarter of a century of the international conferences on parallel computing that started in Berlin in 1983. The aim of the conference is to give an overview of the developments, applications and future trends in high-performance computing for various platforms.

System-Level Design Techniques for Energy-Efficient Embedded Systems Marcus T. Schmitz, Bashir M. Al-Hashimi, Petru Eles, 2006-01-16 System-Level Design Techniques for Energy-Efficient Embedded Systems addresses the development and validation of co-synthesis techniques that allow an effective design of embedded systems with low energy dissipation. The book provides an overview of a system-level co-design flow, illustrating through examples how system performance is influenced at various steps of the flow including allocation, mapping, and scheduling. The book places special emphasis upon system-level co-synthesis techniques for architectures that contain voltage scalable processors, which can dynamically trade off between computational performance and power consumption. Throughout the book, the introduced co-synthesis techniques, which target both single-mode systems and emerging multi-mode applications, are applied to numerous benchmarks and real-life examples including a realistic smart phone.

Maximum PC, 2001-09 Maximum PC is the magazine that every computer fanatic, PC gamer or content creator must read. Each and every issue is packed with punishing product reviews, insightful and innovative how-to stories and the illuminating technical articles that enthusiasts crave.

Audio-Video Engineering,

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