

Simox

Electrochemical Society. Meeting

SIMOX Maria J. Anc,2004-12-03 SIMOX represents the first effort to compile a broad spectrum of knowledge from various groups of researchers and technologists in the world. It provides the reader with a basic understanding of SIMOX technology and in addition gives a good starting point for further investigation and applications.

Method of producing SIMOX wafer ,

Process for fabrication of a SIMOX substrate ,

Simox substrate production process and simox substrate ,

Proceedings of the Seventh International Symposium on Silicon-on-Insulator Technology and Devices Peter L. F. Hemment,1996

Silicon-on-Insulator Technology J.-P. Colinge,2013-03-09

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Physics of Semiconductor Devices K. N. Bhat,A. Dasgupta,2004 Contributed papers of the workshop held at IIT, Madras, in 2003.

SIMOX Evaluation Program ,1998 The equipment bought under this project is used to study the reliability of devices fabricated on SIMOX substrates. SIMOX (separation-by-implantation-of-oxygen) material is used primarily for low power IC application. The reliability of devices fabricated on SIMOX substrates is influenced by oxide degradation due to charge trapping, interface state generation, and bulk trap generation in SIMOX.

Silicon-on-Insulator Technology: Materials to VLSI J.-P. Colinge,2004-02-29 Silicon-on-Insulator Technology: Materials to VLSI, Third Edition, retraces the evolution of SOI materials, devices and circuits over a period of roughly twenty years. Twenty years of progress, research and development during which SOI material fabrication techniques have been born and abandoned, devices have been invented and forgotten, but, most importantly, twenty years during which SOI Technology has little by little proven it could outperform bulk silicon in every possible way. The turn of the century turned out to be a milestone for the semiconductor industry, as high-quality SOI wafers suddenly became available in large quantities. From then on, it took only a few years to witness the use of SOI technology in a wealth of applications ranging from audio amplifiers and wristwatches to 64-bit microprocessors. This book presents a complete and state-of-the-art review of SOI materials, devices and circuits. SOI fabrication and characterization techniques, SOI CMOS processing, and the physics of the SOI MOSFET receive an in-depth analysis. Silicon-on-Insulator Technology: Materials to VLSI, Third Edition, also describes the properties of other SOI devices, such as multiple gate MOSFETs, dynamic threshold devices and power MOSFETs. The advantages and performance of SOI circuits used in both niche and mainstream applications are discussed in detail. The SOI specialist will find this book invaluable as a source of compiled references covering the different aspects of SOI technology. For the non-specialist, the book serves an excellent introduction to the topic with detailed, yet simple and clear explanations. Silicon-on-Insulator Technology: Materials to VLSI, Third Edition is recommended for use as a textbook for classes on semiconductor device processing and physics at the graduate level.

Introduction to Microfabrication Sami Franssila,2004-06-14 Nanotechnology and microengineering are among the top priority research areas for the US and Europe. This text provides coverage of all aspects of the attempt to build functional devices at a molecular size.

Silicon-on-insulator Technology and Devices XI Electrochemical Society. Meeting,2003

Silicon-on-Insulator Technology and Devices X Electrochemical Society. Electronics Division,Electrochemical Society. Meeting,2001

Progress in SOI Structures and Devices Operating at Extreme Conditions Francis Balestra,Alexei N. Nazarov,Vladimir S. Lysenko,2012-12-06 A review of the electrical properties, performance and physical mechanisms of the main silicon-on-insulator (SOI) materials and devices. Particular attention is paid to the reliability of SOI structures operating in harsh conditions. The first part of the book deals with material technology and describes the SIMOX and ELTRAN technologies, the smart-cut technique, SiCOI structures and MBE growth. The second part covers reliability of devices operating under extreme conditions, with an examination of low and high temperature operation of deep submicron MOSFETs and novel SOI technologies and circuits, SOI in

harsh environments and the properties of the buried oxide. The third part deals with the characterization of advanced SOI materials and devices, covering laser-recrystallized SOI layers, ultrashort SOI MOSFETs and nanostructures, gated diodes and SOI devices produced by a variety of techniques. The last part reviews future prospects for SOI structures, analyzing wafer bonding techniques, applications of oxidized porous silicon, semi-insulating silicon materials, self-organization of silicon dots and wires on SOI and some new physical phenomena.

MOS Devices for Low-Voltage and Low-Energy Applications Yasuhisa Omura,Abhijit Mallik,Naoto Matsuo,2017-02-28 Helps readers understand the physics behind MOS devices for low-voltage and low-energy applications Based on timely published and unpublished work written by expert authors Discusses various promising MOS devices applicable to low-energy environmental and biomedical uses Describes the physical effects (quantum, tunneling) of MOS devices Demonstrates the performance of devices, helping readers to choose right devices applicable to an industrial or consumer environment Addresses some Ge-based devices and other compound-material-based devices for high-frequency applications and future development of high performance devices. Seemingly innocuous everyday devices such as smartphones, tablets and services such as on-line gaming or internet keyword searches consume vast amounts of energy. Even when in standby mode, all these devices consume energy. The upcoming 'Internet of Things' (IoT) is expected to deploy 60 billion electronic devices spread out in our homes, cars and cities. Britain is already consuming up to 16 per cent of all its power through internet use and this rate is doubling every four years. According to The UK's Daily Mail May (2015), if usage rates continue, all of Britain's power supply could be consumed by internet use in just 20 years. In 2013, U.S. data centers consumed an estimated 91 billion kilowatt-hours of electricity, corresponding to the power generated by seventeen 1000-megawatt nuclear power plants. Data center electricity consumption is projected to increase to roughly 140 billion kilowatt-hours annually by 2020, the equivalent annual output of 50 nuclear power plants. —Natural Resources Defense Council, USA, Feb. 2015 All these examples stress the urgent need for developing electronic devices that consume as little energy as possible. The book “MOS Devices for Low-Voltage and Low-Energy Applications” explores the different transistor options that can be utilized to achieve that goal. It describes in detail the physics and performance of transistors that can be operated at low voltage and consume little power, such as subthreshold operation in bulk transistors, fully depleted SOI devices, tunnel FETs, multigate and gate-all-around MOSFETs. Examples of low-energy circuits making use of these devices are given as well. The book MOS Devices for Low-Voltage and Low-Energy Applications is a good reference for graduate students, researchers, semiconductor and electrical engineers who will design the electronic systems of tomorrow. —Dr. Jean-Pierre Colinge, Taiwan Semiconductor Manufacturing Company (TSMC) The authors present a creative way to show how different MOS devices can be used for low-voltage and low-power applications. They start with Bulk MOSFET, following with SOI MOSFET, FinFET, gate-all-around MOSFET, Tunnel-FET and others. It is presented the physics behind the devices, models, simulations, experimental results and applications. This book is interesting for researchers, graduate and undergraduate students. The low-energy field is an important topic for integrated circuits in the future and none can stay out of this. —Prof. Joao A. Martino, University of Sao Paulo, Brazil

Deep Implants: Fundamentals and Applications G.G. Bentini,A. Golanski,S. Kalbitzer,1989-01-01 Deep implants are produced by the high-energy implantation of impurities in a host material. The thus created subsurface layers have properties that are different from the very surface and the bulk and show great promise for application in the electronics industry.

Strained Silicon Heterostructures C. K. Maiti,N. B. Chakrabarti,S. K. Ray,Institution of Electrical Engineers,2001 This book comprehensively covers the areas of materials growth, characterisation and descriptions for the new devices in siliconheterostructure material systems. In recent years, the development of powerful epitaxial growth techniques such as molecular beam epitaxy (MBE), ultra-high vacuum chemical vapour deposition (UHVCVD) and other low temperature epitaxy techniques has given rise to a new area of research of bandgap engineering in silicon-based materials. This has paved the way not only for heterojunction bipolar and field effect transistors, but also for other fascinating novel quantum devices. This book provides an excellent introduction and valuable references for postgraduate students and research scientists.

Properties of Crystalline Silicon Robert Hull,1999 A unique and well-organized reference, this book provides illuminating data, distinctive insight and expert guidance on silicon properties.

CVD XV Mark Donald Allendorf,Michael L. Hitchman,2000

Silicon Paul Siffert,Eberhard Krimmel,2013-03-09 With topics ranging from epitaxy through lattice defects and doping to quantum computation, this book provides a personalized survey of the development and use of silicon, the basis for the revolutionary changes in our lives sometimes called The Silicon Age. Beginning with the very first developments more than 50 years ago, this reports on all aspects of silicon and silicon technology up to its use in exciting new technologies, including a glance at possible future developments.

ULSI Process Integration II Cor L. Claeys,Electrochemical Society. Meeting,2001

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