

Encoder

Xiaohua Tian, Thinh M. Le, Yong Lian

Nonlinear Digital Encoders for Data Communications Calin Vladeanu,Safwan El Assad,2014-03-06

This book presents digital encoders for data communications. After an introduction on data communications and different sequences, the authors present the trellis-coded modulation (TCM) encoder as a digital filter followed by the trellis-coded and parallel turbo trellis-coded modulation schemes using nonlinear digital encoders. The book contains many numerical examples that complete the description of the analyzed schemes. Also, some performance simulation results are provided. Appendixes include demonstrations for the mathematical apparatus used throughout the book and some Matlab/Simulink source files used to run the simulations. Therefore, students can easily understand the concepts presented in the book and to simulate the schemes.

A Logarithmic Encoder for Binary Word Compression Joseph A. Sciulli,United States. National Aeronautics and Space Administration,1967

Encoderpro.com Expert Ingenix,2004-01-01

Introduction to Convolutional Codes with Applications Ajay Dholakia,2012-12-06 Introduction to Convolutional Codes with Applications is an introduction to the basic concepts of convolutional codes, their structure and classification, various error correction and decoding techniques for convolutionally encoded data, and some of the most common applications. The definition and representations, distance properties, and important classes of convolutional codes are also discussed in detail. The book provides the first comprehensive description of table-driven correction and decoding of convolutionally encoded data. Complete examples of Viterbi, sequential, and majority-logic decoding technique are also included, allowing a quick comparison among the different decoding approaches. Introduction to Convolutional Codes with Applications summarizes the research of the last two decades on applications of convolutional codes in hybrid ARQ protocols. A new classification allows a natural way of studying the underlying concepts of hybrid schemes and accommodates all of the new research. A novel application of fast decodable invertible convolutional codes for lost packet recovery in high speed networks is described. This opens the door for using convolutional coding for error recovery in high speed networks. Practicing communications, electronics, and networking engineers who want to get a better grasp of the underlying concepts of convolutional coding and its applications will greatly benefit by the simple and concise style of explanation. An up-to-date bibliography of over 300 papers is included. Also suitable for use as a textbook or a reference text in an advanced course on coding theory with emphasis on convolutional codes.

Roberts,2021-12-31

VLSI Design for Video Coding Youn-Long Steve Lin,Chao-Yang Kao,Hung-Chih Kuo,Jian-Wen Chen,2009-12-29 High definition video requires substantial compression in order to be transmitted or stored economically. Advances in video coding standards from MPEG-1, MPEG-2, MPEG-4 to H.264/AVC have provided ever increasing coding efficiency, at the expense of great computational complexity which can only be delivered through massively parallel processing. This book will present VLSI architectural design and chip implementation for high definition H.264/AVC video encoding, using a state-of-the-art video application, with complete VLSI prototype, via FPGA/ASIC. It will serve as an invaluable reference for anyone interested in VLSI design and high-level (EDA) synthesis for video.

Passive Encoder for Range Knobs William H. Schiffbauer,1983

Deep Learning Ian Goodfellow,Yoshua Bengio,Aaron Courville,2016-11-10 An introduction to a broad range of topics in deep learning, covering mathematical and conceptual background, deep learning techniques used in industry, and research perspectives. “Written by three experts in the field, Deep Learning is the only comprehensive book on the subject.” —Elon Musk, cochair of OpenAI; cofounder and CEO of Tesla and SpaceX Deep learning is a form of machine learning that enables computers to learn from experience and understand the world in terms of a hierarchy of concepts. Because the computer gathers knowledge from experience, there is no need for a human computer operator to formally specify all the knowledge that the computer needs. The hierarchy of concepts allows the computer to learn complicated concepts by building them out of simpler ones; a graph of these hierarchies would be many layers deep. This book introduces a broad range of topics in deep learning. The text offers mathematical and conceptual background, covering relevant concepts in linear algebra, probability theory and information theory, numerical computation, and machine learning. It describes deep learning techniques used by practitioners in industry, including deep feedforward networks, regularization, optimization algorithms, convolutional networks, sequence modeling, and practical methodology; and it surveys such applications as natural language processing, speech recognition, computer vision, online recommendation systems, bioinformatics, and videogames. Finally, the book offers research perspectives, covering such theoretical topics as linear factor models, autoencoders, representation learning, structured probabilistic models, Monte Carlo methods, the partition function, approximate inference, and deep generative models. Deep Learning can be used by undergraduate or graduate students planning careers in either industry or research, and by software

engineers who want to begin using deep learning in their products or platforms. A website offers supplementary material for both readers and instructors.

Punctured Convolutional Encoders David John Rowe,1997

Design of a Parallel Encoder/decoder for the Hamming Code, Using ROM. Stanford University Stanford Electronics Laboratories. Digital Systems Laboratory,1972 ROM implementation of logic circuits which have a large number of inputs is generally considered unwise. However, in the design of an encoder/decoder for the Hamming code, ROM implementation is found to yield many advantages over SSI and MSI implementation. There is a one-to-one correspondence between the partition of H matrix into submatrices and the partition of the set of the inputs to the encoder into subsets of the inputs to the ROM modules. Hence, several methods of partitioning the H matrix for the Hamming code are devised. The resulting ROM implementation is shown to save package count compared with other implementations. However, at the present state of technology, there is a trade-off between speed and package count. In the applications where speed is of the utmost importance, the SSI implementation using ECL logic is the most attractive. The disadvantage of ROM in speed should diminish in the near future when semiconductor memory technology will progress to the point where the slow DTL/TTL gates in the input buffer, the address decoder, and the output buffer of ROM, can be replaced by faster gates.

The Performance of 31 and 127 Bit Elastic Surface Wave Encoders and Decoders Peter A. DeVito,Paul H. Carr,Thomas L. Szabo,1972

Artificial Intelligence and Digitalization for Sustainable Development Bereket H. Woldegiorgis,Kibret Mequanint,Mekuanint A. Bitew,Teketay B. Beza,Abdulkerim M. Yibre,2023-03-18 This proceedings, ICAST 2022, constitutes the refereed post-conference proceedings of the 10th International Conference on Advancement of Science and Technology, ICAST 2022, which took place in Bahir Dar, Ethiopia, in November 2022. The 17 revised full papers and one short paper were carefully reviewed and selected from 174 submissions. The papers present economic and technologic developments in modern societies related to important issues such digitization, energy transformation, impact on national economy, and its recent advancements.

A Fine Grained Many-core H.264 Video Encoder Stephen The Uy Le,2010

Optically Coupled Digital Altitude Encoder for General Aviation Altimeters Floyd R. Bryant,1975

Entropy Coders of the H.264/AVC Standard Xiaohua Tian,Thinh M. Le,Yong Lian,2010-10-17 This book presents a collection of algorithms and VLSI architectures of entropy (or statistical) codecs of

recent video compression standards, with focus on the H.264/AVC standard. For any visual data compression scheme, there exists a combination of two, or all of the following three stages: spatial, temporal, and statistical compression. General readers are first introduced with the various algorithms of the statistical coders. The VLSI implementations are also reviewed and discussed. Readers with limited hardware design background are also introduced with a design methodology starting from performance-complexity analyses to software/hardware co-simulation. A typical design of the Contextbased Adaptive Binary Arithmetic Coding (CABAC) encoder is also presented in details. To support System-on-Chip design environment, the CABAC design is wrapped with a SoC-based Wishbone system bus interface.

Design of an Encoder, Converter, and Display Device Sander Rubin, 1963 Communication between an airborne navigational computer and the navigator requires means for encoding decimally entered information, displaying output information, and converting between the binary notation of the computer and the decimal notation of the operator. A design is presented of a device which performs those functions. Conversion between binary and B.C.D. notation is accomplished by a fixed-program, special-purpose computer coupled to the navigational computer. Display devices for decimal digits are proposed based upon a small stepping motor with opto-electronic feedback to assure that the device is actually in the position called for. A complete logic design is presented. Suggested construction is of micro-miniature electronic blocks available now or in the near future.

Pulse Code Modulation (PCM) Encoder Handbook for Aydin Vector MMP-900 Series System
David Raphael, 1995

Computer Controlled Systems G. Perdikaris, 1991-09-30 The primary objective of the book is to provide advanced undergraduate or first-year graduate engineering students with a self-contained presentation of the principles fundamental to the analysis, design and implementation of computer controlled systems. The material is also suitable for self-study by practicing engineers and is intended to follow a first course in either linear systems analysis or control systems. A secondary objective of the book is to provide engineering and/or computer science audiences with the material for a junior/senior-level course in modern systems analysis. Chapters 2, 3, 4, and 5 have been designed with this purpose in mind. The emphasis in such a course is to develop the mathematical tools and methods suitable for the analysis and design of real-time systems such as digital filters. Thus, engineers and/or computer scientists who know how to program computers can understand the mathematics relevant to the issue of what it is they are programming. This is especially important for

those who may work in engineering and scientific environments where, for instance, programming difference equations for real-time applications is becoming increasingly common. A background in linear algebra should be an adequate prerequisite for the systems analysis course. Chapter 1 of the book presents a brief introduction to computer controlled systems. It describes the general issues and terminology relevant to the analysis, design, and implementation of such systems.

Operator's, Organizational, and Direct Support Maintenance Manual, 1993

Digital Video: An Introduction to MPEG-2 Barry G. Haskell, Atul Puri, Arun N. Netravali, 2007-05-08

Digital Video offers comprehensive coverage of the MPEG-2 audio/visual digital compression standard. The treatment includes the specifics needed to implement an MPEG-2 Decoder, including the syntax and semantics of the coded bitstreams. Since the MPEG-2 Encoders are not specified by the standard, and are actually closely held secrets of many vendors, the book only outlines the fundamentals of encoder design and algorithm optimization.

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