

Cache

Duane Wessels

The Cache Memory Book Jim Handy,1998-01-13 The Second Edition of The Cache Memory Book introduces systems designers to the concepts behind cache design. The book teaches the basic cache concepts and more exotic techniques. It leads readers through some of the most intricate protocols used in complex multiprocessor caches. Written in an accessible, informal style, this text demystifies cache memory design by translating cache concepts and jargon into practical methodologies and real-life examples. It also provides adequate detail to serve as a reference book for ongoing work in cache memory design. The Second Edition includes an updated and expanded glossary of cache memory terms and buzzwords. The book provides new real world applications of cache memory design and a new chapter on cachetricks. Illustrates detailed example designs of caches Provides numerous examples in the form of block diagrams, timing waveforms, state tables, and code traces Defines and discusses more than 240 cache specific buzzwords, comparing in detail the relative merits of different design methodologies Includes an extensive glossary, complete with clear definitions, synonyms, and references to the appropriate text discussions

Web Caching Duane Wessels,2001 On the World Wide Web, speed and efficiency are vital. Users have little patience for slow web pages, while network administrators want to make the most of their available bandwidth. A properly designed web cache reduces network traffic and improves access times to popular web sites--a boon to network administrators and web users alike. Web Caching hands you all the technical information you need to design, deploy, and operate an effective web caching service. It starts with the basics of how web caching works, from the HTTP headers that govern cachability to cache validation and replacement algorithms. Topics covered in this book include: Designing an effective cache solution Configuring web browsers to use a cache Setting up a collection of caches that can talk to each other Configuring an interception cache or proxy Monitoring and fine-tuning the performance of a cache Configuring web servers to cooperate with web caches Benchmarking cache products The book also covers the important political aspects of web caching, including privacy, intellectual property, and security issues. Internet service providers, large corporations, or educational institutions--in short, any network that provides connectivity to a wide variety of users--can reap enormous benefit from running a well-tuned web caching service. Web Caching shows you how to do it right.

How to Puzzle Cache Cully Long,2015-04-30 FACED WITH A MAP FULL OF BLUE QUESTION MARKS AND NO IDEA WHERE TO START? Geocaching novices and pros alike are often daunted by puzzle and mystery caches. Ciphers, enigmatic photos, nonsensical text, or just a blank page -- it often isn't clear what the CO wants or expects you to do, and even less clear how any of it will lead you to a cache. THIS BOOK WILL HELP YOU ASK -- AND ANSWER -- ALL THE RIGHT QUESTIONS. 300 pages of lessons, tips, tricks and hints for dealing with even the trickiest puzzle caches, plus step-by-step techniques that start with examining the cache page and lead you through codes, ciphers, steganography, math, music, and dozens of other common puzzle cache types will have you on the trail in no time.

Getting Started with Varnish Cache Thijs Feryn,2017-03-13 How long does it take for your website to load? Web performance is just as critical for small and medium-sized websites as it is for massive websites that receive tons of hits. Before you pour money and time into rewriting your code or replacing your infrastructure, first consider a reverse-caching proxy server like Varnish. With this practical book, you'll learn how Varnish can give your website or API an immediate performance boost. Varnish mimicks the behavior of your webserver, caches its output in memory, and serves the result directly to clients without having to access your webserver. If you're a web developer familiar with HTTP, this book helps you master Varnish basics, so you can get up and running in no time. You'll learn how to use the Varnish Configuration Language and HTTP best practices to achieve faster performance and a higher hit rate. Understand how Varnish helps you gain optimum web performance Use HTTP to improve the cache-ability of your websites, web applications, and APIs Properly invalidate your cache when the origin data changes Optimize access to your backend servers Avoid common mistakes when using Varnish in the wild Use logging and debugging tools to examine the behavior of Varnish

Web Caching and Content Delivery A. Bestavros,M. Rabinovich,2001-12-14 The International Web Content Caching and Distribution Workshop (WCW) is a premiere technical meeting for researchers and practitioners interested in all aspects of content caching, distribution and delivery on the Internet. The 2001 WCW meeting was held on the Boston University Campus. Building on the successes of the five previous WCW meetings, WCW01 featured a strong technical program and record participation from leading researchers and practitioners in the field. This book consists of all the technical papers presented at WCW'01. It includes 20 full papers and four R&D synopses that were presented at the workshop. The collection reflects the latest research in this important area, including such topics as Content Delivery Networks (CDNs), tools and methodology of performance measurements, Web characterization as relates to caching and content distribution, scalable web server architectures, cache prefetching, emerging new edge services, and delivery of streaming content.

North Country Cache Joan H. Young,2005

Cache and Memory Hierarchy Design Steven A. Przybylski,1990 A widely read and authoritative book for hardware and software designers. This innovative book exposes the characteristics of performance-optimal single- and multi-level cache hierarchies by approaching the cache design process through the novel perspective of minimizing execution time.

Memory Systems Bruce Jacob,Spencer Ng,David Wang,2010-07-28 Is your memory hierarchy stopping your microprocessor from performing at the high level it should be? Memory Systems: Cache, DRAM, Disk shows you how to resolve this problem. The book tells you everything you need to know about the logical design and operation, physical design and operation, performance characteristics and resulting design trade-offs, and the energy consumption of modern memory hierarchies. You learn how to tackle the challenging optimization problems that result from the side-effects that can appear at any point in the entire hierarchy. As a result you will be able to design and emulate the entire memory hierarchy. Understand all levels of the system hierarchy -Xcache, DRAM, and disk. Evaluate the system-level effects of all design choices. Model performance and energy consumption for each component in the memory hierarchy.

The Birthday Cache Amanda Zieba,2016-12-12 While twelve year old Mason Miles and his parents love their nomadic lifestyle living and working across the nation in their RV, his twin sister Molly is craving a normal life and scheming to put a stop to their endless road trip. For their twelfth birthday the twins open a GPS receiver and fall in love with the sport of geocaching. When they stumble upon a mysterious puzzle cache will their travels become interesting enough to change Molly's mind? In this first installment of the geocaching series Adventures Await, author Amanda Zieba weaves an exciting middle grade story full of geocaching adventures; geography, technology and national landmark content; and family fun. Join the Miles family for the first of many geocaching adventures to come! Through their conflicts, adventures, and fun, the close-knit, but realistic family in this fast-paced chapter book sucked me into their world and made me care. - Jan Fields, author of the Monster Hunter Series

The Cache ,1993

A Primer on Memory Consistency and Cache Coherence Daniel J. Sorin,Mark D. Hill,David A. Wood,2011 Many modern computer systems and most multicore chips (chip multiprocessors) support shared memory in hardware. In a shared memory system, each of the processor cores may read and write to a single shared address space. For a shared memory machine, the memory consistency model defines the architecturally visible behavior of its memory system. Consistency definitions provide rules about loads and stores (or memory reads and writes) and how they act upon memory. As part of supporting a memory consistency model, many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up-to-date. The goal of this primer is to provide readers with a basic understanding of consistency and coherence. This understanding includes both the issues that must be solved as well as a variety of solutions. We present both highlevel concepts as well as specific, concrete examples from real-world systems. Table of Contents: Preface / Introduction to Consistency and Coherence / Coherence Basics / Memory Consistency Motivation and Sequential Consistency / Total Store Order and the x86 Memory Model / Relaxed Memory

Consistency / Coherence Protocols / Snooping Coherence Protocols / Directory Coherence Protocols / Advanced Topics in Coherence / Author Biographies

Cache Lake Country: Or, Life in the North Woods John J. Rowlands, 2017-04-11 The classic chronicle of life and self-reliance in the great Northern Forest, reissued for its many fans “Cache Lake Country is a gem for many reasons—a simple narrative, the ways in which it conveys the work-a-day joys and exertions of life in the wilderness, the woodcraft techniques it illustrates, and the slow and pleasurable way in which the soul of a serene man is revealed.” —The New York Times Over half a century ago, John Rowlands set out by canoe into the wilds of Canada to survey land for a timber company. After paddling alone for several days, he came upon the lake of my boyhood dreams, which he named Cache Lake because there was stored the best that the north had to offer?timber for a cabin; fish, game, and berries to live on; and the peace and contentment he felt he could not live without. This is his story, containing both folklore and philosophy, with wisdom about the woods and the demand therein for inventiveness. It includes directions for making moccasins, stoves, shelters, outdoor ovens, canoes, and hundreds of other ingenious and useful gadgets.

Cache Leo Damore, 1979-01-01

Analysis of Cache Performance for Operating Systems and Multiprogramming Anant Agarwal, 1989-03-31 As we continue to build faster and faster computers, their performance is becoming increasingly dependent on the memory hierarchy. Both the clock speed of the machine and its throughput per clock depend heavily on the memory hierarchy. The time to complete a cache access is often the factor that determines the cycle time. The effectiveness of the hierarchy in keeping the average cost of a reference down has a major impact on how close the sustained performance is to the peak performance. Small changes in the performance of the memory hierarchy cause large changes in overall system performance. The strong growth of ruse machines, whose performance is more tightly coupled to the memory hierarchy, has created increasing demand for high performance memory systems. This trend is likely to accelerate: the improvements in main memory performance will be small compared to the improvements in processor performance. This difference will lead to an increasing gap between processor cycle time and main memory access time. This gap must be closed by improving the memory hierarchy. Computer architects have attacked this gap by designing machines with cache sizes an order of magnitude larger than those appearing five years ago. Microprocessor-based RISC systems now have caches that rival the size of those in mainframes and supercomputers.

Cache and Memory Hierarchy Design: A Performance Directed Approach Steven A. Przybylski, 1991 An authoritative book for hardware and software designers. Caches are by far the simplest and most effective mechanism for improving computer performance. This innovative book exposes the characteristics of performance-optimal single and multi-level cache hierarchies by approaching the cache design process through the novel perspective of minimizing execution times. It presents useful data on the relative performance of a wide spectrum of machines and offers empirical and analytical evaluations of the underlying phenomena. This book will help computer professionals appreciate the impact of caches and enable designers to maximize performance given particular implementation constraints.

A Primer on Memory Consistency and Cache Coherence Daniel Sorin, Mark Hill, David Wood, 2011-05-10 Many modern computer systems and most multicore chips (chip multiprocessors) support shared memory in hardware. In a shared memory system, each of the processor cores may read and write to a single shared address space. For a shared memory machine, the memory consistency model defines the architecturally visible behavior of its memory system. Consistency definitions provide rules about loads and stores (or memory reads and writes) and how they act upon memory. As part of supporting a memory consistency model, many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up-to-date. The goal of this primer is to provide readers with a basic understanding of consistency and coherence. This understanding includes both the issues that must be solved as well as a variety of solutions. We present both high-level concepts as well as specific, concrete examples from real-world systems. Table of Contents: Preface / Introduction to Consistency and Coherence / Coherence Basics / Memory Consistency Motivation and Sequential Consistency / Total Store Order and the x86 Memory Model / Relaxed Memory Consistency / Coherence Protocols / Snooping Coherence Protocols / Directory Coherence Protocols / Advanced Topics in Coherence / Author Biographies

The Fenn Cache George C. Frison, Bruce A. Bradley, 1999 Co-authored by Bruce Bradley. Includes bibliography and glossary.

The Hogeys Clovis Cache Michael R. Waters, Thomas A. Jennings, 2015-03-02 Roughly thirteen thousand years ago, Clovis hunters cached more than fifty projectile points, preforms, and knives at the toe of a gentle slope near present-day Elgin, Bastrop County, in central Texas. Over the next millennia, deposition buried the cache several meters below the surface. The entombed artifacts lay undisturbed until 2003. A circuitous path brought thirteen of the original thirty-seven Clovis bifaces and points through many hands before reaching the attention of Michael Waters at Texas A&M University. At the site of the original cache, Waters and coauthor Thomas A. Jennings conducted excavations, studied the geology, and dated the geological layers to reconstruct how the cache was buried. This book provides a well-illustrated, thoroughly analyzed description and discussion of the Hogeys Clovis cache, the projectile points and other artifacts from later occupations, and the geological context of the site, which has yielded evidence of multiple Paleoindian, Archaic, and Late Prehistoric occupations. The cache of tools and weapons at Hogeys, when combined with other sites, allows us to envision a snapshot of life at the end of the last Ice Age.

Cache and Interconnect Architectures in Multiprocessors Michel Dubois, S. S. Thakkar, 1990-07-31 A collection of invited papers concerning cache coherence protocols for general interconnects. Covers the major efforts now under way to understand the architecture and performance issues of cache-based multiprocessor computer systems. Annotation copyrighted by Book News, Inc., Portland, OR

Cache a Predator, a Geocaching Mystery M. Weidenbenner, 2013-10-31 GOLD Medal WINNER in the 2014 READERS' FAVORITE INTERNATIONAL AWARDS Geocaching mystery. (Note: This novel is hidden in cache sites as a trackable around the US and Canada.) Readers' Favorite gave CACHE a PREDATOR five-stars. “I highly recommend this book to any readers who are looking for a new, excellent crime novel that is heartrending and thought-provoking. Cache a Predator is a geocaching thriller about a father’s love, justice, and the unhinged game of hide-the-cache. Officer Brett Reed will do anything to gain custody of his five-year-old daughter, Quinn. But when a judge grants Brett’s drug-addicted ex-wife custody and slaps him with a protective order for losing his temper, he fears for Quinn’s safety. Who will protect her now? When Quinn is found abandoned on the streets, the child is placed in a temporary foster home until Child Protective Services can complete an assessment. It should only take a few days. But a lot can happen in a few days. Especially when there’s a deranged psychopath on the loose, someone who’s attacking pedophiles, someone who wants to protect children like Quinn, and someone who’s planting body parts in geocaching sites. Deborah Rutkowski said, “Cache a Predator” shows how predators can slip through the system and cause fear that is unimaginable to parents, family, and the government.” Chloe Joyce said, “I couldn’t put the book down. It was non-stop action, a lovable main character and an original plot.” Jamie Gibson said, “I wanted to crawl in this book and be there to show my support. I cried, laughed, and got angry.” M. Weidenbenner plants the emotion of one vigilante's mission into the cache boxes of a gripping tale that will leave readers locking their doors and pedophiles sleeping with their eyes open.

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