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Petros Zerfos, Rebecca Montanari, Thomas Phan

Mobile Computing, Applications, and Services Petros Zerfos, Rebecca Montanari, Thomas Phan, 2010-05-11 This proceedings volume includes the full research papers presented at the First International Conference on Mobile Computing, Applications, and Services (MobiCASE) held in San Diego, California, during October 26-29, 2009. It was sponsored by ICST and held in conjunction with the First Workshop on Innovative Mobile User Interactivity (WIMUI). MobiCASE highlights state-of-the-art academic and industry research work in - main topics above the OSI transport layer with an emphasis on complete end-to-end systems and their components. Its vision is largely influenced by what we see in the consumer space today: high-end mobile phones, high-bandwidth wireless networks, novel consumer and enterprise mobile applications, scalable software infrastructures, and of course an increasingly larger user base that is moving towards an almost a- mobile lifestyle. This year's program spanned a wide range of research that explored new features, algorithms, and infrastructure related to mobile platforms. We received submissions from many countries around the world with a high number from Europe and Asia in addition to the many from North America. Each paper received at least three independent reviews from our Technical Program Committee members during the Spring of 2009, with final results coming out in July. As a result of the review process, we selected 15 high-quality papers and complemented them with six invited submissions from leading researchers, reaching the final count of 21 papers in the program.

Professional Embedded ARM Development James A. Langbridge, 2013-12-03 A practical Wrox guide to ARM programming for mobile devices With more than 90 percent of mobile phones sold in recent years using ARM-based processors, developers are eager to master this embedded technology. If you know the basics of C programming, this guide will ease you into

the world of embedded ARM technology. With clear explanations of the systems common to all ARM processors and step-by-step instructions for creating an embedded application, it prepares you for this popular specialty. While ARM technology is not new, existing books on the topic predate the current explosive growth of mobile devices using ARM and don't cover these all-important aspects. Newcomers to embedded technology will find this guide approachable and easy to understand. Covers the tools required, assembly and debugging techniques, Optimizations, and more Lists the tools needed for various types of projects and explores the details of the assembly language Examines the optimizations that can be made to ensure fast code Provides step-by-step instructions for a basic application and shows how to build upon it Professional Embedded ARM Development prepares you to enter this exciting and in-demand programming field.

ARM System Developer's Guide Andrew Sloss, Dominic Symes, Chris Wright, 2004-05-10 Over the last ten years, the ARM architecture has become one of the most pervasive architectures in the world, with more than 2 billion ARM-based processors embedded in products ranging from cell phones to automotive braking systems. A world-wide community of ARM developers in semiconductor and product design companies includes software developers, system designers and hardware engineers. To date no book has directly addressed their need to develop the system and software for an ARM-based system. This text fills that gap. This book provides a comprehensive description of the operation of the ARM core from a developer's perspective with a clear emphasis on software. It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code. Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software. The book covers both the ARM and Thumb instruction sets, covers Intel's XScale Processors, outlines distinctions among the versions of the ARM architecture,

demonstrates how to implement DSP algorithms, explains exception and interrupt handling, describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques. A final chapter looks forward to the future of the ARM architecture considering ARMv6, the latest change to the instruction set, which has been designed to improve the DSP and media processing capabilities of the architecture. * No other book describes the ARM core from a system and software perspective. * Author team combines extensive ARM software engineering experience with an in-depth knowledge of ARM developer needs. * Practical, executable code is fully explained in the book and available on the publisher's Website. * Includes a simple embedded operating system.

Embedded Systems Jason D. Bakos, 2023-10-28 Embedded Systems: ARM Programming and Optimization combines an exploration of the ARM architecture with an examination of the facilities offered by the Linux operating system to explain how various features of program design can influence processor performance. It demonstrates methods by which a programmer can optimize program code in a way that does not impact its behavior but improves its performance. Several applications, including image transformations, fractal generation, image convolution, computer vision tasks, and now machine learning, are used to describe and demonstrate these methods. From this, the reader will gain insight into computer architecture and application design, as well as gain practical knowledge in embedded software design for modern embedded systems. The second edition has been expanded to include more topics of interest to upper level undergraduate courses in embedded systems. Covers three ARM instruction set architectures, the ARMv6 and ARMv7-A, as well as three ARM cores, the ARM11 on the Raspberry Pi, Cortex-A9 on the Xilinx Zynq 7020, and Cortex-A15 on the NVIDIA Tegra K1 Describes how to fully leverage the facilities offered by the Linux operating system, including the

Linux GCC compiler toolchain and debug tools, performance monitoring support, OpenMP multicore runtime environment, video frame buffer, and video capture capabilities Designed to accompany and work with most low-cost Linux/ARM embedded development boards currently available Expanded to include coverage of topics such as bus architectures, low-power programming, and sensor interfacing Includes practical application areas such as machine learning

The Definitive Guide to ARM® Cortex®-M0 and Cortex-M0+ Processors Joseph Yiu, 2015-06-15 The Definitive Guide to the ARM® Cortex®-M0 and Cortex-M0+ Processors, Second Edition explains the architectures underneath ARM's Cortex-M0 and Cortex-M0+ processors and their programming techniques. Written by ARM's Senior Embedded Technology Manager, Joseph Yiu, the book is packed with examples on how to use the features in the Cortex-M0 and Cortex-M0+ processors. It provides detailed information on the instruction set architecture, how to use a number of popular development suites, an overview of the software development flow, and information on how to locate problems in the program code and software porting. This new edition includes the differences between the Cortex-M0 and Cortex-M0+ processors such as architectural features (e.g. unprivileged execution level, vector table relocation), new chapters on low power designs and the Memory Protection Unit (MPU), the benefits of the Cortex-M0+ processor, such as the new single cycle I/O interface, higher energy efficiency, better performance and the Micro Trace Buffer (MTB) feature, updated software development tools, updated Real Time Operating System examples using Keil™ RTX with CMSIS-RTOS APIs, examples of using various Cortex-M0 and Cortex-M0+ based microcontrollers, and much more. Provides detailed information on ARM® Cortex®-M0 and Cortex-M0+ Processors, including their architectures, programming model, instruction set, and interrupt handling Presents detailed information on the differences

between the Cortex-M0 and Cortex-M0+ processors Covers software development flow, including examples for various development tools in both C and assembly languages Includes in-depth coverage of design approaches and considerations for developing ultra low power embedded systems, the benchmark for energy efficiency in microcontrollers, and examples of utilizing low power features in microcontrollers

Essential IOS Build and Release Ron Roche, 2012 A comprehensive guide to building, packaging, and distribution--Cover.

Embedded Systems: An Integrated Approach LyLa B. Das, Embedded Systems: An Integrated Approach is exclusively designed for the undergraduate courses in electronics and communication engineering as well as computer science engineering. This book is well-structured and covers all the important processors and their applications in a sequential manner. It begins with a highlight on the building blocks of the embedded systems, moves on to discuss the software aspects and new processors and finally concludes with an insightful study of important applications. This book also contains an entire part dedicated to the ARM processor, its software requirements and the programming languages. Relevant case studies and examples supplement the main discussions in the text.

The Definitive Guide to ARM® Cortex®-M3 and Cortex®-M4 Processors Joseph Yiu, 2013-10-06 This new edition has been fully revised and updated to include extensive information on the ARM Cortex-M4 processor, providing a complete up-to-date guide to both Cortex-M3 and Cortex-M4 processors, and which enables migration from various processor architectures to the exciting world of the Cortex-M3 and M4. This book presents the background of the ARM architecture and outlines the features of the processors such as the instruction set, interrupt-handling and also demonstrates how to program and utilize the advanced features available such as the Memory Protection Unit (MPU).

Chapters on getting started with IAR, Keil, gcc and CoCoX CoIDE tools help beginners develop program codes. Coverage also includes the important areas of software development such as using the low power features, handling information input/output, mixed language projects with assembly and C, and other advanced topics. Two new chapters on DSP features and CMSIS-DSP software libraries, covering DSP fundamentals and how to write DSP software for the Cortex-M4 processor, including examples of using the CMSIS-DSP library, as well as useful information about the DSP capability of the Cortex-M4 processor A new chapter on the Cortex-M4 floating point unit and how to use it A new chapter on using embedded OS (based on CMSIS-RTOS), as well as details of processor features to support OS operations Various debugging techniques as well as a troubleshooting guide in the appendix topics on software porting from other architectures A full range of easy-to-understand examples, diagrams and quick reference appendices

Professional Windows Embedded Compact 7 Samuel

Phung,David Jones,Thierry Joubert,2011-08-15 Learn to program an array of customized devices and solutions As a compact, highly efficient, scalable operating system, Windows Embedded Compact 7 (WEC7) is one of the best options for developing a new generation of network-enabled, media-rich, and service-oriented devices. This in-depth resource takes you through the benefits and capabilities of WEC7 so that you can start using this performance development platform today. Divided into several major sections, the book begins with an introduction and then moves on to coverage of OS design, application development, advanced application development, how to deploy WEC7 devices, and more. Examines the benefits of Windows Embedded Compact 7 (WEC7) Reviews the various elements of OS design, including configuring and building a customized OS runtime image, using debugging and remote tools, and more Explains how to develop native code applications with Visual Studio 2010, develop

database applications with SQL server compact, and use the application deployment option Discusses how to deploy a WEC device, use the boot loader, launch WEC using BIOSLoader, and deploy a WEC power toy If you're interested in learning more about embedded development or you're seeking a higher performance development platform, then this is the book for you.

The Definitive Guide to the ARM Cortex-M3 Joseph Yiu, 2009-11-19 This user's guide does far more than simply outline the ARM Cortex-M3 CPU features; it explains step-by-step how to program and implement the processor in real-world designs. It teaches readers how to utilize the complete and thumb instruction sets in order to obtain the best functionality, efficiency, and reuseability. The author, an ARM engineer who helped develop the core, provides many examples and diagrams that aid understanding. Quick reference appendices make locating specific details a snap! Whole chapters are dedicated to: Debugging using the new CoreSight technology Migrating effectively from the ARM7 The Memory Protection Unit Interfaces, Exceptions, Interrupts ...and much more! The only available guide to programming and using the groundbreaking ARM Cortex-M3 processor Easy-to-understand examples, diagrams, quick reference appendices, full instruction and Thumb-2 instruction sets are included T teaches end users how to start from the ground up with the M3, and how to migrate from the ARM7

The Designer's Guide to the Cortex-M Processor Family Trevor Martin, 2016-06-06 The Designer's Guide to the Cortex-M Microcontrollers gives you an easy-to-understand introduction to the concepts required to develop programs in C with a Cortex-M based microcontroller. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling you to easily develop basic C programs to run on the Cortex-M0/M0+/M3 and M4 and M7. It then examines the more advanced features of the Cortex

architecture such as memory protection, operating modes, and dual stack operation. Once a firm grounding in the Cortex-M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS-DSP library. The book also examines techniques for software testing and code reuse specific to Cortex-M microcontrollers. With this book you will learn: the key differences between the Cortex-M0/M0+/M3 and M4 and M7; how to write C programs to run on Cortex-M based processors; how to make the best use of the CoreSight debug system; the Cortex-M operating modes and memory protection; advanced software techniques that can be used on Cortex-M microcontrollers; how to use a Real Time Operating System with Cortex-M devices; how to optimize DSP code for the Cortex-M4; and how to build real time DSP systems. Includes an update to the latest version (5) of MDK-ARM, which introduces the concept of using software device packs and software components Includes overviews of the new CMSIS specifications Covers developing software with CMSIS-RTOS showing how to use RTOS in a real world design Provides a new chapter on the Cortex-M7 architecture covering all the new features Includes a new chapter covering test driven development for Cortex-M microcontrollers Features a new chapter on creating software components with CMSIS-Pack and device abstraction with CMSIS-Driver Features a new chapter providing an overview of the ARMv8-M architecture including the TrustZone hardware security model

The Definitive Guide to the ARM Cortex-M0 Joseph Yiu, 2011-04-04 The Definitive Guide to the ARM Cortex-M0 is a guide for users of ARM Cortex-M0 microcontrollers. It presents many examples to make it easy for novice embedded-software developers to use the full 32-bit ARM Cortex-M0 processor. It provides an overview of ARM and ARM processors and discusses the benefits of ARM Cortex-M0 over 8-bit or 16-bit devices in terms of energy efficiency, code density, and ease of use, as well as their features and applications. The book describes the

architecture of the Cortex-M0 processor and the programmers model, as well as Cortex-M0 programming and instruction set and how these instructions are used to carry out various operations. Furthermore, it considers how the memory architecture of the Cortex-M0 processor affects software development; Nested Vectored Interrupt Controller (NVIC) and the features it supports, including flexible interrupt management, nested interrupt support, vectored exception entry, and interrupt masking; and Cortex-M0 features that target the embedded operating system. It also explains how to develop simple applications on the Cortex-M0, how to program the Cortex-M0 microcontrollers in assembly and mixed-assembly languages, and how the low-power features of the Cortex-M0 processor are used in programming. Finally, it describes a number of ARM Cortex-M0 products, such as microcontrollers, development boards, starter kits, and development suites. This book will be useful to both new and advanced users of ARM Cortex devices, from students and hobbyists to researchers, professional embedded- software developers, electronic enthusiasts, and even semiconductor product designers. The first and definitive book on the new ARM Cortex-M0 architecture targeting the large 8-bit and 16-bit microcontroller market Explains the Cortex-M0 architecture and how to program it using practical examples Written by an engineer at ARM who was heavily involved in its development

Definitive Guide to Arm Cortex-M23 and Cortex-M33

Processors Joseph Yiu, 2020-12-01 The Definitive Guide to Arm® Cortex®-M23 and Cortex-M33 Processors focuses on the Armv8-M architecture and the features that are available in the Cortex-M23 and Cortex- M33 processors. This book covers a range of topics, including the instruction set, the programmer's model, interrupt handling, OS support, and debug features. It demonstrates how to create software for the Cortex-M23 and Cortex-M33 processors by way of a range of examples, which will enable embedded software developers to understand the Armv8-

M architecture. This book also covers the TrustZone® technology in detail, including how it benefits security in IoT applications, its operations, how the technology affects the processor's hardware (e.g., memory architecture, interrupt handling, etc.), and various other considerations in creating secure software. Presents the first book on Armv8-M Architecture and its features as implemented in the Cortex-M23 and Cortex-M33 processors. Covers TrustZone technology in detail. Includes examples showing how to create software for Cortex-M23/M33 processors.

Developing C# Apps for iPhone and iPad using

MonoTouch Bryan Costanich, 2011-08-05. Developing C# Applications for iPhone and iPad using MonoTouch shows you how to use your existing C# skills to write apps for the iPhone and iPad. Fortunately, there's MonoTouch, Novell's .NET library that allows C# developers to write C# code that executes in iOS. Furthermore, MonoTouch allows you to address all the unique functions of the iPhone, iPod Touch, and iPad. And the big plus: You needn't learn any Objective-C to master MonoTouch! Former Microsoft engineer and published app-store developer Bryan Costanich shows you how to use the tools you already know to create native apps in iOS using C# and the .NET Base Class Libraries. The magic is in Novell's implementation of Apple's Cocoa libraries in MonoTouch. You'll master the same elegant and rich Cocoa environment, but without the need to learn a new programming language. Developing C# Applications for iPhone and iPad using MonoTouch takes you from your first Hello, World example through the major APIs and features of iOS. The coverage is comprehensive and makes use of frequent examples, complete with sample code you can download and reuse to create your own powerful and playful apps.

Hacking and Securing IOS Applications Jonathan

Zdziarski, 2012. Explores hacking the iPhone and iPad; provides practical information on specific security threats; and presents a discussion of code level countermeasures for implementing

security.

Foundations and Practice of Security Frédéric

Cuppens, Lingyu Wang, Nora Cuppens-Boulahia, Nadia Tawbi, Joaquin Garcia-Alfaro, 2016-12-28 This book constitutes the thoroughly refereed post-conference proceedings of the 9th International Symposium on Foundations and Practice of Security, FPS 2016, held in Québec City, QC, Canada, in October 2016. The 18 revised regular papers presented together with 5 short papers and 3 invited talks were carefully reviewed and selected from 34 submissions. The accepted papers cover diverse research themes, ranging from classic topics, such as malware, anomaly detection, and privacy, to emerging issues, such as security and privacy in mobile computing and cloud.

Yocto for Raspberry Pi Pierre-Jean Texier, Petter

Mabacker, 2016-06-29 Create unique and amazing projects by using the powerful combination of Yocto and Raspberry Pi About This Book Set up and configure the Yocto Project efficiently with Raspberry Pi Deploy multimedia applications from existing Yocto/OE layers An easy-to-follow guide to utilize your custom recipes on your Raspberry Pi Who This Book Is For If you are a student or a developer of embedded software, embedded Linux engineer or embedded systems in competence with Raspberry Pi and want to discover the Yocto Project, then this book is for you. Experience with Yocto is not needed. What You Will Learn Explore the basic concept of Yocto's build system and how it is organized in order to use it efficiently with Raspberry Pi Generate your first image with Yocto for the Raspberry Pi Understand how to customize your Linux kernel within the Yocto Project Customize your image in order to integrate your own applications Write your own recipes for your graphical applications Integrate a custom layer for the Raspberry Pi In Detail The Yocto Project is a Linux Foundation workgroup, which produces tools (SDK) and processes (configuration, compilation, installation) that will enable the creation of Linux distributions for embedded software,

independent of the architecture of embedded software (Raspberry Pi, i.MX6, and so on). It is a powerful build system that allows you to master your personal or professional development. This book presents you with the configuration of the Yocto Framework for the Raspberry Pi, allowing you to create amazing and innovative projects using the Yocto/OpenEmbedded eco-system. It starts with the basic introduction of Yocto's build system, and takes you through the setup and deployment steps for Yocto. It then helps you to develop an understanding of Bitbake (the task scheduler), and learn how to create a basic recipe through a GPIO application example. You can then explore the different types of Yocto recipe elements (LICENSE, FILES, SRC_URI, and so on). Next, you will learn how to customize existing recipes in Yocto/OE layers and add layers to your custom environment (qt5 for example). Style and approach A step by step guide covering the fundamentals to create amazing new projects with Raspberry Pi and Yocto.

More iPhone Cool Projects Ben Smith, Danton Chin, Leon Palm, Dave Smith, Charles Smith, Claus Hoefele, Saul Mora, Arne de Vries, Joost van de Wijgerd, Scott Penberthy, Ben Kazez, Roderick Smith, Stephen Chin, 2010-07-30 Everyone is developing iPhone applications, and it's clear why. The iPhone is the coolest mobile device available, and the App Store makes it simple to get an application out into the unstoppable iPhone app market. With hundreds of thousands of app developers entering the game, it's crucial to learn from those who have actually succeeded. This book shows you how some of the most innovative and creative iPhone application developers have developed cool, best-selling apps. Not only does every successful application have a story, but behind every great app is excellent code. In this book, you'll see the code and learn how to use it to make your own cool applications. You'll learn everything from importing 3D art assets into your iPhone game to using Cocos2d for iPhone and iPad. This book shares the secrets of the coolest iPhone apps being built today by the best iPhone developers—invaluable knowledge for

anyone who wants to create the app that everyone is talking about.

Symbian OS Internals Jane Sales, 2005-12-13 Take a look inside Symbian OS with an under-the-hood view of Symbian's revolutionary new real-time smartphone kernel Describes the functioning of the new real-time kernel, which will become ubiquitous on Symbian OS phones in the next 5-10 years Will benefit the base-porting engineer by providing a more solid understanding of the OS being ported Contains an in-depth explanation of how Symbian OS drivers work. Device drivers have changed considerably with the introduction of a single code - this book helps those converting them to the new kernel The book has broad appeal and is relevant to all who work with Symbian OS at a low level, whatever Symbian OS they are targeting Written by the engineers who actually designed and built the real-time kernel

Electronic Engineering Design ,2002

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