Virtual Processor

Scott Vetter, An Ding Chen, Dave Freeman, Breno Henrique Leitao, IBM Redbooks

Embedded Processor Design Challenges Ed F. Deprettere, Stamatis Vassiliadis, 2003-07-31 This textbook is intended to give an introduction to and an overview of sta- of-the-art techniques in the design of complex embedded systems. The book title is SAMOS for two major reasons. First, it tries to focus on the actual distinct, yet important problem ?elds of System-Level design of embedded systems, including mapping techniques and synthesis, Architectural design, Modeling issues such as speci?cation languages, formal models, and- nallySimulation. The second reason is that the volume includes a number of papers presented at a workshop with the same name on the Island of Samos, Greece, in July 2001. In order to receive international attention, a number of reputed researchers were invited to this workshop to present their current work. Participation was by invitation only. For the volume presented here, a number of additional papers where selected based on a call for papers. All contributions were refereed. This volume presents a selection of 18 of the refereed papers, including 2 invited papers. The textbook is organized according to four topics: The ?rst isA)System-LevelDesignandSimulation.Inthissection,wepresentac ollectionofpapers that give an overview of the challenging goal to design and explore alternatives of embedded system implementations at the system-level. One paper gives an overview of models and tools used in system-level design. The other papers present new models to describe applications, provide models for re?nement and

design space exploration, and for tradeo? analysis between cost and ?exibility of an implementation.

Languages and Compilers for Parallel Computing Utpal Banerjee, Alex Nicolau, 1994-01-28 This book contains papers selected for presentation at the Sixth Annual Workshop on Languages and Compilers for Parallel Computing. The workshop washosted by the Oregon Graduate Institute of Science and Technology. All the major research efforts in parallel languages and compilers are represented in this workshop series. The 36 papers in the volume aregrouped under nine headings: dynamic data structures, parallel languages, High Performance Fortran, loop transformation, logic and dataflow language implementations, fine grain parallelism, scalar analysis, parallelizing compilers, and analysis of parallel programs. The book represents a valuable snapshot of the state of research in the field in 1993.

Mastering Windows Server 2016 Hyper-V John
Savill,2016-11-29 Build a seamless, flexible,
full-service datacenter solution Microsoft Windows
Server 2016 Hyper-V is the IT administrator's
guide to this rising datacenter solution. Hyper-V
has already surpassed VMWare in datacenter
management, identity service for multiple devices,
and more; this book shows you how to harness the
power of this hypervisor to simplify the
infrastructure, reduce costs, improve
productivity, and better manage system resources.
From a tour of the technology through
architecture, deployment, and integration of
System Center, Microsoft Azure, and Microsoft
Azure Stack, the discussion illustrates the skills

you need to create a complete solution for optimum enterprise management. Coverage includes Windows Azure capabilities for virtual machines, managing a hybrid cloud, IaaS, storage capabilities, PowerShell, and more, with practical real-world guidance from a leading authority in the field. Hyper-V has recently undergone improvements in scalability and features that have positioned it as an ideal solution in the Small/Medium Business and Enterprise markets. This book shows you how to exploit these new capabilities to build a robust data solution for your organization. Discover the capabilities of Microsoft Hyper-V Architect a Hyper-V datacenter solution Plan and manage a deployment or migration Integrate complementary technologies for full scalability Data is everywhere—on desktops, laptops, phones, and multiple operating systems, accessed through email, text messages, web searches, online services, and more. All of this data must be stored, accessible, updated, backed up, secured, managed, sorted, and analyzed—sometimes instantly. Hyper-V is the rising star in the virtualization space, and Microsoft Windows Server 2016 Hyper-V shows you how to turn greater capabilities into better datacenter solutions.

STACS 92 Alain Finkel, Matthias
Jantzen, 1992-02-04 This volume gives the
proceedings of the ninth Symposium on Theoretical
Aspects of Computer Science (STACS). This annual
symposium is held alternately in France and
Germany and is organized jointly by the Special
Interest Group for Fundamental Computer Science of
the Association Francaise des Sciences et

Technologies de l'Information et des Syst mes (AFCET) and the Special Interest Group for Theoretical Computer Science of the Gesellschaft f}r Informatik (GI). The volume includes three invited lectures and sections on parallel algorithms, logic and semantics, computational geometry, automata and languages, structural complexity, computational geometry and learning theory, complexity and communication, distributed systems, complexity, algorithms, cryptography, VLSI, words and rewriting, and systems.

IBM PowerVM Virtualization Active Memory Sharing Allyson Brito, Loïc Fura, Bartlomiej Grabowski, IBM Redbooks, 2012-11-14 This IBM® RedpaperTM publication introduces PowerVMTM Active MemoryTM Sharing on IBM Power SystemsTM based on POWER6® and later processor technology. Active Memory Sharing is a virtualization technology that allows multiple partitions to share a pool of physical memory. This is designed to increase system memory utilization, thereby enabling you to realize a cost benefit by reducing the amount of physical memory required. The paper provides an overview of Active Memory Sharing, and then demonstrates, in detail, how the technology works and in what scenarios it can be used. It also contains chapters that describe how to configure, manage and migrate to Active Memory Sharing based on hands-on examples. The paper is targeted to both architects and consultants who need to understand how the technology works to design solutions, and to technical specialists in charge of setting up and managing Active Memory Sharing environments. For performance related information, see:

ftp://ftp.software.ibm.com/common/ssi/sa/wh/n/pow0
3017usen/POW03017USEN.PDF

DB2 10 for Linux on System z Using z/VM v6.2, Single System Image Clusters and Live Guest Relocation Lydia Parziale, Dan Davenport, Eduardo Kienetz, Tito Ogando, Manoj Srinivasan Pattabhiraman, Srivatsan Venkatesan, IBM Redbooks, 2012-11-03 IBM® z/VM® 6.2 introduced significant changes to z/VM with a multi-system clustering technology that allows up to four z/VM instances in a single system image (SSI) cluster. This technology is important because it offers you an attractive alternative to vertical growth by adding new z/VM systems. In the past, this capability required duplicate efforts to install, maintain, and manage each system. With SSI, these duplicate efforts are reduced or eliminated. Support for live guest relocation (LGR) allows you to move Linux virtual servers without disrupting your business or incurring loss of service, thus reducing planned outages. The z/VM systems are aware of each other and take advantage of their combined resources. LGR enables you to relocate quests from a system requiring maintenance to a system that will remain active during maintenance. A major advantage for DB2 v10 customers is that using z/VM 6.2 does not require any changes to existing DB2 structures. This remarkable benefit is due to the fact that DB2 v10 is installed as part of the LInux quest on z/VM and is fully integrated into LGR. This allows you to smoothly move DB2 v10 when you move Linux virtual servers, without interrupting either DB2 v10 or z/VM operations and services. This IBM Redbooks®

publication will help you understand how DB2 10 on Linux for System z® behaves while running on a z/VM that is being relocated using z/VM's 6.2 Live Guest Relocation feature. In this book, we explore memory management, the DB2 Self-tuning memory manager feature, time synchronization, networking, and storage and performance considerations with regards to relocation. We also offer some best practices found during a live guest relocation for DB2 v10.

IBM PowerVM Virtualization Introduction and Configuration Scott Vetter, Mel Cordero, Lucio Correia, Hai Lin, Vamshikrishna Thatikonda, Rodrigo Xavier, IBM Redbooks, 2015-11-24 This IBM® Redbooks® publication provides an introduction to PowerVMTM virtualization technologies on Power System servers. PowerVM is a combination of hardware. firmware, and software that provides CPU, network, and disk virtualization. These are the main virtualization technologies: POWER7, POWER6, and POWER5 hardware POWER Hypervisor Virtual I/O Server Though the PowerVM brand includes partitioning, management software, and other offerings, this publication focuses on the virtualization technologies that are part of the PowerVM Standard and Enterprise Editions. This publication is also designed to be an introduction quide for system administrators, providing instructions for these tasks: Configuration and creation of partitions and resources on the HMC Installation and configuration of the Virtual I/O Server Creation and installation of virtualized partitions Examples using AIX, IBM i, and Linux This edition has been updated with the latest

updates available and an improved content organization.

Best Practices for DB2 on AIX 6.1 for POWER Systems Rakesh Dash, Bernard Goelen, Vasfi Gucer, Rajesh K Jeyapaul, Sunil Kamath, Naveen Kumar Bharatha, Michael Kwok, Anupama Padmanabhan, Punit Shah, Basker Shanmugam, Sweta Singh, Amar Thakkar, Adriana Zubiri, IBM Redbooks, 2015-08-27 This IBM® Redbooks® publication presents a best practices guide for DB2® and InfoSphereTM Warehouse performance on a AIX® 6L with Power SystemsTM virtualization environment. It covers Power hardware features such as PowerVMTM, multipage support, Reliability, Availability, and Serviceability (RAS) and how to best exploit them with DB2 LUW workloads for both transactional and data warehousing systems. The popularity and reach of DB2 and InfoSphere Warehouse has grown in recent years. Enterprises are relying more on these products for their mission-critical transactional and data warehousing workloads. It is critical that these products be supported by an adequately planned infrastructure. This publication offers a reference architecture to build a DB2 solution for transactional and data warehousing workloads using the rich features offered by Power systems. IBM Power Systems have been leading players in the server industry for decades. Power Systems provide great performance while delivering reliability and flexibility to the infrastructure. This book presents a reference architecture to build a DB2 solution for transactional and data warehousing workloads using the rich features offered by Power systems. It

aims to demonstrate the benefits DB2 and InfoSphere Warehouse can derive from a Power Systems infrastructure and how Power Systems support these products. The book is intended as a guide for a Power Systems specialist to understand the DB2 and InfoSphere Warehouse environment and for a DB2 and InfoSphere Warehouse specialist to understand the facilities available for Power Systems supporting these products.

DB2 Virtualization Whei-Jen Chen, Jason Chan, Olaf Mueller, Malcolm Singh, Tapio Väättänen, IBM Redbooks, 2009-11-25 Server virtualization technologies are becoming more popular to help efficiently utilize resources by consolidating servers. IBM® , the first company that developed and made available the virtual technology in 1966, offers advanced, powerful, reliable, and costsaving virtualization technologies in various hardware and software products including DB2® for Linux, UNIX, and Windows. This IBM Redbooks® publication describes using IBM DB2 9 with server virtualization. We start with a general overview of virtualization and describe specific server virtualization technologies to highlight how the server virtualization technologies have been implemented. With this introduction anyone new to virtualization will have a better understanding of server virtualization and the industry server virtualization technologies available in the market. Following the virtualization concept, we describe in detail the setup, configuration, and managing of DB2 with three leading server virtualization technologies: IBM Power SystemsTM with PowerVMTM VMware Hyper-V We discuss the

virtual machine setup with DB2 in mind to help IT support understand the effective ways of setting up a virtual environment specific for DB2. We explain the architecture and components of these three server virtualization technologies to allow DBAs to understand how a database environment using DB2 can benefit from using the server virtualization technologies. In addition, we discuss the DB2 features and functions that can take advantage of using server virtualization. These features are put into practice when describing how to set up DB2 with the three virtualization technologies discussed in this book. This book also includes a list of best practices from the various tests performed while using these virtualization technologies. These best practices can be used as a guideline or a reference when setting up DB2 using these virtualization technologies.

Analysis, Architectures and Modelling of Embedded Systems Achim Rettberg, Mauro C. Zanella, Michael Amann, Michael Keckeisen, Franz J. Rammig, 2009-09-19 This book presents the technical program of the International Embedded Systems Symposium (IESS) 2009. Timely topics, techniques and trends in embedded system design are covered by the chapters in this volume, including modelling, simulation, verification, test, scheduling, platforms and processors. Particular emphasis is paid to automotive systems and wireless sensor networks. Sets of actual case studies in the area of embedded system design are also included. Over recent years, embedded systems have gained an enormous amount of proce- ing power

and functionality and now enter numerous application areas, due to the fact that many of the formerly external components can now be integrated into a single System-on-Chip. This tendency has resulted in a dramatic reduction in the size and cost of embedded systems. As a unique technology, the design of embedded systems is an essential element of many innovations. Embedded systems meet their performance goals, including real-time constraints, through a combination of special-purpose hardware and software components tailored to the system requirements. Both the development of new features and the reuse of existing intellectual property components are essential to keeping up with ever more demanding customer requirements. Furthermore, design complexities are steadily growing with an increasing number of components that have to cooperate properly. Embedded system designers have to cope with multiple goals and constraints simulneously, including timing, power, reliability, dependability, maintenance, packaging and, last but not least, price.

Operating Systems (Self Edition 1.1.Abridged)
Sibsankar Haldar,2016-05-29 Some previous editions
of this book were published from Pearson Education
(ISBN 9788131730225). This book, designed for
those who are taking introductory courses on
operating systems, presents both theoretical and
practical aspects of modern operating systems.
Although the emphasis is on theory, while exposing
you (the reader) the subject matter, this book
maintains a balance between theory and practice.
The theories and technologies that have fueled the

evolution of operating systems are primarily geared towards two goals: user convenience in maneuvering computers and efficient utilization of hardware resources. This book also discusses many fundamental concepts that have been formulated over the past several decades and that continue to be used in many modern operating systems. In addition, this book also discusses those technologies that prevail in many modern operating systems such as UNIX, Solaris, Linux, and Windows. While the former two have been used to present many in-text examples, the latter two are dealt with as separate technological case studies. They highlight the various issues in the design and development of operating systems and help you correlate theories to technologies. This book also discusses Android exposing you a modern software platform for embedded devices. This book supersedes ISBN 9788131730225 and its other derivatives, from Pearson Education India. (They have been used as textbooks in many schools worldwide.) You will definitely love this self edition, and you can use this as a textbook in undergraduate-level operating systems courses.

VMware ESX Essentials in the Virtual Data Center David Marshall, Stephen S. Beaver, Jason W. McCarty, 2008-08-25 Flexible and efficient, VMware ESX is the tool of choice for enterprise data centers looking to make the most of the latest virtualization methods. However, to date, no single manual provides users with a systematic way to understand and take full advantage of all its features and options. Novel Solutions for Every Level of the IT Chain VMware ESX Es

IBM Power Systems S922, S914, and S924 Technical Overview and Introduction Scott Vetter, Young Hoon Cho.Gareth Coates, Bartlomiej Grabowski, Volker Haug, IBM Redbooks, 2022-08-26 This IBM® RedpaperTM publication is a comprehensive guide that covers the IBM Power System S922 (9009-22A), IBM Power System S914 (9009-41A), and IBM Power System S924 (9009-42A) servers that support IBM AIX®, IBM i, and Linux operating systems. The objective of this paper is to introduce the major innovative Power S914, Power S922, and Power 924 offerings and their relevant functions: The new IBM POWER9TM processor, which is available at frequencies of 2.3 - 3.8 GHz, 2.8 - 3.8 GHz, 2.9 - 3.8 GHz, 3.4 -3.9 GHz, 3.5 - 3.9 GHz, and 3.8 - 4.0 GHz. Significantly strengthened cores and larger caches. Two integrated memory controllers that double the memory footprint of IBM POWER8® servers. Integrated I/O subsystem and hotpluggable Peripheral Component Interconnect Express (PCIe) Gen4 and Gen3 I/O slots. I/O drawer expansion options offer greater flexibility. Support for Coherent Accelerator Processor Interface (CAPI) 2.0. New IBM EnergyScaleTM technology offers new variable processor frequency modes that provide a significant performance boost beyond the static nominal frequency. This publication is for professionals who want to acquire a better understanding of IBM Power SystemsTM products. The intended audience includes the following roles: Clients Sales and marketing professionals Technical support professionals IBM Business Partners Independent software vendors (ISVs) This paper expands the current set of IBM

Power Systems documentation by providing a desktop reference that offers a detailed technical description of the Power S914, Power S922, and Power S924 systems. This paper does not replace the current marketing materials and configuration tools. It is intended as an extra source of information that, together with existing sources, can be used to enhance your knowledge of IBM server solutions.

Self-Stabilizing Systems Calif.) Sss 200 (2003 San Francisco, Shing-Tsaan Huang, 2003-06-11 This book constitutes the refereed proceedings of the 6th International Symposium on Self-Stabilizing Systems, SSS 2003, held in San Francisco, CA, USA, in June 2003. The 15 revised full papers presented were carefully reviewed and selected from 27 submissions. The papers address self-stabilization issues for various types of systems and software including communication protocols, sensor networks, biological systems, and directed networks; several new algorithms are presented.

IBM Power 770 and 780 Technical Overview and Introduction Scott Vetter, An Ding Chen, Dave Freeman, Breno Henrique Leitao, IBM Redbooks, 2013-06-06 This IBM® RedpaperTM publication is a comprehensive guide covering the IBM Power 770 (9117-MMD) and Power 780 (9179-MHD) servers that support IBM AIX®, IBM i, and Linux operating systems. The goal of this paper is to introduce the major innovative Power 770 and 780 offerings and their prominent functions: The IBM POWER7+TM processor, available at frequencies of 3.8 GHz and 4.2 GHz for the Power 770 and 3.7 GHz and 4.4 GHz for the Power 780 The specialized IBM

POWER7+ Level 3 cache that provides greater bandwidth, capacity, and reliability The 1 Gb or 10 Gb Integrated Multifunction Card that provides two USB ports, one serial port, and four Ethernet connectors for a processor enclosure and does not require a PCI slot The Active MemoryTM Mirroring (AMM) for Hypervisor feature that mirrors the main memory used by the firmware IBM PowerVM® virtualization, including PowerVM Live Partition Mobility and PowerVM Active Memory Sharing Active Memory Expansion that provides more usable memory than what is physically installed on the system IBM EnergyScaleTM technology that provides features such as power trending, power-saving, capping of power, and thermal measurement Enterprise-ready reliability, serviceability, and availability Dynamic Platform Optimizer Highperformance SSD drawer Professionals who want to acquire a better understanding of IBM Power SystemsTM products can benefit from reading this paper.

IBM Power System E850 Technical Overview and Introduction Scott Vetter, Volker Haug, Andrew Laidlaw, Seulgi Yoppy Sung, IBM Redbooks, 2017-04-25 This IBM® RedpaperTM publication is a comprehensive guide covering the IBM Power System E850 (8408-E8E) server that supports IBM AIX®, and Linux operating systems. The objective of this paper is to introduce the major innovative Power E850 offerings and their relevant functions: The new IBM POWER8TM processor, available at frequencies of 3.02 GHz, 3.35 GHz, and 3.72 GHz Significantly strengthened cores and larger caches Two integrated memory controllers with improved

latency and bandwidth Integrated I/O subsystem and hot-pluggable PCIe Gen3 I/O slots I/O drawer expansion options offer greater flexibility Improved reliability, serviceability, and availability (RAS) functions IBM EnergyScaleTM technology that provides features such as power trending, power-saving, capping of power, and thermal measurement This publication is for professionals who want to acquire a better understanding of IBM Power SystemsTM products. The intended audience includes the following roles: Clients Sales and marketing professionals Technical support professionals IBM Business Partners Independent software vendors This paper expands the current set of IBM Power Systems documentation by providing a desktop reference that offers a detailed technical description of the Power E850 system. This paper does not replace the latest marketing materials and configuration tools. It is intended as an additional source of information that, together with existing sources, can be used to enhance your knowledge of IBM server solutions.

Performance Optimization and Tuning Techniques for IBM Power Systems Processors Including IBM POWER8 Brian Hall, Peter Bergner, Alon Shalev Housfater, Madhusudanan Kandasamy, Tulio Magno, Alex Mericas, Steve Munroe, Mauricio Oliveira, Bill Schmidt, Will Schmidt, Bernard King Smith, Julian Wang, Suresh Warrier, David Wendt, IBM Redbooks, 2017-03-31 This IBM® Redbooks® publication focuses on gathering the correct technical information, and laying out simple guidance for optimizing code performance on IBM

POWER8® processor-based systems that run the IBM AIX®, IBM i, or Linux operating systems. There is straightforward performance optimization that can be performed with a minimum of effort and without extensive previous experience or in-depth knowledge. The POWER8 processor contains many new and important performance features, such as support for eight hardware threads in each core and support for transactional memory. The POWER8 processor is a strict superset of the IBM POWER7+TM processor, and so all of the performance features of the POWER7+ processor, such as multiple page sizes, also appear in the POWER8 processor. Much of the technical information and quidance for optimizing performance on POWER8 processors that is presented in this guide also applies to POWER7+ and earlier processors, except where the guide explicitly indicates that a feature is new in the POWER8 processor. This guide strives to focus on optimizations that tend to be positive across a broad set of IBM POWER® processor chips and systems. Specific guidance is given for the POWER8 processor; however, the general guidance is applicable to the IBM POWER7+, IBM POWER7®, IBM POWER6®, IBM POWER5, and even to earlier processors. This guide is directed at personnel who are responsible for performing migration and implementation activities on POWER8 processor-based systems. This includes system administrators, system architects, network administrators, information architects, and database administrators (DBAs).

IBM Power Systems Performance Guide:
Implementing and Optimizing Dino

Ouintero, Sebastien Chabrolles, Chi Hui Chen, Murali Dhandapani, Talor Holloway, Chandrakant Jadhay, Sae Kee Kim, Sijo Kurian, Bharath Raj, Ronan Resende, Bjorn Roden, Niranjan Srinivasan, Richard Wale, William Zanatta, Zhi Zhang, IBM Redbooks, 2013-05-01 This IBM® Redbooks® publication addresses performance tuning topics to help leverage the virtualization strengths of the POWER® platform to solve clients' system resource utilization challenges, and maximize system throughput and capacity. We examine the performance monitoring tools, utilities, documentation, and other resources available to help technical teams provide optimized business solutions and support for applications running on IBM POWER systems' virtualized environments. The book offers application performance examples deployed on IBM Power SystemsTM utilizing performance monitoring tools to leverage the comprehensive set of POWER virtualization features: Logical Partitions (LPARs), micropartitioning, active memory sharing, workload partitions, and more. We provide a well-defined and documented performance tuning model in a POWER system virtualized environment to help you plan a foundation for scaling, capacity, and optimization . This book targets technical professionals (technical consultants, technical support staff, IT Architects, and IT Specialists) responsible for providing solutions and support on IBM POWER systems, including performance tuning.

Algorithms and Parallel VLSI Architectures III M. Moonen, F. Catthoor, 1995-03-16 A comprehensive overview of the current evolution of research in

algorithms, architectures and compilation for parallel systems is provided by this publication. The contributions focus specifically on domains where embedded systems are required, either oriented to application-specific or to programmable realisations. These are crucial in domains such as audio, telecom, instrumentation, speech, robotics, medical and automotive processing, image and video processing, TV, multimedia, radar and sonar. The book will be of particular interest to the academic community because of the detailed descriptions of research results presented. In addition, many contributions feature the real-life applications that are responsible for driving research and the impact of their specific characteristics on the methodologies is assessed. The publication will also be of considerable value to senior design engineers and CAD managers in the industrial arena, who wish either to anticipate the evolution of commercially available design tools or to utilize the presented concepts in their own R&D programmes.

Power Systems Enterprise Servers with PowerVM Virtualization and RAS Dino Quintero, JinHoon Baek, Guillermo Diez, Hassan Elsetohy, Debra Francis, Bing He, Rajesh Jeyapaul, Anil Kalavakolanu, Tejaswini Kaujalgi, David Kgabo, Ricardo Puig, Vani Ramagiri, IBM Redbooks, 2013-03-06 This IBM® Redbooks® publication illustrates implementation, testing, and helpful scenarios with IBM Power® Systems 780 and 795 using the comprehensive set of the Power virtualization features. We focus on the Power

Systems functional improvements, in particular, highlighting the reliability, availability, and serviceability (RAS) features of the enterprise servers. This document highlights IBM Power Systems Enterprise Server features, such as system scalability, virtualization features, and logical partitioning among others. This book provides a documented deployment model for Power 780 and Power 795 within a virtualized environment, which allows clients to plan a foundation for exploiting and using the latest features of the IBM Power Systems Enterprise Servers. The target audience for this book includes technical professionals (IT consultants, technical support staff, IT Architects, and IT Specialists) responsible for providing IBM Power Systems solutions and support.

Adopting the Beat of Term: An Mental Symphony within **Virtual Processor**

In a global taken by displays and the ceaseless chatter of immediate transmission, the melodic elegance and psychological symphony produced by the prepared word frequently disappear in to the backdrop, eclipsed by the relentless sound and disturbances that permeate our lives. But, located within the pages of **Virtual Processor** an enchanting literary value filled with fresh feelings, lies an immersive symphony waiting to be embraced. Crafted by a wonderful musician of language, that charming masterpiece conducts readers on a mental journey, well unraveling the concealed songs and profound influence resonating

within each carefully constructed phrase. Within the depths of the poignant examination, we will discover the book is central harmonies, analyze its enthralling publishing type, and submit ourselves to the profound resonance that echoes in the depths of readers souls.

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Virtual Processor Introduction

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