

Cronus Emulator

Timothy S. Drake

Survey of Remote Terminal Emulators Shirley Ward Watkins, Marshall D. Abrams, Institute for Computer Sciences and Technology, 1977

How to Develop Embedded Software Using the QEMU Machine Emulator Apriorit Inc., Artem Kotovsky, 2019-09-10 This e-book has been written for embedded software developers by Apriorit experts. It goes in-depth on how to save time when developing a Windows device driver by emulating a physical device with QEMU and explores the details of device driver emulation based on QEMU virtual devices. Key Highlights □ Develop a Windows device driver without a physical device □ Learn to securely test device drivers, find and fix defects □ Leverage the QEMU machine emulator for your needs Book Description Developing a Windows driver for an embedded device is a very specific task, as this software should ensure stable and secure communication between operating system and a highly specialized device. Besides, in the world of ever increasing competitiveness, manufacturers tend to force developers to start working on embedded software before hardware is manufactured. Fortunately, developers can emulate a physical device using virtualization technologies like QEMU. After creating a virtual device in QEMU, developers can use it not only for a device driver development but also for its testing and fixing defects. In this e-book, the author shares his practical experience on developing Windows drivers using a QEMU virtual device. You'll dive deep into driver implementation process and learn about all the benefits and limitations of device emulation in QEMU. The e-book includes detailed steps to establish communication between a device and its driver. It also shows how to use QEMU for building running, testing, and debugging the whole environment. This approach was tested by Apriorit team for quite a long time, so it has already confirmed its value and effectiveness. What you will learn □ Dive into driver implementation stages □ Weigh up the pros and cons of using a QEMU virtual device □ Initialize the device in QEMU □ Develop your own Windows driver for a QEMU virtual hardware □ Establish communication between a device and its driver □ Process requests from a user mode application □ Use QEMU for building running, testing, and debugging embedded software About the Author Artem Kotovsky is a Software Analyst at Apriorit Inc. Apriorit Inc. is a software development service provider headquartered in the Dover, DE, US, with several development centers in Eastern Europe. With over 350 professionals, it brings high-quality services on software consulting, research, and development to software vendors and IT companies worldwide. Apriorit's main specialties are cybersecurity and data management projects, where system programming, driver and kernel level development, research and reversing matter. The company has an independent web platform development department focusing on building cloud platforms for business. Table of Contents Introduction Why do we use QEMU? Pros and cons of using a QEMU virtual device Driver implementation stages Communication between a device and its driver I/O address space Interrupts Line-based interrupts Message-signaled interrupts Bus mastering Test device specifications Structure of the device I/O memory Interrupts Device description in QEMU Initializing the device in QEMU Working with the I/O memory space Working with interrupts Working with DMA memory Processing requests QEMU device Implementing a WDF driver for the test device The minimum driver Initializing device resources Working with I/O memory Interrupt handling Working with DMA Sending requests to the device Processing requests from a user mode application Testing and debugging Quality control of driver code Driver installation Driver communication Implementing driver unit tests Implementing driver autotest Driver verification with Driver Verifier and WDF Verifier References

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A tale of three emulators Stanford University Stanford Electronics Laboratories. Digital Systems Laboratory,1975 This is a preliminary report on the development of emulator code for the Stanford EMMY.

32-Bit Emulator Chip for High Throughput Processing ,1996 Systems and Processes Engineering Corporation (SPEC) has developed a high throughput emulator chip which provides direct execution of existing AN/AYK-14 standard code on the commercially available PowerPC microprocessor. The SPEC emulator chip allows legacy and PowerPC code to be executed simultaneously, supporting an easy migration path to a full PowerPC code implementation. The SPEC emulator chip has better performance than a traditional software emulation or ROM based design. The SPEC emulator chip has lower fabrication and maintenance cost than using a multiple parallel processor approach. In summary, the SPEC emulator chip has the following features: (1) Full AN/AYK-14 emulation, (2) Simultaneously integrates legacy and new native PowerPC code, (3) Requires no software modifications, (4) Direct replacement of the AN/AYK-14 processor card, (5) Ease in performance validation, and (6) Low cost and maintenance. SPEC can develop drop in replacement circuit cards to meet specific user applications based on its ASIC design implementation. Emulator ASICs can either be implemented in CMOS or GaAs providing up to 500 MHz operation.

System/360 Emulator Performance Estimate Stanford University Stanford Electronics Laboratories. Digital Systems Laboratory,1975

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An Emmy based emulation of the CDC 6000 series CPU. Stanford University Stanford Electronics Laboratories. Digital Systems Laboratory,E. T. Roush,1977

SoC Emulation Bernard Murphy,Daniel Nenni,2018-02 When you turn the key in a modern car, you set in motion a chain of electronic events more complex, in some cases, than you'll find in a Boeing 787 or an F22 jet-fighter. The magic that makes this possible is captured in the many System-on-Chip (SoC) devices and software you'll find buried around the car. Leveraging many of the concepts at the heart of your smartphone, each of these SoC devices is itself very complex and must be validated extensively before manufacturing, to prove that it will always operate correctly and safely. Traditional methods used in validation, primarily simulation, are still widely used but are simply too slow to allow for the exhaustive levels of testing required to have high confidence in validation. This need has driven the semiconductor support industry to create special-purpose emulation engines to massively accelerate the SoC verification task. This book, targeted to general readers in the semiconductor and systems industry, describes the origins of SoC emulation and different approaches to addressing the need. It also describes some of the newer applications for emulation in SoC design. To illustrate current usage the second part of the book discusses how SoC emulation can be applied to verifying a controller for the solid-state drive (SSD) data storage now found in many modern devices.

Programmable Logic Controller Emulator Enhancements to Facilitate a Distributed Manufacturing Simulation Environment Sathishkumar Kunnamareddi,2001

The Design and Implementation of a Real-Time Distributed Application Emulator Timothy S. Drake,2001-03-01 This thesis

details the engineering, design and implementation of a real-time, distributed, application emulator system (AE system). The project had two main goals for the tool: emulation of real-time distributed systems, and as a programmable resource consumer. The AE system is currently being used in the HiPer-D test bed to activate a resource leveling tool that monitors several software components for real-time response. The AE system is highly flexible and can be used in the context of a variety of network topologies and system loading options. The results presented show that the AE system can also emulate distributed systems.

NOVA Emulator Design Using Intel's 3000 Series Microprocessor Elements Hari K. Nagpal,1976

Brill's Companion to Hellenistic Epigram Peter Bing,Jon Bruss,2007-04-30 An internationally renowned set of experts on epigram offers an introduction, fresh approaches, and new direction to the study of Hellenistic-era epigram by exploring the models, forms, poetology, sub-genera, intertexts, and ancient and modern reception of Hellenistic epigram.

Microprogramming Primer Harry Katzan,1988

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Advanced Ethernet/802.3 Network Management and Performance Bill Hancock,1996 Including information on the new Fast Ethernet (100BASE T) standard, the new 802.3 fiber standards, the use of switching bridges to improve performance through traffic isolation and how to clearly identify proper settings of heartbeat, this book answers the myriad of questions that come up after a network is installed.

The Structure and Uses of the Edinburgh Remote Terminal Emulator John Colin Adams,William Scott Currie,B. A. C. Gilmore,1977

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