

OTIMIZADOR DE MEM[?]RIA

D KEEGAN

❏ **OPENMP SHARED MEMORY PARALLEL PROGRAMMING** MICHAEL J. VOSS, INTERNATIONAL WORKSHOP ON OPENMP APPLICATIONS AND TOOLS, 2003-06-16 THE REFEREED PROCEEDINGS OF THE INTERNATIONAL WORKSHOP ON OPENMP APPLICATIONS AND TOOLS, WOMPAT 2003, HELD IN TORONTO, CANADA IN JUNE 2003. THE 20 REVISED FULL PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FOR INCLUSION IN THE BOOK. THE PAPERS ARE ORGANIZED IN SECTIONS ON TOOLS AND TOOL TECHNOLOGY, OPENMP IMPLEMENTATIONS, OPENMP EXPERIENCE, AND OPENMP ON CLUSTERS.

❏ **FAST, EFFICIENT AND PREDICTABLE MEMORY ACCESSSES** LARS WEHMEYER, PETER MARWEDEL, 2006-09-08 SPEED IMPROVEMENTS IN MEMORY SYSTEMS HAVE NOT KEPT PACE WITH THE SPEED IMPROVEMENTS OF PROCESSORS, LEADING TO EMBEDDED SYSTEMS WHOSE PERFORMANCE IS LIMITED BY THE MEMORY. THIS BOOK PRESENTS DESIGN TECHNIQUES FOR FAST, ENERGY-EFFICIENT AND TIMING-PREDICTABLE MEMORY SYSTEMS THAT ACHIEVE HIGH PERFORMANCE AND LOW ENERGY CONSUMPTION. IN ADDITION, THE USE OF SCRATCHPAD MEMORIES SIGNIFICANTLY IMPROVES THE TIMING PREDICTABILITY OF THE ENTIRE SYSTEM, LEADING TO TIGHTER WORST CASE EXECUTION TIME BOUNDS.

❏ **EXAM 70-432** MICROSOFT OFFICIAL ACADEMIC COURSE, 2011-02-15 THIS BOOK FOLLOWS THE NEWEST ITERATIONS OF THE MICROSOFT OFFICIAL ACADEMIC COURSE (MOAC) PROGRAM FOR DATABASE ADMINISTRATION COURSES USING SQL SERVER 2005 AND SQL SERVER 2008. IT PROVIDES THE INFORMATION AND RESOURCES NEEDED TO PREPARE STUDENTS FOR THE MICROSOFT CERTIFIED INFORMATION TECHNOLOGY PROFESSIONAL (MCITP) 70-444 EXAM FOR SQL SERVER 2005, AND THE 70-432 MICROSOFT CERTIFIED TECHNOLOGY SPECIALIST (MCTS) EXAM FOR SQL SERVER 2008. HANDS ON EXERCISES AND CERTIFICATION READY FEATURES ARE INTEGRATED WITH THE ACCOMPANYING LAB MANUAL THROUGHOUT THE CHAPTERS THAT ENABLE THEM TO REINFORCE WHAT THEY'VE LEARNED BY COMPLETING VARIOUS EXERCISES. BUSINESS CASES ARE PRESENTED TO PROVIDE A REAL WORLD PERSPECTIVE ON HOW THE INFORMATION IS USED IN THE FIELD. THE BOOK ALSO INCLUDES A CD THAT ENABLES STUDENTS WITH EXAM PRACTICE AND INSTANT FEEDBACK. BY FOLLOWING THE EXERCISES IN THIS BOOK, STUDENTS WILL LEARN HOW TO INSTALL AND CONFIGURE SQL SERVER, MANAGE DATABASES, IMPLEMENT SECURITY, TROUBLESHOOT SQL ACTIVITY, AND MORE.

❏ **WINDOWS INTERNALS** MARK E. RUSSINOVICH, DAVID A. SOLOMON, ALEX IONESCU, 2012 A GUIDE TO THE ARCHITECTURE AND INTERNAL STRUCTURE OF MICROSOFT WINDOWS 7 AND MICROSOFT WINDOWS SERVER 2008 R2.

❏ **MANAGING MICROSOFT EXCHANGE SERVER** PAUL ROBICHAUX, 1999 TARGETED AT MEDIUM-SIZED INSTALLATIONS AND UP, MANAGING MICROSOFT EXCHANGE SERVER ADDRESSES THE DIFFICULT PROBLEMS THESE USERS FACE: INTERNET INTEGRATION, STORAGE MANAGEMENT, COST OF OWNERSHIP, SYSTEM SECURITY, AND PERFORMANCE MANAGEMENT. GOING BEYOND THE BASICS, IT PROVIDES HANDS ON ADVICE ABOUT WHAT ONE NEEDS TO KNOW AFTER GETTING A SITE UP AND RUNNING AND FACING ISSUES OF GROWTH, OPTIMIZATION, OR RECOVERY PLANNING.

❏ **DB2 12 FOR z OPTIMIZER** TERRY PURCELL, IBM REDBOOKS, 2017-06-28 THERE HAS BEEN A CONSIDERABLE FOCUS ON PERFORMANCE IMPROVEMENTS AS ONE OF THE MAIN THEMES IN RECENT IBM DB2® RELEASES, AND DB2 12 FOR IBM z/OS® IS CERTAINLY NO EXCEPTION. WITH THE HIGH-VALUE DATA RETAINED ON DB2 FOR z/OS AND THE z SYSTEMS PLATFORM, CUSTOMERS ARE INCREASINGLY ATTEMPTING TO EXTRACT VALUE FROM THAT DATA FOR COMPETITIVE ADVANTAGE. ALTHOUGH CUSTOMERS HAVE HISTORICALLY MOVED DATA OFF PLATFORM TO GAIN INSIGHT, THE LANDSCAPE HAS CHANGED SIGNIFICANTLY AND ALLOWED z SYSTEMS TO AGAIN CONVERGE OPERATIONAL SYSTEMS WITH ANALYTICS FOR REAL-TIME INSIGHT. BUSINESS-CRITICAL ANALYTICS IS NOW REQUIRING THE SAME LEVELS OF SERVICE AS EXPECTED FOR OPERATIONAL SYSTEMS, AND REAL-TIME OR NEAR REAL-TIME CURRENCY OF DATA IS EXPECTED. HENCE THE RESURGENCE OF z SYSTEMS. AS A PRECURSOR TO THIS SHIFT, IDAA BROUGHT THE DATA WAREHOUSE BACK TO DB2 FOR z/OS AND, WITH ITS TIGHT INTEGRATION WITH DB2, SIGNIFICANTLY REDUCES DATA LATENCY AS COMPARED TO THE ETL PROCESSING THAT IS INVOLVED WITH MOVING DATA TO A STAND-ALONE DATA WAREHOUSE ENVIRONMENT. THAT CHANGE HAS OPENED UP NEW OPPORTUNITIES FOR OPERATIONAL SYSTEMS TO EXTEND THE BREADTH OF ANALYTICS PROCESSING WITHOUT AFFECTING THE MISSION-CRITICAL SYSTEM AND INTEGRATING NEAR REAL-TIME ANALYTICS WITHIN THAT SYSTEM, ALL WHILE MAINTAINING THE SAME z SYSTEMS QUALITIES OF SERVICE. APACHE SPARK ON z/OS AND LINUX FOR SYSTEM z ALSO ALLOW ANALYTICS IN-PLACE, IN REAL-TIME OR NEAR REAL-TIME. ENABLING SPARK NATIVELY ON z SYSTEMS REDUCES THE SECURITY RISK OF MULTIPLE COPIES OF THE ENTERPRISE DATA, WHILE PROVIDING AN APPLICATION DEVELOPER-FRIENDLY PLATFORM FOR FASTER INSIGHT IN A SIMPLIFIED AND MORE SECURE ANALYTICS FRAMEWORK. HOW IS ALL OF THIS RELEVANT TO DB2 FOR z/OS? GIVEN THAT z SYSTEMS IS PROVING AGAIN TO BE THE CORE ENTERPRISE HYBRID TRANSACTIONAL/ANALYTICAL PROCESSING (HTAP) SYSTEM, IT IS CRITICAL THAT DB2 FOR z/OS CAN HANDLE ITS TRADITIONAL TRANSACTIONAL APPLICATIONS AND ADDRESS THE REQUIREMENTS FOR ANALYTICS PROCESSING THAT MIGHT NOT BE CANDIDATES FOR THESE RAPIDLY EVOLVING TARGETED ANALYTICS SYSTEMS. AND NOT ONLY ARE THERE OPPORTUNITIES FOR DB2 FOR z/OS TO PLAY AN INCREASING ROLE IN ANALYTICS, THE COMPLEXITY OF THE TRANSACTIONAL SYSTEMS IS INCREASING. ANALYTICS IS BEING INTEGRATED WITHIN THE SCOPE OF THOSE TRANSACTIONS. DB2 12 FOR z/OS HAS TARGETED PERFORMANCE TO INCREASE THE SUCCESS OF NEW APPLICATION DEPLOYMENTS AND INTEGRATION OF ANALYTICS TO ENSURE THAT WE KEEP PACE WITH THE RAPID EVOLUTION OF IDAA AND SPARK AS EQUAL PARTNERS IN HTAP SYSTEMS. THIS PAPER DESCRIBES THE ENHANCEMENTS DELIVERED SPECIFICALLY BY THE QUERY PROCESSING ENGINE OF DB2. THIS ENGINE IS GENERALLY CALLED THE OPTIMIZER OR THE RELATIONAL DATA SERVICES (RDS) COMPONENTS, WHICH ENCOMPASSES THE QUERY TRANSFORMATION, ACCESS PATH SELECTION, RUN TIME, AND PARALLELISM. DB2 12 FOR z/OS ALSO DELIVERS IMPROVEMENTS TARGETED AT OLTP APPLICATIONS, WHICH ARE THE REALM OF THE DATA MANAGER, INDEX MANAGER, AND BUFFER MANAGER COMPONENTS (TO NAME A FEW), AND

ARE NOT IDENTIFIED HERE. ALTHOUGH THE PERFORMANCE MEASUREMENT FOCUS IS BASED ON REDUCING CPU, IMPROVEMENT IN ELAPSED TIME IS LIKELY TO BE SIMILARLY ACHIEVED AS CPU IS REDUCED AND PERFORMANCE CONSTRAINTS ALLEVIATED. HOWEVER, ELAPSED TIME IMPROVEMENTS CAN BE ACHIEVED WITH PARALLELISM, AND DB2 12 DOES INCREASE THE PERCENTAGE OFFLOAD FOR PARALLEL CHILD TASKS, WHICH CAN FURTHER REDUCE CHARGEABLE CPU FOR ANALYTICS WORKLOADS.

🔗 **LONG SHORT-TERM MEMORY NETWORKS WITH PYTHON** JASON BROWNLEE, 2017-07-20 THE LONG SHORT-TERM MEMORY NETWORK, OR LSTM FOR SHORT, IS A TYPE OF RECURRENT NEURAL NETWORK THAT ACHIEVES STATE-OF-THE-ART RESULTS ON CHALLENGING PREDICTION PROBLEMS. IN THIS LASER-FOCUSED EBOOK, FINALLY CUT THROUGH THE MATH, RESEARCH PAPERS AND PATCHWORK DESCRIPTIONS ABOUT LSTMS. USING CLEAR EXPLANATIONS, STANDARD PYTHON LIBRARIES AND STEP-BY-STEP TUTORIAL LESSONS YOU WILL DISCOVER WHAT LSTMS ARE, AND HOW TO DEVELOP A SUITE OF LSTM MODELS TO GET THE MOST OUT OF THE METHOD ON YOUR SEQUENCE PREDICTION PROBLEMS.

🔗 **ADVANCES IN NON-VOLATILE MEMORY AND STORAGE TECHNOLOGY** YOSHIO NISHI, 2014-06-24 NEW SOLUTIONS ARE NEEDED FOR FUTURE SCALING DOWN OF NONVOLATILE MEMORY. ADVANCES IN NON-VOLATILE MEMORY AND STORAGE TECHNOLOGY PROVIDES AN OVERVIEW OF DEVELOPING TECHNOLOGIES AND EXPLORES THEIR STRENGTHS AND WEAKNESSES. AFTER AN OVERVIEW OF THE CURRENT MARKET, PART ONE INTRODUCES IMPROVEMENTS IN FLASH TECHNOLOGIES, INCLUDING DEVELOPMENTS IN 3D NAND FLASH TECHNOLOGIES AND FLASH MEMORY FOR ULTRA-HIGH DENSITY STORAGE DEVICES. PART TWO LOOKS AT THE ADVANTAGES OF DESIGNING PHASE CHANGE MEMORY AND RESISTIVE RANDOM ACCESS MEMORY TECHNOLOGIES. IT LOOKS IN PARTICULAR AT THE FABRICATION, PROPERTIES, AND PERFORMANCE OF NANOWIRE PHASE CHANGE MEMORY TECHNOLOGIES. LATER CHAPTERS ALSO CONSIDER MODELING OF BOTH METAL OXIDE AND RESISTIVE RANDOM ACCESS MEMORY SWITCHING MECHANISMS, AS WELL AS CONDUCTIVE BRIDGE RANDOM ACCESS MEMORY TECHNOLOGIES. FINALLY, PART THREE LOOKS TO THE FUTURE OF ALTERNATIVE TECHNOLOGIES. THE AREAS COVERED INCLUDE MOLECULAR, POLYMER, AND HYBRID ORGANIC MEMORY DEVICES, AND A VARIETY OF RANDOM ACCESS MEMORY DEVICES SUCH AS NANO-ELECTROMECHANICAL, FERROELECTRIC, AND SPIN-TRANSFER-TORQUE MAGNETORESISTIVE DEVICES. ADVANCES IN NON-VOLATILE MEMORY AND STORAGE TECHNOLOGY IS A KEY RESOURCE FOR POSTGRADUATE STUDENTS AND ACADEMIC RESEARCHERS IN PHYSICS, MATERIALS SCIENCE, AND ELECTRICAL ENGINEERING. IT IS A VALUABLE TOOL FOR RESEARCH AND DEVELOPMENT MANAGERS CONCERNED WITH ELECTRONICS, SEMICONDUCTORS, NANOTECHNOLOGY, SOLID-STATE MEMORIES, MAGNETIC MATERIALS, ORGANIC MATERIALS, AND PORTABLE ELECTRONIC DEVICES. PROVIDES AN OVERVIEW OF DEVELOPING NONVOLATILE MEMORY AND STORAGE TECHNOLOGIES AND EXPLORES THEIR STRENGTHS AND WEAKNESSES EXAMINES IMPROVEMENTS TO FLASH TECHNOLOGY, CHARGE TRAPPING, AND RESISTIVE RANDOM ACCESS MEMORY DISCUSSES EMERGING DEVICES SUCH AS THOSE BASED ON POLYMER AND MOLECULAR ELECTRONICS, AND NANOELECTROMECHANICAL RANDOM ACCESS MEMORY (RAM)

🔗 **PC MAG**, 2003-06-17 PCMAG.COM IS A LEADING AUTHORITY ON TECHNOLOGY, DELIVERING LABS-BASED, INDEPENDENT REVIEWS OF THE LATEST PRODUCTS AND SERVICES. OUR EXPERT INDUSTRY ANALYSIS AND PRACTICAL SOLUTIONS HELP YOU MAKE BETTER BUYING DECISIONS AND GET MORE FROM TECHNOLOGY.

🔗 **EMBEDDED COMPUTER SYSTEMS: ARCHITECTURES, MODELING, AND SIMULATION** STAMATIS VASSILIADIS, STEPHAN WONG, TIMO D. HOFMEIER, 2006-07-18 THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 6TH INTERNATIONAL WORKSHOP ON SYSTEMS, ARCHITECTURES, MODELING, AND SIMULATION, SAMOS 2006, HELD IN SAMOS, GREECE ON JULY 2006. THE 47 REVISED FULL PAPERS PRESENTED TOGETHER WITH 2 KEYNOTE TALKS WERE THOROUGHLY REVIEWED AND SELECTED FROM 130 SUBMISSIONS. THE PAPERS ARE ORGANIZED IN TOPICAL SECTIONS ON SYSTEM DESIGN AND MODELING, WIRELESS SENSOR NETWORKS, PROCESSOR DESIGN, DEPENDABLE COMPUTING, ARCHITECTURES AND IMPLEMENTATIONS, AND EMBEDDED SENSOR SYSTEMS.

🔗 **BEST TOOLS FOR WINDOWS 7** WAHANA KOMPUTER,

🔗 **ALGORITHMS FOR MEMORY HIERARCHIES** ULRICH MEYER, PETER SANDERS, 2003-04-07 ALGORITHMS THAT HAVE TO PROCESS LARGE DATA SETS HAVE TO TAKE INTO ACCOUNT THAT THE COST OF MEMORY ACCESS DEPENDS ON WHERE THE DATA IS STORED. TRADITIONAL ALGORITHM DESIGN IS BASED ON THE VON NEUMANN MODEL WHERE ACCESSES TO MEMORY HAVE UNIFORM COST. ACTUAL MACHINES INCREASINGLY DEVIATE FROM THIS MODEL: WHILE WAITING FOR MEMORY ACCESS, NOWADAYS, MICROPROCESSORS CAN IN PRINCIPLE EXECUTE 1000 ADDITIONS OF REGISTERS; FOR HARD DISK ACCESS THIS FACTOR CAN REACH SIX ORDERS OF MAGNITUDE. THE 16 COHERENT CHAPTERS IN THIS MONOGRAPH-LIKE TUTORIAL BOOK INTRODUCE AND SURVEY ALGORITHMIC TECHNIQUES USED TO ACHIEVE HIGH PERFORMANCE ON MEMORY HIERARCHIES; EMPHASIS IS PLACED ON METHODS INTERESTING FROM A THEORETICAL AS WELL AS IMPORTANT FROM A PRACTICAL POINT OF VIEW.

🔗 **EXPERT SQL SERVER IN-MEMORY OLTP** DMITRI KOROTKEVITCH, 2017-09-02 TAKE A DEEP DIVE INTO ONE OF THE MOST SIGNIFICANT SQL SERVER FEATURES—SUPPORT FOR IN-MEMORY ONLINE TRANSACTION PROCESSING. THE LATEST DEVELOPMENTS ARE COVERED, INCLUDING SUPPORT OF OFF-ROW STORAGE, COLUMNSTORE INDEXES AND OPERATIONAL ANALYTICS, CHANGES IN PROGRAMMABILITY AND NATIVE COMPILATION, AND MORE. THIS BOOK DESCRIBES THE ARCHITECTURE AND INTERNALS OF THE IN-MEMORY OLTP ENGINE AND EXPLAINS HOW TO DEVELOP, DEPLOY, AND MAINTAIN SYSTEMS USING IT. WITH IT YOU CAN DRAMATICALLY INCREASE TRANSACTION THROUGHPUT TO HANDLE THOUSANDS OF TRANSACTIONS PER SECOND SUPPORTING MILLIONS OF CUSTOMERS. LEARN THE ARCHITECTURE AND THE INTERNALS OF IN-MEMORY OLTP IN ORDER TO RECOGNIZE WHEN TECHNOLOGY CAN MAKE A DIFFERENCE. RECOGNIZE OPPORTUNITIES FOR IN-MEMORY OLTP IN NEW DEVELOPMENT AND UNDERSTAND HOW TO BENEFIT FROM IT IN EXISTING SYSTEMS. DON'T BE WITHOUT DMITRI KOROTKEVITCH AND THE DEEP

EXPERTISE HE IMPARTS IN *EXPERT SQL SERVER IN-MEMORY OLTP, 2ND EDITION* AS YOU MOVE FORWARD IN USING SQL SERVER'S IN-MEMORY OLTP TECHNOLOGY. DMITRI KOROTKEVITCH IS THE FIVE-STAR AUTHOR OF *PRO SQL SERVER INTERNALS*, AND BRINGS THE SAME COMBINATION OF CLEAR THINKING AND DEEP EXPERTISE TO HELP YOU IN THIS SECOND EDITION. THE BOOK: EXPLAINS IN-MEMORY OLTP INTERNALS, ARCHITECTURE AND PROGRAMMABILITY, INCLUDING DATA STORAGE, INDEXING, MULTI-VERSION CONCURRENCY CONTROL, TRANSACTION LOGGING AND RECOVERY, AND NATIVE COMPILATION COVERS SQL SERVER 2016 TECHNOLOGY ENHANCEMENTS, INCLUDING COLUMNSTORE INDEXES AND OFF-ROW STORAGE GUIDES IN USING IN-MEMORY OLTP IN NEW DEVELOPMENT AND EXISTING SYSTEMS WHAT YOU'LL LEARN GRASP HOW SQL SERVER STORES AND WORKS WITH DATA IN MEMORY-OPTIMIZED TABLES PROPERLY DESIGN AND INDEX MEMORY-OPTIMIZED TABLES PLAN SUCCESSFUL DEPLOYMENTS, INCLUDING THE REQUIRED MEMORY SIZE AND I/O CONFIGURATION ACCELERATE T-SQL PROCESSING THROUGH THE CREATION OF NATIVELY COMPILED MODULES MONITOR AND REPORT ON THE BENEFITS AND PERFORMANCE OF YOUR IN-MEMORY OLTP SOLUTIONS BENEFIT FROM THE TECHNOLOGY IN EXISTING SYSTEMS AND IN THE SYSTEMS WITH THE MIXED WORKLOAD WHO THIS BOOK IS FOR APPLICATION DEVELOPERS AND DATABASE ADMINISTRATORS WHO CREATE AND MANAGE ONLINE TRANSACTION PROCESSING (OLTP) SYSTEMS; IN PARTICULAR, THOSE WANTING TO TAKE ADVANTAGE OF SQL SERVER'S NEW OFFERING OF IN-MEMORY OLTP TO DRAMATICALLY IMPROVE PERFORMANCE AND THROUGHPUT OF THEIR SYSTEMS

📖 *OPENMP SHARED MEMORY PARALLEL PROGRAMMING* MATTHIAS S. MILLER, 2008-05-21 THIS BOOK CONSTITUTES THE THOROUGHLY REFEREED POST-WORKSHOP PROCEEDINGS OF THE FIRST AND THE SECOND INTERNATIONAL WORKSHOP ON OPENMP, IWOMP 2005 AND IWOMP 2006, HELD IN EUGENE, OR, USA, AND IN REIMS, FRANCE, IN JUNE 2005 AND 2006 RESPECTIVELY. THE FIRST PART OF THE BOOK PRESENTS 16 REVISED FULL PAPERS CAREFULLY REVIEWED AND SELECTED FROM THE IWOMP 2005 PROGRAM AND ORGANIZED IN TOPICAL SECTIONS ON PERFORMANCE TOOLS, COMPILER TECHNOLOGY, RUN-TIME ENVIRONMENT, APPLICATIONS, AS WELL AS THE OPENMP LANGUAGE AND ITS EVALUATION. IN THE SECOND PART THERE ARE 19 PAPERS OF IWOMP 2006, FULLY REVISED AND GROUPED THEMATICALLY IN SECTIONS ON ADVANCED PERFORMANCE TUNING ASPECTS OF CODE DEVELOPMENT APPLICATIONS, AND PROPOSED EXTENSIONS TO OPENMP.

📖 *PROCEEDINGS 2002 VLDB CONFERENCE* VLDB, 2002-12-11 PROCEEDINGS OF THE 28TH ANNUAL INTERNATIONAL CONFERENCE ON VERY LARGE DATA BASES HELD IN HONG KONG, CHINA ON AUGUST 20-23, 2002. ORGANIZED BY THE VLDB ENDOWMENT, VLDB IS THE PREMIER INTERNATIONAL CONFERENCE ON DATABASE TECHNOLOGY.

📖 **IBM POWER 720 AND 740 TECHNICAL OVERVIEW AND INTRODUCTION** SCOTT VETTER, JAMES CRUICKSHANK, SORIN HANGANU, VOLKER HAUG, STEPHEN LUTZ, JOHN T. SCHMIDT, MARCO VALLONE, IBM REDBOOKS, 2013-05-16 THIS IBM® REDPAPER™ PUBLICATION IS A COMPREHENSIVE GUIDE COVERING THE IBM POWER 720 AND POWER 740 SERVERS THAT SUPPORT IBM AIX®, IBM i, AND LINUX OPERATING SYSTEMS. THE GOAL OF THIS PAPER IS TO INTRODUCE THE INNOVATIVE POWER 720 AND POWER 740 OFFERINGS AND THEIR MAJOR FUNCTIONS: THE IBM POWER7+™ PROCESSOR IS AVAILABLE AT FREQUENCIES OF 3.6 GHz, AND 4.2 GHz. THE LARGER IBM POWER7+ LEVEL 3 CACHE PROVIDES GREATER BANDWIDTH, CAPACITY, AND RELIABILITY. THE 4-PORT 10/100/1000 BASE-TX ETHERNET PCI EXPRESS ADAPTER IS INCLUDED IN BASE CONFIGURATION AND INSTALLED IN A PCIe GEN2 x4 SLOT. THE INTEGRATED SAS/SATA CONTROLLER FOR HDD, SSD, TAPE, AND DVD SUPPORTS BUILT-IN HARDWARE RAID 0, 1, AND 10. NEW IBM POWERVM® V2.2.2 FEATURES, SUCH AS 20 LPARs PER CORE. THE IMPROVED IBM ACTIVE MEMORY™ EXPANSION TECHNOLOGY PROVIDES MORE USABLE MEMORY THAN IS PHYSICALLY INSTALLED IN THE SYSTEM. HIGH-PERFORMANCE SSD DRAWER. PROFESSIONALS WHO WANT TO ACQUIRE A BETTER UNDERSTANDING OF IBM POWER SYSTEMS™ PRODUCTS CAN BENEFIT FROM READING THIS PAPER. THIS PAPER EXPANDS THE CURRENT SET OF IBM POWER SYSTEMS DOCUMENTATION BY PROVIDING A DESKTOP REFERENCE THAT OFFERS A DETAILED TECHNICAL DESCRIPTION OF THE POWER 720 AND POWER 740 SYSTEMS. THIS PAPER DOES NOT REPLACE THE LATEST MARKETING MATERIALS AND CONFIGURATION TOOLS. IT IS INTENDED AS AN ADDITIONAL SOURCE OF INFORMATION THAT, TOGETHER WITH EXISTING SOURCES, CAN BE USED TO ENHANCE YOUR KNOWLEDGE OF IBM SERVER SOLUTIONS.

📖 **IBM POWER 795 (9119-FHB) TECHNICAL OVERVIEW AND INTRODUCTION** SCOTT VETTER, AN DING CHEN, CARLO COSTANTINI, JAMES CRUICKSHANK, DAVE FREEMAN, VOLKER HAUG, BRENO HENRIQUE LEITAO, CESAR DINIZ MACIEL, JOHN T. SCHMIDT, IBM REDBOOKS, 2013-02-11 THIS IBM® REDPAPER™ PUBLICATION IS A COMPREHENSIVE GUIDE THAT COVERS THE IBM POWER 795 SERVER THAT SUPPORTS IBM AIX®, IBM i, AND LINUX OPERATING SYSTEMS. THE GOAL OF THIS PAPER IS TO INTRODUCE THE INNOVATIVE POWER 795 OFFERING AND ITS MAJOR FUNCTIONS: IBM POWER7® PROCESSOR, AVAILABLE AT FREQUENCIES OF 3.7 GHz AND 4.0 GHz WITH TURBOCORE OPTIONS OF 4.25 GHz AND 4.31 GHz SPECIALIZED POWER7 LEVEL 3 CACHE THAT PROVIDES GREATER BANDWIDTH, CAPACITY, AND RELIABILITY IBM POWERVM® VIRTUALIZATION, INCLUDING POWERVM LIVE PARTITION MOBILITY AND POWERVM IBM ACTIVE MEMORY™ SHARING TURBOCORE MODE THAT DELIVERS THE HIGHEST PERFORMANCE PER CORE ENHANCED RELIABILITY, ACCESSIBILITY, AND SERVICEABILITY (RAS) FEATURES THAT ARE DESIGNED FOR MAXIMUM AVAILABILITY ACTIVE MEMORY EXPANSION THAT PROVIDES MORE USABLE MEMORY THAN WHAT IS PHYSICALLY INSTALLED ON THE SYSTEM IBM ENERGYSCALE™ TECHNOLOGY THAT PROVIDES FEATURES SUCH AS POWER TRENDING, POWER-SAVING, CAPPING OF POWER, AND THERMAL MEASUREMENT PROFESSIONALS WHO WANT TO ACQUIRE A BETTER UNDERSTANDING OF IBM POWER SYSTEMS™ PRODUCTS CAN BENEFIT FROM READING THIS PAPER. THIS PAPER COMPLEMENTS THE AVAILABLE SET OF IBM POWER SYSTEMS DOCUMENTATION BY PROVIDING A DESKTOP REFERENCE THAT OFFERS A DETAILED TECHNICAL DESCRIPTION OF THE POWER 795 SYSTEM. THIS PAPER DOES NOT REPLACE THE LATEST MARKETING MATERIALS AND CONFIGURATION TOOLS. IT IS INTENDED AS AN ADDITIONAL SOURCE OF INFORMATION THAT, TOGETHER WITH EXISTING SOURCES, CAN BE USED TO ENHANCE YOUR

KNOWLEDGE OF IBM SERVER SOLUTIONS.

❑ **MODEL-DRIVEN ENGINEERING AND SOFTWARE DEVELOPMENT** SLIMANE HAMMOUDI, LUÍS FERREIRA PIRES, BRAN SELIC, 2019-01-31 THIS BOOK CONSTITUTES THOROUGHLY REVISED AND SELECTED PAPERS FROM THE 6TH INTERNATIONAL CONFERENCE ON MODEL-DRIVEN ENGINEERING AND SOFTWARE DEVELOPMENT, MODELSWARD 2018, HELD IN FUNCHAL, MADEIRA, PORTUGAL, IN JANUARY 2018. THE 22 THOROUGHLY REVISED AND EXTENDED PAPERS PRESENTED IN THIS VOLUME WERE CAREFULLY REVIEWED AND SELECTED FROM 101 SUBMISSIONS. THEY CONTRIBUTE TO THE DEVELOPMENT OF HIGHLY RELEVANT RESEARCH TRENDS IN MODEL-DRIVEN ENGINEERING AND SOFTWARE DEVELOPMENT SUCH AS INNOVATIVE METHODS FOR MDD-BASED DEVELOPMENT AND TESTING OF WEB-BASED APPLICATIONS AND USER INTERFACES, SUPPORT FOR DEVELOPMENT OF DOMAIN-SPECIFIC LANGUAGES (DSLs), MDD-BASED APPLICATION DEVELOPMENT ON MULTIPROCESSOR PLATFORMS, ADVANCES IN MDD TOOLING, FORMAL SEMANTICS AND BEHAVIOUR MODELLING, AND MDD-BASED PRODUCT-LINE ENGINEERING.

❑ **OPENMP SHARED MEMORY PARALLEL PROGRAMMING** RUDOLF EIGENMANN, MICHAEL J. VOSS, 2003-05-15 THIS BOOK CONTAINS THE PRESENTATIONS GIVEN AT THE WORKSHOP ON OPENMP APPLICATIONS AND TOOLS, WOMPAT 2001. THE WORKSHOP WAS HELD ON JULY 30 AND 31, 2001 AT PURDUE UNIVERSITY, WEST LAFAYETTE, INDIANA, USA. IT BROUGHT TOGETHER DESIGNERS, USERS, AND RESEARCHERS OF THE OPENMP APPLICATION PROGRAMMING INTERFACE. OPENMP HAS EMERGED AS THE STANDARD FOR SHARED MEMORY PARALLEL PROGRAMMING. FOR THE FIRST TIME, IT IS POSSIBLE TO WRITE PARALLEL PROGRAMS THAT ARE PORTABLE ACROSS THE MAJORITY OF SHARED MEMORY PARALLEL COMPUTERS. WOMPAT 2001 SERVED AS A FORUM FOR ALL THOSE INTERESTED IN OPENMP AND ALLOWED THEM TO MEET, SHARE IDEAS AND EXPERIENCES, AND DISCUSS THE LATEST DEVELOPMENTS OF OPENMP AND ITS APPLICATIONS. WOMPAT 2001 WAS CO-SPONSORED BY THE OPENMP ARCHITECTURE REVIEW BOARD (ARB). IT FOLLOWED A SERIES OF WORKSHOPS ON OPENMP, INCLUDING WOMPAT 2000, EWOMP 2000, AND WOMPEI 2000. FOR WOMPAT 2001, WE SOLICITED PAPERS FORMALLY AND PUBLISHED THEM IN THE FORM OF THIS BOOK. THE AUTHORS SUBMITTED EXTENDED ABSTRACTS, WHICH WERE REVIEWED BY THE PROGRAM COMMITTEE. ALL SUBMITTED PAPERS WERE ACCEPTED. THE AUTHORS WERE ASKED TO PREPARE A FINAL PAPER IN WHICH THEY ADDRESSED THE REVIEWERS COMMENTS. THE PROCEEDINGS, IN THE FORM OF THIS BOOK, WERE CREATED IN TIME TO BE AVAILABLE AT THE WORKSHOP. IN THIS WAY, WE HOPE TO HAVE BROUGHT OUT A TIMELY REPORT OF ONGOING OPENMP-RELATED RESEARCH AND DEVELOPMENT EFFORTS AS WELL AS IDEAS FOR FUTURE IMPROVEMENTS.

❑ **METAHEURISTIC OPTIMIZATION VIA MEMORY AND EVOLUTION** CESAR REGO, BAHRAM ALIDAEI, 2006-03-30 TABU SEARCH (TS) AND, MORE RECENTLY, SCATTER SEARCH (SS) HAVE PROVED HIGHLY EFFECTIVE IN SOLVING A WIDE RANGE OF OPTIMIZATION PROBLEMS, AND HAVE HAD A VARIETY OF APPLICATIONS IN INDUSTRY, SCIENCE, AND GOVERNMENT. THE GOAL OF METAHEURISTIC OPTIMIZATION VIA MEMORY AND EVOLUTION: TABU SEARCH AND SCATTER SEARCH IS TO REPORT ORIGINAL RESEARCH ON ALGORITHMS AND APPLICATIONS OF TABU SEARCH, SCATTER SEARCH OR BOTH, AS WELL AS VARIATIONS AND EXTENSIONS HAVING ADAPTIVE MEMORY PROGRAMMING AS A PRIMARY FOCUS. INDIVIDUAL CHAPTERS IDENTIFY USEFUL NEW IMPLEMENTATIONS OR NEW WAYS TO INTEGRATE AND APPLY THE PRINCIPLES OF TS AND SS, OR THAT PROVE NEW THEORETICAL RESULTS, OR DESCRIBE THE SUCCESSFUL APPLICATION OF THESE METHODS TO REAL WORLD PROBLEMS.

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