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Intel Memory Design Handbook Intel Corporation,1977

Product Overview Intel Corporation,1992

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Intel Trusted Execution Technology for Server Platforms William Futral,James Greene,2013-10-08 This book is a must have resource guide for anyone who wants to ... implement TXT within their environments. I wish we had this guide when our engineering teams were implementing TXT on our solution platforms!" John McAuley,EMC Corporation This book details innovative technology that provides significant benefit to both the cloud consumer and the cloud provider when working to meet the ever increasing requirements of trust and control in the cloud." Alex Rodriguez, Expedient Data Centers This book is an invaluable reference for understanding enhanced server security, and how to deploy and leverage computing environment trust to reduce supply chain risk." Pete Nicoletti. Virtustream Inc. Intel® Trusted Execution Technology (Intel TXT) is a new security technology that started appearing on Intel server platforms in 2010. This book explains Intel Trusted Execution Technology for Servers, its purpose, application, advantages, and limitations. This book guides the server administrator / datacenter manager in enabling the technology as well as establishing a launch control policy that he can use to customize the server's boot process to fit the datacenter's requirements. This book explains how the OS (typically a Virtual Machine Monitor or Hypervisor) and supporting software can build on the secure facilities afforded by Intel TXT to provide additional security features and functions. It provides examples how the datacenter can create and use trusted pools. With a foreword from Albert Caballero, the CTO at Trapezoid.

Intel Xeon Phi Coprocessor High Performance Programming James Jeffers,James Reinders,2013-02-11 Authors Jim Jeffers and James Reinders spent two years helping educate customers about the prototype and pre-production hardware before Intel introduced the first Intel Xeon Phi coprocessor. They have distilled their own experiences coupled with insights from many expert customers, Intel Field Engineers, Application Engineers and Technical Consulting Engineers, to create this authoritative first book on the essentials of programming for this new architecture and these new products. This book is useful even before you ever touch a system with an Intel Xeon Phi coprocessor. To ensure that your applications run at maximum efficiency, the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors, Intel Xeon Phi coprocessors, or other high performance microprocessors. Applying these techniques will generally increase your program performance on any system, and better prepare you for Intel Xeon Phi coprocessors and the Intel MIC architecture. A practical guide to the essentials of the Intel Xeon Phi coprocessor Presents best practices for portable, high-performance computing and a familiar and proven threaded, scalar-vector programming model Includes simple but informative code examples that explain the unique aspects of this new highly parallel and high performance computational product Covers wide vectors, many cores, many threads and high bandwidth cache/memory architecture

Intel Threading Building Blocks James Reinders,2007-07-12 Book explains how to maximize the benefits of Intel's new dual-core and multi-core processors through a portable C++ library that works on Windows, Linux, Macintosh, and Unix systems.

Optimizing HPC Applications with Intel Cluster Tools Alexander Supalov,Andrey Semin,Christopher Dahnken,Michael Klemm,2014-10-09 Optimizing HPC Applications with Intel® Cluster Tools takes the reader on a tour of the fast-growing area of high performance computing and the optimization of hybrid programs. These programs typically combine distributed memory and shared memory programming models and use the Message Passing Interface (MPI) and OpenMP for multi-threading to achieve the ultimate goal of high performance at low power consumption on enterprise-class workstations and compute clusters. The book focuses on optimization for clusters consisting of the Intel® Xeon processor, but the optimization methodologies also apply to the Intel® Xeon Phi™ coprocessor and heterogeneous clusters mixing both architectures. Besides the tutorial and reference content, the authors address and refute many myths and misconceptions surrounding the topic. The text is augmented and enriched by descriptions of real-life situations.

Intel's SL Architecture Desmond Yuen,1993 The first and only book to explain the architecture, function, and application of the Intel i386SL microprocessor. Both engineers and programmers will discover comprehensive coverage of system internals and programming techniques with an eye towards implementing this advanced microprocessor.

Product Overview and Master Index for Intel Processors ,1992

Intel Three-Eight-Six Sl Mircroprocessor Superset System Design Guide Intel Corporation,Intel Corporation Staff,1992-01-01

Intel Integrated Performance Primitives Stewart Taylor,2003 Annotation Introducing the basics of Intel Integrated Performance Primitives, this book explores the range of possible applications from audio processing to graphics and video. Included are extensive examples written in C++ on how to solve common imaging, audio/video, and graphics problems using Intel IPP. Demonstrated is how the IPP Programming interface can increase performance in Intel's latest microprocessors while providing time-to-market advantaged and reducing the cost of development. Also covered is how Intel IPP provides access to advanced processor features without having to write processor-specific code. The accompanying CD-ROM includes code samples and an evaluation version of the Intel Integrated Performance Primitives.

I750, I860, I960 Processors and Related Products Intel Corporation,1993

Intel386 DX Microprocessor Hardware Reference Manual Intel Corporation,1991

80960KB Programmer's Reference Manual Intel Corporation,1989

Migration and Compatibility Guide for Intel Processors UNIX System Laboratories,1992

Intel 486 Microprocessors and Related Products Intel Corporation,1995 This databook contains extensive information on Intel486 microprocessor families, OverDrive processors, supporting PCIsets, floppy and hard disk controllers, mobile peripheral products and flash memory components for the desktop and mobile family of Intel486 microprocessors. The datasheets and application notes contained in this databook include comprehensive charts, diagrams and instruction and hardware information for leading-edge 32-bit system development.--BOOK JACKET.Title Summary field provided by Blackwell North America, Inc. All Rights Reserved

Oem Boards, Systems and Software Intel Corporation,1995 This book features a broad range of hardware and software products for real-time applications and software development. Included are application notes and datasheets for Intel OEM boards and platforms. Datasheets include specifications and features of the products as well as architectural and operational descriptions.

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Pentium Processor User's Manual Intel Corporation,1994

The Pentium Chronicles Robert P. Colwell,2006 Publisher Description

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