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Mitch Tulloch, Windows Server Core Team at Microsoft

Proceedings of the ... USENIX Symposium on Operating Systems Design and Implementation (OSDI) ,1994

Proceedings of the First USENIX Symposium on Operating Systems Design and Implementation (OSDI) USENIX Association,1994

Equalizer Programming and User Guide Stefan Eilemann,2013-07-26 The official reference for developing and deploying parallel, scalable OpenGL applications based on the Equalizer parallel rendering framework.

Windows Internals, Part 2 Mark E. Russinovich,David A. Solomon,Alex Ionescu,2012-09-15 Delve inside Windows architecture and internals—and see how core components work behind the scenes. Led by three renowned internals experts, this classic guide is fully updated for Windows 7 and Windows Server 2008 R2—and now presents its coverage in two volumes. As always, you get critical insider perspectives on how Windows operates. And through hands-on experiments, you'll experience its internal behavior firsthand—knowledge you can apply to improve application design, debugging, system performance, and support. In Part 2, you'll examine: Core subsystems for I/O, storage, memory management, cache manager, and file systems Startup and shutdown processes Crash-dump analysis, including troubleshooting tools and techniques

In Memory Data Management and Analysis Arun Jagatheesan,Justin Levandoski,Thomas Neumann,Andrew Pavlo,2015-01-13 This book constitutes the thoroughly refereed post conference proceedings of the First and Second International Workshops on In Memory Data Management and Analysis held in Riva del Garda, Italy, August 2013 and Hangzhou, China, in September 2014. The 11 revised full papers were carefully reviewed and selected from 18 submissions and cover topics from main-memory graph analytics platforms to main-memory OLTP applications.

Windows Internals Mark E. Russinovich,David A. Solomon,Alex Ionescu,2012 A guide to the architecture and internal structure of Microsoft Windows 7 and Microsoft Windows server 2008 R2.

Modelling, Analysis, and Simulation of Computer and Telecommunication Systems Maria Carla Calzarossa,Erol Gelenbe,Krzysztof Grochla,Ricardo Lent,Tadeusz Czachórski,2021-01-28 This book constitutes the post proceedings of the 28th International Symposium on Modelling, Analysis, and Simulation of Computer and Telecommunication Systems, MASCOTS 2020, held online -due to COVID -19- in Nice, France, in November 2020. The 17 full papers presented were carefully reviewed and selected from 124 submissions. The symposium collected the most relevant papers describing state-of-the-art research in the areas of the performance evaluation of computer systems and networks as well as in related areas.

Windows Internals Pavel Yosifovich,David A. Solomon,Alex Ionescu,Mark E. Russinovich,2017-05-05 The definitive guide—fully updated for Windows 10 and Windows Server 2016 Delve inside Windows architecture and internals, and see how core components work behind the scenes. Led by a team of internals experts, this classic guide has been fully updated for Windows 10 and Windows Server 2016. Whether you are a developer or an IT professional, you'll get critical, insider perspectives on how Windows operates. And through hands-on experiments, you'll experience its internal behavior firsthand—knowledge you can apply to improve application design, debugging, system performance, and support. This book will help you: · Understand the Window system architecture and its most important entities, such as processes and threads · Examine how processes manage resources and threads scheduled for execution inside processes · Observe how Windows manages virtual and physical memory · Dig into the Windows I/O system and see how device drivers work and integrate with the rest of the system · Go inside the Windows security model to see how it manages

access, auditing, and authorization, and learn about the new mechanisms in Windows 10 and Server 2016

Building iOS 5 Games James Sugrue, 2011-12-06 Mobile-app development, and mobile-game-app development in particular, is attracting developers with the promise of a large and growing user base and ginormous unit sales. For example, over during the Christmas holiday, Tapulous reported iOS users were downloading the newest version of Tap Tap Revenge 25,000 times per hour, peaking at 45,000 downloads per hour on Christmas day. This book teaches iOS game development fundamentals. The book is broken up into sections, each building from the last. By the end of the book, the reader will have a firm grasp on the concepts of game development for iOS devices. The book offers real world examples and actual games the reader can code and play and is aimed at people who understand programming concepts but are new to iOS game development.

Complete Maya Programming David Gould, 2003-01-07 Learning Maya, the world's leading 3D animation and effects package, is a challenge, especially for those who want to master Maya's versatile programming features in addition to its built-in tools. Finally, here is a practical, step-by-step guide that shows how to use Maya to its fullest potential, beginning with the basics. Readers of Complete Maya Programming will first gain a thorough understanding of Maya's inner workings, and then learn how to customize and extend Maya with scripts and plugins that take control and productivity to new levels. Users new to programming can apply Maya's easy scripting language MEL (Maya Embedded Language), while more advanced users can work with the C++ API (Application Programming Interface). Both a fundamental tutorial for Maya beginners and a solid reference for experienced developers, Complete Maya Programming is every user's guide to Maya mastery. * Provides a multitude of real-world examples illustrating applications of Maya programming. * Demonstrates how to use MEL to control Maya, customize its interface, automate procedures, and more * Details how to use the C++ API to modify Maya functionality and develop tools and features to meet any need * Explains when to use MEL, when to use the C++ API, and how to use them together * Ideal for technical directors, developers, or anyone wishing to master Maya * Provides a storehouse of MEL scripts and C++ source code, glossary, and list of resources, available at www.davidgould.com

Parallel Computer Architecture David Culler, Jaswinder Pal Singh, Anoop Gupta, 1999 This book outlines a set of issues that are critical to all of parallel architecture--communication latency, communication bandwidth, and coordination of cooperative work (across modern designs). It describes the set of techniques available in hardware and in software to address each issues and explore how the various techniques interact.

Entropy Coders of the H.264/AVC Standard Xiaohua Tian, Thinh M. Le, Yong Lian, 2010-10-17 This book presents a collection of algorithms and VLSI architectures of entropy (or statistical) codecs of recent video compression standards, with focus on the H.264/AVC standard. For any visual data compression scheme, there exists a combination of two, or all of the following three stages: spatial, temporal, and statistical compression. General readers are first introduced with the various algorithms of the statistical coders. The VLSI implementations are also reviewed and discussed. Readers with limited hardware design background are also introduced with a design methodology starting from performance-complexity analyses to software/hardware co-simulation. A typical design of the Contextbased Adaptive Binary Arithmetic Coding (CABAC) encoder is also presented in details. To support System-on-Chip design environment, the CABAC design is wrapped with a SoC-based Wishbone system bus interface.

Essentials of Computer Architecture, Second Edition Douglas Comer, 2017-01-06 This easy to read textbook provides an

introduction to computer architecture, while focusing on the essential aspects of hardware that programmers need to know. The topics are explained from a programmer's point of view, and the text emphasizes consequences for programmers. Divided in five parts, the book covers the basics of digital logic, gates, and data paths, as well as the three primary aspects of architecture: processors, memories, and I/O systems. The book also covers advanced topics of parallelism, pipelining, power and energy, and performance. A hands-on lab is also included. The second edition contains three new chapters as well as changes and updates throughout.

Windows Server 2008 Server Core Administrator's Pocket Consultant Mitch Tulloch, Windows Server Core Team at Microsoft, 2008-09-17 Portable and precise, this pocket-sized guide delivers ready answers for administering your Server Core installation. Zero in on essential support and maintenance tasks using quick-reference tables, instructions, and lists. You'll get the focused information you need to solve problems and get the job done—whether at your desk or in the field! Get fast facts to: Plan your installation and perform initial configuration Deploy server roles and features Set up Active Directory Domain Services and domain controllers Install, manage, and troubleshoot DHCP and DNS servers Manage IIS 7.0 roles, services, and dependencies Implement File, Print, Hyper-V, streaming media, and other roles Administer services, devices, and processes from the command line Configure and use remote-management tools Monitor, update, and maintain Server Core

Parallel Programming Thomas Rauber, Gudula Rünger, 2013-06-13 Innovations in hardware architecture, like hyper-threading or multicore processors, mean that parallel computing resources are available for inexpensive desktop computers. In only a few years, many standard software products will be based on concepts of parallel programming implemented on such hardware, and the range of applications will be much broader than that of scientific computing, up to now the main application area for parallel computing. Rauber and Rünger take up these recent developments in processor architecture by giving detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers. Their book is structured in three main parts, covering all areas of parallel computing: the architecture of parallel systems, parallel programming models and environments, and the implementation of efficient application algorithms. The emphasis lies on parallel programming techniques needed for different architectures. For this second edition, all chapters have been carefully revised. The chapter on architecture of parallel systems has been updated considerably, with a greater emphasis on the architecture of multicore systems and adding new material on the latest developments in computer architecture. Lastly, a completely new chapter on general-purpose GPUs and the corresponding programming techniques has been added. The main goal of the book is to present parallel programming techniques that can be used in many situations for a broad range of application areas and which enable the reader to develop correct and efficient parallel programs. Many examples and exercises are provided to show how to apply the techniques. The book can be used as both a textbook for students and a reference book for professionals. The material presented has been used for courses in parallel programming at different universities for many years.

Principles of Distributed Systems Roberto Baldoni, Paola Flocchini, Ravindran Binoy, 2012-12-15 This book constitutes the refereed proceedings of the 16th International Conference on Principles of Distributed Systems, OPODIS 2012, held in Rome, Italy, in December 2012. The 24 papers presented were carefully reviewed and selected from 89 submissions. The conference is an international forum for the exchange of state-of-the-art knowledge on distributed computing and systems. Papers were sought

soliciting original research contributions to the theory, specification, design and implementation of distributed systems.

The Cache Memory Book Jim Handy,1998-01-13 The Second Edition of The Cache Memory Book introduces systems designers to the concepts behind cache design. The book teaches the basic cache concepts and more exotic techniques. It leads readers through some of the most intricate protocols used in complex multiprocessor caches. Written in an accessible, informal style, this text demystifies cache memory design by translating cache concepts and jargon into practical methodologies and real-life examples. It also provides adequate detail to serve as a reference book for ongoing work in cache memory design. The Second Edition includes an updated and expanded glossary of cache memory terms and buzzwords. The book provides new real world applications of cache memory design and a new chapter on cachetricks. Illustrates detailed example designs of caches Provides numerous examples in the form of block diagrams, timing waveforms, state tables, and code traces Defines and discusses more than 240 cache specific buzzwords, comparing in detail the relative merits of different design methodologies Includes an extensive glossary, complete with clear definitions, synonyms, and references to the appropriate text discussions

Billboard ,2011-01-08 In its 114th year, Billboard remains the world's premier weekly music publication and a diverse digital, events, brand, content and data licensing platform. Billboard publishes the most trusted charts and offers unrivaled reporting about the latest music, video, gaming, media, digital and mobile entertainment issues and trends.

Parallel and Distributed Computing: Applications and Technologies Kim-Meow Liew,Hong Shen,Simon See,Wentong Cai,Pingzhi Fan,Susumu Horiguchi,2004-12-02 This book constitutes the refereed proceedings of the 5th International Conference on Parallel and Distributed Computing, Applications and Technologies; PDCAT 2004, held in Singapore in December 2004. The 173 papers presented were carefully reviewed and selected from 242 submissions. The papers focus on parallel and distributed computing from the perspectives of algorithms, networking and architecture, software systems and technologies, and applications. Besides classical topics from high performance computing, major recent developments are addressed, such as molecular computing, data mining, knowledge discovery, optical networks, secure computing and communications, wireless networks, mobile computing, component-based systems, Internet computing, and Web Technologies.

Network and Parallel Computing Erik Altman,Weisong Shi,2011-10-18 This book constitutes the refereed proceedings of the 8th IFIP International Conference on Network and Parallel Computing, NPC 2011, held in Changsha, China, in October 2011. The 28 papers presented were carefully reviewed selected from 54 submissions. The papers are organized in the following topical sections: filesystems and data, network and parallel algorithms, cluster and grid, trust and authentication, and monitor, diagnose, and then optimize.

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