Pixel Stream

Paul Leroux

Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,2002

Image Analysis and Recognition Mohamed Kamel,Aurélio Campilho,2005-10-10 ICIAR 2005, the International Conference on Image Analysis and Recognition, was the second ICIAR conference, and was held in Toronto, Canada. ICIAR is organized annually, and alternates between Europe and North America. ICIAR 2004 was held in Porto, Portugal. The idea of o?ering these conferences came as a result of discussion between researchers in Portugal and Canada to encourage collaboration and exchange, mainly between these two countries, but also with the open participation of other countries, addressing recent advances in theory, methodology and applications. TheresponsetothecallforpapersforICIAR2005wasencouraging.From295 full papers submitted, 153 were ?nally accepted (80 oral presentations, and 73 posters). The review process was carried out by the Program Committee m-bersandotherreviewers;allareexpertsinvariousimageanalysisandrecognition areas. Each paper was reviewed by at least two reviewers, and also checked by the conference co-chairs. The high quality of the papers in these proceedings is attributed ?rst to the authors,and second to the quality of the reviews provided by the experts. We would like to thank the authors for responding to our call, andwewholeheartedlythankthe reviewersfor theirexcellentwork,andfortheir timely response. It is this collective e?ort that resulted in the strong conference program and high-quality proceedings in your hands.

Image Processing Using FPGAs Donald Bailey,2019-06-11 This book presents a selection of papers representing current research on using field programmable gate arrays (FPGAs) for realising image processing algorithms. These papers are reprints of papers selected for a Special Issue of the Journal of Imaging on image processing using FPGAs. A diverse range of topics is covered, including parallel soft processors, memory management, image filters, segmentation, clustering, image analysis, and image compression. Applications include traffic sign recognition for autonomous driving, cell detection for histopathology, and video compression. Collectively, they represent the current state-of-the-art on image processing using FPGAs.

The Streaming Adventures of Pixel and Pete Michael Jensen,2024-01-12

Big C++ Cay S. Horstmann,2020-08-04 Big C++: Late Objects, 3rd Edition focuses on the essentials of effective learning and is suitable for a two-semester introduction to programming sequence. This text requires no prior programming experience and only a modest amount of high school algebra. It provides an approachable introduction to fundamental programming techniques and design skills, helping students master basic concepts and become competent coders. The second half covers algorithms and data structures at a level suitable for beginning students. Horstmann and Budd combine their professional and academic experience to guide the student from the basics to more advanced topics and contemporary applications such as GUIs and XML programming. More than a reference, Big C++ provides well-developed exercises, examples, and case studies that engage students in the details of useful C++ applications. Choosing the enhanced eText format allows students to develop their coding skills using targeted, progressive interactivities designed to integrate with the eText. All sections include built-in activities, open-ended review exercises, programming exercises, and projects to help students practice programming and build confidence. These activities go far beyond simplistic multiple-choice questions and animations. They have been designed to guide students along a learning path for mastering the complexities of programming. Students demonstrate comprehension of programming structures, then practice programming with simple steps in scaffolded settings, and finally write complete, automatically graded programs. The perpetual access VitalSource Enhanced eText, when integrated with your school's learning management system, provides the capability to monitor student progress in VitalSource SCORECenter and track grades for homework or participation. *Enhanced eText and interactive functionality available through select vendors and may require LMS integration approval for SCORECenter.

Design for Embedded Image Processing on FPGAs Donald G. Bailey,2023-08-08 Design for Embedded Image Processing on FPGAs Bridge the gap between software and hardware with this foundational design reference Field-programmable gate arrays (FPGAs) are integrated circuits designed so that configuration can take place. Circuits of this kind play an integral role in processing images, with FPGAs increasingly embedded in digital cameras and other devices that produce visual data outputs for subsequent realization and compression. These uses of FPGAs require specific design processes designed to mediate smoothly between hardware and processing algorithm. Design for Embedded Image Processing on FPGAs provides a comprehensive overview of these processes and their applications in embedded image processing. Beginning with an overview of image processing and its core principles, this book discusses specific design and computation techniques, with a smooth progression from the foundations of the field to its advanced principles. Readers of the second edition of Design for Embedded Image Processing on FPGAs will also find: Detailed discussion of image processing techniques including point operations, histogram operations, linear transformations, and more New chapters covering Deep Learning algorithms and Image and Video Coding Example applications throughout to ground principles and demonstrate techniques Design for Embedded Image Processing on FPGAs is ideal for engineers and academics working in the field of Image Processing, as well as graduate students studying Embedded Systems Engineering, Image Processing, Digital Design, and related fields.

Computer Graphics James D. Foley,1996 On computer graphics

Pattern Recognition and Image Analysis Joao Miguel Sanches,Luisa Micó,Jaime Cardoso,2013-05-23 This book constitutes the refereed proceedings of the 6th Iberian Conference on Pattern Recognition and Image Analysis, IbPRIA 2013, held in Funchal, Madeira, Portugal, in June 2013. The 105 papers (37 oral and 68 poster ones) presented were carefully reviewed and selected from 181 submissions. The papers are organized in topical sections on computer vision, pattern recognition, image and signal, applications.

FPGAs: World Class Designs Clive Maxfield,2009-02-24 All the design and development inspiration and direction a harware engineer needs in one blockbuster book! Clive Max Maxfield renowned author, columnist, and editor of PL DesignLine has selected the very best FPGA design material from the Newnes portfolio and has compiled it into this volume. The result is a book covering the gamut of FPGA design from design fundamentals to optimized layout techniques with a strong pragmatic emphasis. In addition to specific design techniques and practices, this book also discusses various approaches to solving FPGA design problems and how to successfully apply theory to actual design tasks. The material has been selected for its timelessness as well as for its relevance to contemporary FPGA design issues. Contents Chapter 1 Alternative FPGA Architectures Chapter 2 Design Techniques, Rules, and Guidelines Chapter 3 A VHDL Primer: The Essentials Chapter 4 Modeling Memories Chapter 5 Introduction to Synchronous State Machine Design and Analysis Chapter 6 Embedded Processors Chapter 7 Digital Signal Processing Chapter 8 Basics of Embedded Audio Processing Chapter 9 Basics of Embedded Video and Image Processing Chapter 10 Programming Streaming FPGA Applications Using Block Diagrams In Simulink Chapter 11 Ladder and functional block programming Chapter 12 Timers *Hand-picked content selected by Clive Max Maxfield, character, luminary, columnist, and author *Proven best design practices for FPGA development, verification, and low-power *Case histories and design examples get you off and running on your current project

Deep Learning on Edge Computing Devices Xichuan Zhou,Haijun Liu,Cong Shi,Ji Liu,2022-02-02 Deep Learning on Edge Computing Devices: Design Challenges of Algorithm and Architecture focuses on hardware architecture and embedded deep learning, including neural networks. The title helps researchers maximize the performance of Edge-deep learning models for mobile computing and other applications by presenting neural network algorithms and hardware design optimization approaches for Edge-deep learning. Applications are introduced in each section, and a comprehensive example, smart surveillance cameras, is presented at the end of the book, integrating innovation in both algorithm and hardware architecture. Structured into three parts, the book covers core concepts, theories and algorithms and architecture optimization. This book provides a solution for researchers looking to maximize the performance of deep learning models on Edge-computing devices through algorithm-hardware co-design. Focuses on hardware architecture and embedded deep learning, including neural networks Brings together neural network algorithm and hardware design optimization approaches to deep learning, alongside real-world applications Considers how Edge computing solves privacy, latency and power consumption concerns related to the use of the Cloud Describes how to maximize the performance of deep learning on Edge-computing devices Presents the latest research on neural network compression coding, deep learning algorithms, chip co-design and intelligent monitoring

FPGA Prototyping by VHDL Examples Pong P. Chu,2017-10-12 A hands-on introduction to FPGA prototyping and SoC design This Second Edition of the popular book follows the same "learning-by-doing" approach to teach the fundamentals and practices of VHDL synthesis and FPGA prototyping. It

uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC (system on a chip) framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RT (register transfer) level modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an introductory text, the examples are developed in a rigorous manner, and the derivations follow strict design guidelines and coding practices used for large, complex digital systems. The new edition is completely updated. It presents the hardware design in the SoC context and introduces the hardware-software co-design concept. Instead of treating examples as isolated entities, the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software "programmability" and develop complex and interesting embedded system projects. The revised edition: Adds four general-purpose IP cores, which are multi-channel PWM (pulse width modulation) controller, I2C controller, SPI controller, and XADC (Xilinx analog-to-digital converter) controller. Introduces a music synthesizer constructed with a DDFS (direct digital frequency synthesis) module and an ADSR (attack-decay-sustain-release) envelop generator. Expands the original video controller into a complete stream-based video subsystem that incorporates a video synchronization circuit, a test pattern generator, an OSD (on-screen display) controller, a sprite generator, and a frame buffer. Introduces basic concepts of software-hardware co-design with Xilinx MicroBlaze MCS soft-core processor. Provides an overview of bus interconnect and interface circuit. Introduces basic embedded system software development. Suggests additional modules and peripherals for interesting and challenging projects. The FPGA Prototyping by VHDL Examples, Second Edition makes a natural companion text for introductory and advanced digital design courses and embedded system course. It also serves as an ideal self-teaching guide for practicing engineers who wish to learn more about this emerging area of interest.

Radiation Tolerant Electronics Paul Leroux,2019-08-26 Research on radiation-tolerant electronics has increased rapidly over the past few years, resulting in many interesting approaches to modeling radiation effects and designing radiation-hardened integrated circuits and embedded systems. This research is strongly driven by the growing need for radiation-hardened electronics for space applications, high-energy physics experiments such as those on the Large Hadron Collider at CERN, and many terrestrial nuclear applications including nuclear energy and nuclear safety. With the progressive scaling of integrated circuit technologies and the growing complexity of electronic systems, their susceptibility to ionizing radiation has raised many exciting challenges, which are expected to drive research in the coming decade. In this book we highlight recent breakthroughs in the study of radiation effects in advanced semiconductor devices, as well as in high-performance analog, mixed signal, RF, and digital integrated circuits. We also focus on advances in embedded radiation hardening in both FPGA and microcontroller systems and apply radiation-hardened embedded systems for cryptography and image processing, targeting space applications.

Beginning Microsoft Kinect for Windows SDK 2.0 Mansib Rahman,2017-08-12 Develop applications in Microsoft Kinect 2 using gesture and speech recognition, scanning of objects in 3D, and body tracking. Create motion-sensing applications for entertainment and practical uses, including for commercial products and industrial applications. Beginning Microsoft Kinect for Windows SDK 2.0 is dense with code and examples to ensure that you understand how to build Kinect applications that can be used in the real world. Techniques and ideas are presented to facilitate incorporation of the Kinect with other technologies. What You Will Learn Set up Kinect 2 and a workspace for Kinect application development Access audio, color, infrared, and skeletal data streams from Kinect Use gesture and speech recognition Perform computer vision manipulations on image data streams Develop Windows Store apps and Unity3D applications with Kinect 2 Take advantage of Kinect Fusion (3D object mapping technology) and Kinect Ripple (Kinect projector infotainment system) Who This Book Is For Developers who want to include the simple but powerful Kinect technology into their projects, including amateurs and hobbyists, and professional developers

Beginning Kinect Programming with the Microsoft Kinect SDK Jarrett Webb,James Ashley,2012-06-12 Beginning Kinect Programming with the Microsoft Kinect SDK gets you up and running developing Kinect applications for your PC using Microsoft tools and the official SDK. You will have a working Kinect program by the end of the first chapter! The following chapters will open up the secrets of three-dimensional vision, skeleton tracking, audio through the Kinect, and more. Examples illustrate the concepts in the form of simple games that react to your body movements. The result is a fun read that helps you learn one of the hottest technologies out there today. Beginning Kinect Programming with the Microsoft Kinect SDK also provides building blocks and ideas for mashing up the Kinect with other technologies to create art, interactive games, 3D models and enhanced office automation. You'll learn the fundamental code basic to almost all Kinect applications. You'll learn to integrate that code with other tools and manipulate data to create amazing Kinect applications. Beginning Kinect Programming with the Microsoft Kinect SDK is your gateway into the exciting world of three-dimensional, real-time computer interaction. Helps you create a proper development environment for Kinect applications. Covers the basics of three-dimensional vision, skeleton tracking, gesture recognition, and audio Provides fun examples that keep you engaged and learning

Multicomputers and Image Processing Kendall Preston,1982-01-01 Multicomputers and Image Processing: Algorithms and Programs is the second of a set presenting papers from the third meeting held in Madison, Wisconsin on May 27-30, 1981. The workshop explores the large and powerful multicomputer arrays and networks, with particular emphasis on the related aspects of developing algorithms and programs for multicomputer architectures. Separating 33 papers into chapters, this book reflects the three major aspects of the problem: user algorithms and programs; higher level languages; and multicomputer architectures. The first chapters present specific, larger structure, as well as whole program algorithms and their respective applications. Other chapters describe the important high-level programming of Fortran-based language for the massively parallel processors and the set-theory-based language for expressing the structural image processing and perceptual operations effected by Cytocomputer and other cellular-array-motivated architectures. The concluding chapters examine several major types of computer architectures that are being developed for large parallel-serial multicomputer systems. This book is of great value to computer programmers and scientists.

Languages and Compilers for Parallel Computing Eduard Ayguadé,2006-12-22 This book constitutes the thoroughly refereed post-proceedings of the 18th International Workshop on Languages and Compilers for Parallel Computing, LCPC 2005, held in Hawthorne, NY, USA in October 2005. The 26 revised full papers and eight short papers presented were carefully selected during two rounds of reviewing and improvement. The papers are organized in topical sections.

A New Paradigm of Knowledge Engineering by Soft Computing Liya Ding,2001 Soft computing (SC) consists of several computing paradigms, including neural networks, fuzzy set theory, approximate reasoning, and derivative-free optimization methods such as genetic algorithms. The integration of those constituent methodologies forms the core of SC. In addition, the synergy allows SC to incorporate human knowledge effectively, deal with imprecision and uncertainty, and learn to adapt to unknown or changing environments for better performance. Together with other modern technologies, SC and its applications exert unprecedented influence on intelligent systems that mimic human intelligence in thinking, learning, reasoning, and many other aspects. Knowledge engineering (KE), which deals with knowledge acquisition, representation, validation, inferencing, explanation, and maintenance, has made significant progress recently, owing to the indefatigable efforts of researchers. Undoubtedly, the hot topics of data mining and knowledge/data discovery have injected new life into the classical AI world. This book tells readers how KE has been influenced and extended by SC and how SC will be helpful in pushing the frontier of KE further. It is intended for researchers and graduate students to use as a reference in the study of knowledge engineering and intelligent systems. The reader is expected to have a basic knowledge of fuzzy logic, neural networks, genetic algorithms, and knowledge-based systems. Contents: Knowledge Engineering and Soft Computing OCo An Introduction (L-Y Ding); Fuzzy Knowledge-Based Systems: Linguistic Integrity: A Framework for Fuzzy Modeling OCo AFRELI Algorithm (J Espinosa & J Vandewalle); A New Approach to Acquisition of Comprehensible Fuzzy Rules (H Ohno & T Furuhashi); Fuzzy Rule Generation with Fuzzy Singleton-Type Reasoning Method (Y Shi & M Mizumoto); Antecedent Validity Adaptation Principle for Table Look-Up Scheme (P-T Chan & A B Rad); Fuzzy Spline Interpolation in Sparse Fuzzy Rule Bases (M F Kawaguchi & M Miyakoshi); Revision Principle Applied for Approximate Reasoning (L-Y Ding et al.); Handling Null Queries with Compound Fuzzy Attributes (S-L Wang & Y-J Tsai); Fuzzy System Description Language (K Otsuka et al.); Knowledge Representation, Integration, and Discovery by Soft Computing: Knowledge Representation and Similarity Measure in Learning a Vague Legal Concept (M Q Xu et al.); Trend Fuzzy Sets and Recurrent Fuzzy Rules for Ordered Dataset Modelling (J F Baldwin et al.); Approaches to the Design of Classification Systems from Numerical Data

and Linguistic Knowledge (H Ishibuchi et al.); A Clustering Based on Self-Organizing Map and Knowledge Discovery by Neural Network (K Nakagawa et al.); Probabilistic Rough Induction (J-Z Dong et al.); Data Mining via Linguistic Summaries of Databases: An Interactive Approach (J Kacprzyk & S Zadrozny); and other papers. Readership: Graduate students, researchers and lecturers in knowledge engineering and soft computing.

Windows 8. 1 Apps with XAML and C# Sams Teach Yourself in 24 Hours Adam Nathan,2014 In just 24 sessions of one hour or less, Sams Teach Yourself Windows 8.1 Apps with XAML and C# in 24 Hours will help you build compelling apps for Windows 8.1 devices of all kinds.

XAML Unleashed Adam Nathan,2014-12-22 Full Color: Figures and code appear as they do in Visual Studio. If you want to write Windows apps with XAML, one person can help you more than anyone else: Adam Nathan. He has built a well-deserved reputation as the world's #1 expert on putting it to work. Now, he's written the definitive, practical XAML tutorial and reference: XAML Unleashed. Nathan answers the questions you're most likely to ask, walks through the tasks you're sure to perform, and helps you avoid problems as you use XAML. You'll learn how to create effective user interfaces for line-of-business apps, consumer apps, reusable controls, or anything else. These techniques will be invaluable whether you're creating universal Windows apps or working with Silverlight or WPF. XAML Unleashed is packed with C# and XAML code examples that are fully color-coded to match their appearance in Visual Studio--the same approach that has made Nathan's previous Unleashed books so popular. Detailed information on how to... Understand and apply XAML's syntax, namespaces, and keywords Organize controls and other elements in a smooth and intuitive user interface Make the most of XAML's rich controls for content, items, images, text, and media Build exceptionally powerful user and custom controls Master reliable and efficient ways to mix XAML with procedural code Extend XAML with type converters, markup extensions, and other third-party desktop classes Use data binding to link and synchronize controls with in-memory representations of data Leverage XAML's support for binary and logical resources Use styles, templates, and visual states to radically redesign controls without sacrificing their built-in functionality Access the Windows animation library to create stunning theme transitions and animations Build custom panels to enforce consistency in unusual user interfaces Understand subtle changes in XAML's behavior across different Microsoft UI frameworks

Recent Advances in Sensing Technology Gourab Sen Gupta,Yueh-Min Ray Huang,2009-10-01 This Special Issue titled Recent Advances in Sensing Technology in the book series of Lecture Notes in Electrical Engineering contains the extended version of the papers selected from those that were presented at the 3rd International Conference on Sensing Technology (ICST 2008) which was held in November 30 to December 3, 2008 at National Cheng-Kung University, Tainan, Taiwan. A total of 131 papers were presented at ICST 2008, of which 19 papers have been selected for this special issue. This Special Issue has focussed on the recent advancements of the different aspects of sensing technology, i.e. information processing, adaptability, recalibration, data fusion, validation, high reliability and integration of novel and high performance sensors. The advancements are in the areas of magnetic, ultrasonic, vision and image sensing, wireless sensors and network, microfluidic, tactile, gyro, flow, surface acoustic wave, humidity, gas, MEMS thermal and ultra-wide band. While future interest in this field is ensured by the constant supply of emerging modalities, techniques and engineering solutions, many of the basic concepts and strategies have already matured and now offer opportunities to build upon.

Uncover the mysteries within Explore with is enigmatic creation, Embark on a Mystery with **Pixel Stream** . This downloadable ebook, shrouded in suspense, is available in a PDF format (Download in PDF: *). Dive into a world of uncertainty and anticipation. Download now to unravel the secrets hidden within the pages.

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