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**Weidong Liu,Chenming Hu**

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**Preparing Winning Proposals : Infoscan Inc. Presents a Seminar for Consulting Architects and Engineers, Management Consultants and Others who Depend on Bids and Proposals to Develop Business** Infoscan Ltd,Kenneth F. Watson,1982

*Who's who of Southern Africa* ,1993 Vols. for 1967-70 include as a section: Who's who of Rhodesia, Mauritius, Central and East Africa.

**Research and Comply with Regulatory Requirements** Futura Group Pty Ltd,2014-07-15

Choosing Sites Logotron Ltd,1988-08-01

*Lloyd's Maritime Directory* ,1989

**FinFETs and Other Multi-Gate Transistors** J.-P. Colinge,2008 This book explains the physics and properties of multi-gate field-effect transistors (MuGFETs), how they are made and how circuit designers can use them to improve the performances of integrated circuits. It covers the emergence of quantum effects due to the reduced size of the devices and describes the evolution of the MOS transistor from classical structures to SOI (silicon-on-insulator) and then to MuGFETs.

**Drugs in Pregnancy and Lactation** Gerald G. Briggs,Roger K. Freeman,Sumner J. Yaffe,2012-03-28 An A-Z listing of drugs by generic name. Each monograph summarizes the known and/or possible effects of the drug on the fetus. It also summarizes the known/possible passage of the drug into the human breast milk. A careful and exhaustive summarization of the world literature as it relates to drugs in pregnancy and lactation. Each monograph contains six parts: generic US name, Pharmacologic class, Risk factor, Fetal risk summary, Breast feeding summary, References

**Electric Relays** Vladimir Gurevich,2018-10-03 Electric relays pervade the electronics that dominate our world. They exist in many forms, fulfill many roles, and each have their own behavioral nuances and peculiarities. To date, there exists no comprehensive reference surveying the broad spectrum of electric relays, save one-Electric Relays: Principles and Applications. This ambitious work is not only unique in its scope, but also in its practical approach that focuses on the operational and functional aspects rather than on theory and mathematics. Accomplished engineer Dr. Vladimir Gurevich builds the presentation from first principles, unfolding the concepts and constructions via discussion of their historical development from the earliest ideas to modern technologies. He uses a show-not-tell approach that employs nearly 1300 illustrations and reveals valuable insight based on his extensive experience in the field. The book begins with the basic principles of relay construction and the major functional parts, such as contact and magnetic systems. Then, it devotes individual chapters to the various types of relays. The author describes the principles of function and construction for each type as well as features of several relays belonging to a type that operate on different principles. Remarkably thorough and uniquely practical, *Electric Relays: Principles and Applications* serves as the perfect introduction to the plethora of electric relays and offers a quick-reference guide for the experienced engineer.

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*Berthold Auerbach's deutscher Volkskalender* ,1867

**MOSFET Modeling & BSIM3 User's Guide** Yuhua Cheng,Chenming Hu,2007-05-08 Circuit simulation is essential in integrated circuit design, and the accuracy of circuit simulation depends on the accuracy of the transistor model. BSIM3v3 (BSIM for Berkeley Short-channel IGFET Model) has been selected as the first MOSFET model for standardization by the Compact Model Council, a consortium of leading companies in semiconductor and design tools. In the next few years, many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3. This will require many device engineers and most circuit designers to learn the basics of BSIM3. *MOSFET Modeling & BSIM3 User's Guide* explains the detailed physical effects that are important in modeling MOSFETs, and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters. It is the first book devoted to BSIM3. It treats the BSIM3 model in detail as used in digital, analog and RF circuit design. It covers the complete set of models, i.e., I-V model, capacitance model, noise model, parasitics model, substrate current model, temperature effect model and non quasi-static model. *MOSFET Modeling & BSIM3 User's Guide* not only addresses the

device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital/analog circuit design, RF modeling, statistical modeling, and technology prediction. This book is written for circuit designers and device engineers, as well as device scientists worldwide. It is also suitable as a reference for graduate courses and courses in circuit design or device modelling. Furthermore, it can be used as a textbook for industry courses devoted to BSIM3. MOSFET Modeling & BSIM3 User's Guide is comprehensive and practical. It is balanced between the background information and advanced discussion of BSIM3. It is helpful to experts and students alike.

**A Practical Introduction to PSL** Cindy Eisner, Dana Fisman, 2007-06-19 This book describes the Property Specification Language PSL, recently standardized as IEEE Standard 1850-2005. PSL was developed to fulfill the following requirements: easy to learn, write, and read; concise syntax; rigorously well-defined formal semantics; expressive power, permitting the specification for a large class of real world design properties; known efficient underlying algorithms in simulation, as well as formal verification. Basic features are covered, as well as advanced topics such as the use of PSL in multiply-clocked designs. A full chapter is devoted to common errors, gathered through the authors' many years of experience in using and teaching the language.

Silicon Compilation Daniel D. Gajski, 1988

**BSIM4 and MOSFET Modeling for IC Simulation** Weidong Liu, Chenming Hu, 2011 This book presents the art of advanced MOSFET modeling for integrated circuit simulation and design. It provides the essential mathematical and physical analyses of all the electrical, mechanical and thermal effects in MOS transistors relevant to the operation of integrated circuits. Particular emphasis is placed on how the BSIM model evolved into the first ever industry standard SPICE MOSFET model for circuit simulation and CMOS technology development. The discussion covers the theory and methodology of how a MOSFET model, or semiconductor device models in general, can be implemented to be robust and efficient, turning device physics theory into a production-worthy SPICE simulation model. Special attention is paid to MOSFET characterization and model parameter extraction methodologies, making the book particularly useful for those interested or already engaged in work in the areas of semiconductor devices, compact modeling for SPICE simulation, and integrated circuit design.

**SystemVerilog Assertions and Functional Coverage** Ashok B. Mehta, 2016-05-11 This book provides a hands-on, application-oriented guide to the language and methodology of both SystemVerilog Assertions and SystemVerilog Functional Coverage. Readers will benefit from the step-by-step approach to functional hardware verification using SystemVerilog Assertions and Functional Coverage, which will enable them to uncover hidden and hard to find bugs, point directly to the source of the bug, provide for a clean and easy way to model complex timing checks and objectively answer the question 'have we functionally verified everything'. Written by a professional end-user of ASIC/SoC/CPU and FPGA design and Verification, this book explains each concept with easy to understand examples, simulation logs and applications derived from real projects. Readers will be empowered to tackle the modeling of complex checkers for functional verification, thereby drastically reducing their time to design and debug. This updated second edition addresses the latest functional set released in IEEE-1800 (2012) LRM, including numerous additional operators and features. Additionally, many of the Concurrent Assertions/Operators explanations are enhanced, with the addition of more examples and figures. · Covers in its entirety the latest IEEE-1800 2012 LRM syntax and semantics; · Covers both SystemVerilog Assertions and SystemVerilog Functional Coverage language and methodologies; · Provides practical examples of the what, how and why of Assertion Based Verification and Functional Coverage methodologies; · Explains each concept in a step-by-step fashion and applies it to a practical real life example; · Includes 6 practical LABs that enable readers to put in practice the concepts explained in the book.

Transaction-Level Modeling with SystemC Frank Ghenassia, 2006-01-16 Suitable for bookstore catalogue

**Fundamentals of Ultra-Thin-Body MOSFETs and FinFETs** Jerry G. Fossum,Vishal P. Trivedi,2013-08-29 Understand the theory, design and applications of the two principal candidates for the next mainstream semiconductor-industry device with this concise and clear guide to FD/UTB transistors. • Describes FD/SOI MOSFETs and 3-D FinFETs in detail • Covers short-channel effects, quantum-mechanical effects, applications of UTB devices to floating-body DRAM and conventional SRAM • Provides design criteria for nanoscale FinFET and nanoscale thin- and thick-BOX planar FD/SOI MOSFET to help reduce technology development time • Projects potential nanoscale UTB CMOS performances • Contains end-of-chapter exercises. For professional engineers in the CMOS IC field who need to know about optimal non-classical device design and integration, this is a must-have resource.

**More than Moore** Guo Qi Zhang,Alfred van Roosmalen,2010-01-23 In the past decades, the mainstream of microelectronics progression was mainly powered by Moore's law focusing on IC miniaturization down to nano scale. However, there is a fast increasing need for More than Moore (MtM) products and technology that are based upon or derived from silicon technologies, but do not simply scale with Moore's law. This book provides new vision, strategy and guidance for the future technology and business development of micro/nanoelectronics.

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