

Digital Highs

Elizabeth Grossman

Complete Guide to High Dynamic Range Digital Photography Ferrell McCollough, 2008 It's the latest and hottest technique, made possible only through digital. High Dynamic Range photography is the process of taking several pictures of a scene at various exposures, then merging them into one file. So the entire photo can look crisp and detailed, from highlights to midtones to shadows and photographers needn't sacrifice any part of their image. And the best way to master this exciting technology is with this thorough, easy-to-follow, and visually spectacular guide. No other title does justice to these cutting-edge techniques, which actually take the viewer into worlds far beyond normal photography, sometimes even beyond normal human perception. Ferrell McCollough, a widely respected photographer, pushes the boundaries and inspires others to pursue their artistic vision, too. The amazing results simply can't be achieved any other way.

High Tech Trash Elizabeth Grossman, 2006-05-06 The Digital Age was expected to usher in an era of clean production, an alternative to smokestack industries and their pollutants. But as environmental journalist Elizabeth Grossman reveals in this penetrating analysis of high tech manufacture and disposal, digital may be sleek, but it's anything but clean. Deep within every electronic device lie toxic materials that make up the bits and bytes, a complex thicket of lead, mercury, cadmium, plastics, and a host of other often harmful ingredients. High Tech Trash is a wake-up call to the importance of the e-waste issue and the health hazards involved. Americans alone own more than two billion pieces of high tech electronics and discard five to seven million tons each year. As a result, electronic waste already makes up more than two-thirds of the heavy metals and 40 percent of the lead found in our landfills. But the problem goes far beyond American shores, most tragically to the cities in China and India where shiploads of discarded electronics arrive daily. There, they are recycled-picked apart by hand, exposing thousands of workers and community residents to toxics. As Grossman notes, This is a story in which we all play a part, whether we know it or not. If you sit at a desk in an office, talk to friends on your cell phone, watch television, listen to music on headphones, are a child in Guangdong, or a native of the Arctic, you are part of this story. The answers lie in changing how we design, manufacture, and dispose of high tech electronics. Europe has led the way in regulating materials used in electronic devices and in e-waste recycling. But in the United States many have yet to recognize the persistent human health and environmental effects of the toxics in high tech devices. If Silent Spring brought national attention to the dangers of DDT and other pesticides, High Tech Trash could do the same for a new generation of technology's products.

Digital Illusion Clark Dodsworth, 1998 Digital Illusion is the future of entertainment. That future, as seen in this book, is at the intersection of show business and interactivity. It is a future where games, theme-park attractions, and networked virtual worlds are built with seamless, interactive, computer technology, and where exciting new kinds of experience and enjoyment are made possible. It's a future that has already begun! Clark Dodsworth has participated for years in this convergence of the computer and entertainment industries. Here, he gathers prominent contributors from both worlds to describe the design and implementation of computer-based entertainment applications. With striking examples, they show what has been accomplished and preview what is yet to come.

High Density Digital Recording K.H.J. Buschow, G.J. Long, F. Grandjean, 2012-12-06 High density digital magnetic and magneto-optical storage devices are widely used in audio, video, and data processing information technology, as well as in CAD/CAM computer systems. These widespread uses generate a continually increasing demand for both increased information storage densities and capacities, and for reduced access times. Hence, the materials engineering of high density storage media, with a high signal to noise ratio, and the associated design of sophisticated read and write heads, form the basis of major technological research. This research is especially complex because, ideally, the recorded information should be both erasable and, at the same time, secure and accessible over periods of many decades. As a result, research on these complex problems requires a multidisciplinary approach which utilizes the expertise in such widely differing fields as organic, inorganic, and solid state chemistry, metallurgy, solid state physics, electrical and mechanical engineering, and systems analysis. Often, further research specialization is necessary in each of these different disciplines. For instance, solid state physics and chemistry address the problems of crystallographic structure and phase diagram determination, magnetism, and optics, but more advanced research methods, such as high resolution electron microscopy and electronic band structure calculations, are necessary to understand the microstructure of particulate recording media or the electronic spectra of magneto-optical recording media.

High-Speed Digital System Design Stephen H. Hall, Garrett W. Hall, James A. McCall, 2000-09-08 A cutting-edge guide to the theory and practice of high-speed digital system design. An understanding of high-speed interconnect phenomena is essential for digital designers who must deal with the challenges posed by the ever-increasing operating speeds of today's microprocessors. This book provides a much-needed, practical guide to the state of the art of modern digital system design, combining easily accessible explanations with immensely useful problem-solving strategies. Written by three leading Intel engineers, High-Speed Digital System Design clarifies difficult and often neglected topics involving the effects of high frequencies on digital buses and presents a variety of proven techniques and application examples. Extensive appendices, formulas, modeling techniques as well as hundreds of figures are also provided. Coverage includes: * A thorough introduction to the digital aspects of basic transmission line theory * Crosstalk and

nonideal transmission line effects on signal quality and timings * The impact of packages, vias, and connectors on signal integrity * The effects of nonideal return current paths, high frequency power delivery, and simultaneous switching noise * Explanations of how driving circuit characteristics affect the quality of the digital signal * Digital timing analysis at the system level that incorporates high-speed signaling effects into timing budgets * Methodologies for designing high-speed buses and handling the very large number of variables that affect interconnect performance * Radiated emission problems and how to minimize system noise * The practical aspects of making measurements in high-speed digital systems

Teaching the Digital Generation Frank S. Kelly, Ted McCain, Ian Jukes, 2008-09-17 The authors show how traditional industrial-type high schools have failed to meet students' learning needs and explore ten alternative high school models that address 21st-century skills.

High-Speed Analog-to-Digital Conversion Michael J. Demler, 2012-12-02 This book covers the theory and applications of high-speed analog-to-digital conversion. An analog-to-digital converter takes real-world inputs (such as visual images, temperature readings, and rates of speed) and transforms them into digital form for processing by computer. This book discusses the design and uses of such circuits, with particular emphasis on improving the speed of the conversion process and the accuracy of its output--how well the output is a corresponding digital representation of the output*^{b1}input signal. As computers become increasingly interfaced to the outside world, ADC techniques will become ever more important.

Advanced Signal Integrity for High-Speed Digital Designs Stephen H. Hall, Howard L. Heck, 2011-09-20 A synergistic approach to signal integrity for high-speed digital design This book is designed to provide contemporary readers with an understanding of the emerging high-speed signal integrity issues that are creating roadblocks in digital design. Written by the foremost experts on the subject, it leverages concepts and techniques from non-related fields such as applied physics and microwave engineering and applies them to high-speed digital design—creating the optimal combination between theory and practical applications. Following an introduction to the importance of signal integrity, chapter coverage includes: Electromagnetic fundamentals for signal integrity Transmission line fundamentals Crosstalk Non-ideal conductor models, including surface roughness and frequency-dependent inductance Frequency-dependent properties of dielectrics Differential signaling Mathematical requirements of physical channels S-parameters for digital engineers Non-ideal return paths and via resonance I/O circuits and models Equalization Modeling and budgeting of timing jitter and noise System analysis using response surface modeling Each chapter includes many figures and numerous examples to help readers relate the concepts to everyday design and concludes with problems for readers to test their understanding of the material. Advanced Signal Integrity for High-Speed Digital Designs is suitable as a textbook for graduate-level courses on signal integrity, for programs taught in industry for professional engineers, and as a reference for the high-speed digital designer.

High-speed Digital Design Howard W. Johnson, Martin Graham, 1993-01-01 Focused on the field of knowledge lying between digital and analog circuit theory, this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems. The scope of the material covered includes signal reflection, crosstalk, and noise problems which occur in high speed digital machines (above 10 megahertz). This volume will be of practical use to digital logic designers, staff and senior communications scientists, and all those interested in digital design.

High Frequency Continuous Time Filters in Digital CMOS Processes Shanthi Pavan, Yannis Tsididis, 2007-05-08 There is an ever increasing trend towards putting entire systems on a single chip. This means that analog circuits will have to coexist on the same substrate along with massive digital systems. Since technologies are optimized with these digital systems in mind, designers will have to make do with standard CMOS processes in the years to come. We address analog filter design from this perspective. Filters form important blocks in applications ranging from computer disc-drive chips to radio transceivers. In this book, we develop the theory and techniques necessary for the implementation of high frequency (hundreds of megahertz) programmable continuous time filters in standard CMOS processes. Since high density poly-poly capacitors are not available in these technologies, alternative capacitor structures have to be found. Metal-metal capacitors have low specific capacitance. An alternative is to use the (inherently nonlinear) capacitance formed by MOSFET gates. In Chapter 2, we focus on the use of MOS capacitors as integrating elements. A physics-based model which predicts distortion accurately is presented for a two-terminal MOS structure in accumulation. Distortion in these capacitors as a function of signal swing and bias voltage is computed. Chapter 3 reviews continuous-time filter architectures in the light of bias-dependent integrating capacitors. We also discuss the merits and demerits of various CMOS transconductance elements. The problems encountered in designing high frequency programmable filters are discussed in detail.

High Speed Digital Design Hanqiao Zhang, Steven Krooswyk, Jeffrey Ou, 2015-08-17 High Speed Digital Design discusses the major factors to consider in designing a high speed digital system and how design concepts affect the functionality of the system as a whole. It will help you understand why signals act so differently on a high speed digital system, identify the various problems that may occur in the design, and research solutions to minimize their impact and address their root causes. The authors offer a strong foundation that will help you get high speed digital system designs right the first time. Taking a systems design approach, High Speed Digital Design offers a progression from fundamental to advanced concepts, starting with

transmission line theory, covering core concepts as well as recent developments. It then covers the challenges of signal and power integrity, offers guidelines for channel modeling, and optimizing link circuits. Tying together concepts presented throughout the book, the authors present Intel processors and chipsets as real-world design examples. Provides knowledge and guidance in the design of high speed digital circuits Explores the latest developments in system design Covers everything that encompasses a successful printed circuit board (PCB) product Offers insight from Intel insiders about real-world high speed digital design

Digital Control of High-Frequency Switched-Mode Power Converters Luca Corradini, Dragan Maksimović, Paolo Mattavelli, Regan Zane, 2015-07-10

High Speed Digital Transmission Networking Gilbert Held, 1999 Keeping up-to-date with the latest developments in the field, this book provides practical information about the characteristics, operation, utilization and testing of digital networks. Also examines digital network facilities that are available in the US and UK, and North American and European T-Carrier framing and coding formats. The book also includes information on DSL, FT1, FT3 and T3 carriers.

High-density Digital Recording Ford Kalil, 1985

High-density Digital Recording, 1985

Digital Learning in High-Needs Schools Heejung An, David A. Fuentes, 2023-06-21 Digital Learning in High-Needs Schools examines the challenges and affordances that arise when high-needs school communities integrate educational technologies into their unique settings. Although remote, blended, and networked learning are ubiquitous today, a number of cultural, economic, and political realities—from the digital divide and digital literacy to poverty and language barriers—affect our most vulnerable and underresourced teachers and students. This book uses critical theory to compassionately scrutinize and unpack the systemic issues that impact high-needs schools’ implementation of digital learning tools. Incisive sociocultural analyses across fifteen original chapters explore the intersection of society, technology, people, politics, and education in high-needs school contexts. Informed by real-world cases pertaining to technology infrastructure, formative feedback, Universal Design for Learning, and more, these chapters illuminate how best practices emerge from culturally responsive and context-specific foundations.

Signal Integrity and Radiated Emission of High-Speed Digital Systems Spartaco Caniggia, Francescaromana Maradei, 2008-11-20 Before putting digital systems for information technology or telecommunication applications on the market, an essential requirement is to perform tests in order to comply with the limits of radiated emission imposed by the standards. This book provides an investigation into signal integrity (SI) and electromagnetic interference (EMI) problems. Topics such as reflections, crosstalk, switching noise and radiated emission (RE) in high-speed digital systems are covered, which are essential for IT and telecoms applications. The highly important topic of modelling is covered which can reduce costs by enabling simulation data to demonstrate that a product meets design specifications and regulatory limits. According to the new European EMC directive, this can help to avoid the expensive use of large semi-anechoic chambers or open area test sites for radiated emission assessments. Following a short introduction to signalling and radiated interference in digital systems, the book provides a detailed characterization of logic families in terms of static and dynamic characteristic useful for modelling techniques. Crosstalk in multi-coupled line structures are investigated by analytical, graphical and circuit-based methods, and techniques to mitigate these phenomena are provided. Grounding, filtering and shielding with multilayer PCBs are also examined and design rules given. Written by authors with extensive experience in industry and academia. Explains basic conceptual problems from a theoretical and practical point of view by using numerous measurements and simulations. Presents models for mathematical and SPICE-like circuit simulators. Provides examples of using full-wave codes for SI and RE investigations. Companion website containing lists of codes and sample material. Signal Integrity and Radiated Emission of High-Speed Digital Systems is a valuable resource to industrial designers of information technology, telecommunication equipment and automation equipment as well as to development engineers. It will also be of interest to managers and designers of consumer electronics, and researchers in electronics.

High-Velocity Digital Marketing Steven Mark Kahan, 2022-12-06 High-Velocity Digital Marketing provides B2B sales and marketing teams with a blueprint for quickly accelerating revenue growth 67 percent of B2B buyers no longer prefer to interact with sales representatives when making purchasing decisions. Gathering information online is now the hallmark of the buying process. During his 30+ year career, Steven Mark Kahan has developed a digital marketing blueprint that helps companies quickly increase revenue in brutally competitive markets. In High-Velocity Digital Marketing, he lays out how you can: Set in motion a modern marketing strategy to dramatically increase revenue growth in record time Create content that resonates with your ideal target customers Drive more visitors to your website without paying for ads Build digital demand generation campaigns that quickly deliver more leads, sales pipeline, and revenue Improve your website to convert at least 5 percent of traffic to leads Leverage public relations so it contributes to revenue growth Synchronize your sales and marketing teams in order to close prospects faster and at a higher rate Marketing online is no longer one of the ways to market—it is the way to do marketing. High-Velocity Digital Marketing is your essential, easy-to-implement guide to getting found online, providing the most critical information, and getting your buyers to purchase—fast.

High-Speed Digital System Design Anatoly Belous,Vitali Saladukha,2019-11-13 This book describes for readers the entire, interconnected complex of theoretical and practical aspects of designing and organizing the production of various electronic devices, the general and main distinguishing feature of which is the high speed of processing and transmitting of digital signals. The authors discuss all the main stages of design - from the upper system level of the hierarchy (telecommunications system, 5G mobile communications) to the lower level of basic semiconductor elements, printed circuit boards. Since the developers of these devices in practice deal with distorted digital signals that are transmitted against a background of interference, the authors not only explain the physical nature of such effects, but also offer specific solutions as to how to avoid such parasitic effects, even at the design stage of high-speed devices.

Low-Power High-Resolution Analog to Digital Converters Amir Zjajo,José Pineda de Gyvez,2010-10-29 With the fast advancement of CMOS fabrication technology, more and more signal-processing functions are implemented in the digital domain for a lower cost, lower power consumption, higher yield, and higher re-configurability. This has recently generated a great demand for low-power, low-voltage A/D converters that can be realized in a mainstream deep-submicron CMOS technology. However, the discrepancies between lithography wavelengths and circuit feature sizes are increasing. Lower power supply voltages significantly reduce noise margins and increase variations in process, device and design parameters. Consequently, it is steadily more difficult to control the fabrication process precisely enough to maintain uniformity. The inherent randomness of materials used in fabrication at nanoscopic scales means that performance will be increasingly variable, not only from die-to-die but also within each individual die. Parametric variability will be compounded by degradation in nanoscale integrated circuits resulting in instability of parameters over time, eventually leading to the development of faults. Process variation cannot be solved by improving manufacturing tolerances; variability must be reduced by new device technology or managed by design in order for scaling to continue. Similarly, within-die performance variation also imposes new challenges for test methods. In an attempt to address these issues, Low-Power High-Resolution Analog-to-Digital Converters specifically focus on: i) improving the power efficiency for the high-speed, and low spurious spectral A/D conversion performance by exploring the potential of low-voltage analog design and calibration techniques, respectively, and ii) development of circuit techniques and algorithms to enhance testing and debugging potential to detect errors dynamically, to isolate and confine faults, and to recover errors continuously. The feasibility of the described methods has been verified by measurements from the silicon prototypes fabricated in standard 180nm, 90nm and 65nm CMOS technology.

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