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CMOS Data Converters for Communications Mikael Gustavsson, J. Jacob Wikner, Nianxiong Tan, 2006-04-18 CMOS Data Converters for Communications distinguishes itself from other data converter books by emphasizing system-related aspects of the design and frequency-domain measures. It explains in detail how to derive data converter requirements for a given communication system (baseband, passband, and multi-carrier systems). The authors also review CMOS data converter architectures and discuss their suitability for communications. The rest of the book is dedicated to high-performance CMOS data converter architecture and circuit design. Pipelined ADCs, parallel ADCs with an improved passive sampling technique, and oversampling ADCs are the focus for ADC architectures, while current-steering DAC modeling and implementation are the focus for DAC architectures. The principles of the switched-current and the switched-capacitor techniques are reviewed and their applications to crucial functional blocks such as multiplying DACs and integrators are detailed. The book outlines the design of the basic building blocks such as operational amplifiers, comparators, and reference generators with emphasis on the practical aspects. To operate analog circuits at a reduced supply voltage, special circuit techniques are needed. Low-voltage techniques are also discussed in this book. CMOS Data Converters for Communications can be used as a reference book by analog circuit designers to understand the data converter requirements for communication applications. It can also be used by telecommunication system designers to understand the difficulties of certain performance requirements on data converters. It is also an excellent resource to prepare analog students for the new challenges ahead.

CMOS Integrated Analog-to-Digital and Digital-to-Analog

Converters Rudy J. van de Plassche, 2013-04-17 CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes in depth converter specifications like Effective Number of Bits (ENOB), Spurious Free Dynamic Range (SFDR), Integral Non-Linearity (INL), Differential Non-Linearity (DNL) and sampling clock jitter requirements. Relations between these specifications and practical issues like matching of components and offset parameters of differential pairs are derived. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes the requirements of input and signal reconstruction filtering in case a converter is applied into a signal processing system. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes design details of high-speed A/D and D/A converters, high-resolution A/D and D/A converters, sample-and-hold amplifiers, voltage and current references, noise-shaping converters and sigma-delta converters, technology parameters and matching performance, comparators and limitations of comparators and finally testing of converters.

Low-Noise Wide-Band Amplifiers in Bipolar and CMOS

Technologies Zhong Yuan Chong, Willy M.C. Sansen, 2013-03-09 Analog circuit design has grown in importance because so many circuits cannot be realized with digital techniques. Examples are receiver front-ends, particle detector circuits, etc. Actually, all circuits which require high precision, high speed and low power consumption need analog solutions. High precision also needs low noise. Much has been written already on low noise design and optimization for low noise. Very little is available however if the source is not resistive but capacitive or inductive as is the case with antennas or semiconductor detectors. This book provides design

techniques for these types of optimization. This book is thus intended firstly for engineers on senior or graduate level who have already designed their first operational amplifiers and want to go further. It is especially for engineers who do not want just a circuit but the best circuit. Design techniques are given that lead to the best performance within a certain technology. Moreover, this is done for all important technologies such as bipolar, CMOS and BiCMOS. Secondly, this book is intended for engineers who want to understand what they are doing. The design techniques are intended to provide insight. In this way, the design techniques can easily be extended to other circuits as well. Also, the design techniques form a first step towards design automation. Thirdly, this book is intended for analog design engineers who want to become familiar with both bipolar and CMOS technologies and who want to learn more about which transistor to choose in BiCMOS.

Low-Power CMOS Design Anantha Chandrakasan, Robert W. Brodersen, 1998-02-11 This collection of important papers provides a comprehensive overview of low-power system design, from component technologies and circuits to architecture, system design, and CAD techniques. LOW POWER CMOS DESIGN summarizes the key low-power contributions through papers written by experts in this evolving field.

CMOS Cookbook DON LANCASTER, Howard M. Berlin, 1988-01-01 The CMOS Cookbook contains all you need to know to understand and successfully use CMOS (Complementary Metal-Oxide Semiconductor) integrated circuits. Written in a cookbook format that requires little math, this practical, user-oriented book covers all the basics for working with digital logic and many of its end applications. Whether you're a newcomer to logic and electronics or a

senior design engineer, you'll find CMOS Cookbook and its examples helpful as a self-learning guide, a reference handbook, a project-idea book, or a text for teaching others digital logic at the high school through university levels. In the pages of this revised edition, you'll discover:

- *What CMOS is, who makes it, and how the basic transistors, inverters, and logic and transmission gates work
- *CMOS usage rules, power-supply examples, and information on breadboards, state testing, tools, and interfacing
- *Discussions of the latest CMOS devices and sub-families, including the 74C, 74HC, and 74HCT series that streamline TTL and CMOS interfacing
- *An in-depth look at multivibrators - including astable, monostable, and bistable - and linear techniques
- *Clocked-logic designs and the extensive applications of JK and D-type flip-flops
- *A helpful appendix featuring a TTL-to-CMOS conversion chart

CMOS - MEMS Henry Baltes, Oliver Brand, Gary K.

Fedder, Christofer Hierold, Jan G. Korvink, Osamu Tabata, 2013-03-26

This edition of 'CMOS-MEMS' was originally published in the successful series 'Advanced Micro & Nanosystems'. Here, the combination of the globally established, billion dollar chip mass fabrication technology CMOS with the fascinating and commercially promising new world of MEMS is covered from all angles. The book introduces readers to this field and takes them from fabrication technologies and material characterization aspects to the actual applications of CMOS-MEMS - a wide range of miniaturized physical, chemical and biological sensors and RF systems. Vital knowledge on circuit and system integration issues concludes this in-depth treatise, illustrating the advantages of combining CMOS and MEMS in the first place, rather than having a hybrid solution.

CMOS Circuits for Piezoelectric Energy Harvesters Thorsten

Hehn, Yiannos Manoli, 2014-07-30 This book deals with the challenge of exploiting ambient vibrational energy which can be used to power small and low-power electronic devices, e.g. wireless sensor nodes. Generally, particularly for low voltage amplitudes, low-loss rectification is required to achieve high conversion efficiency. In the special case of piezoelectric energy harvesting, pulsed charge extraction has the potential to extract more power compared to a single rectifier. For this purpose, a fully autonomous CMOS integrated interface circuit for piezoelectric generators which fulfills these requirements is presented. Due to these key properties enabling universal usage, other CMOS designers working in the field of energy harvesting will be encouraged to use some of the shown structures for their own implementations. The book is unique in the sense that it highlights the design process from scratch to the final chip. Hence, it gives the designer a comprehensive guide of how to (i) setup an appropriate harvester model to get realistic simulation results, (ii) design the integrated circuits for low power operation, (iii) setup a laboratory measurement environment in order to extensively characterize the chip in combination with the real harvester and finally, (iv) interpret the simulation/measurement results in order to improve the chip performance. Since the dimensions of all devices (transistors, resistors etc.) are given, readers and other designers can easily re-use the presented circuit concepts.

CMOS Capacitive Sensors for Lab-on-Chip Applications Ebrahim Ghafar-Zadeh, Mohamad Sawan, 2010-03-10 1.1 Overview of Lab-on-Chip Laboratory-on-Chip (LoC) is a multidisciplinary approach used for the miniaturization, integration and automation of biological assays or procedures in analytical chemistry [1–3]. Biology and chemistry are experimental sciences that are continuing to evolve and develop new

protocols. Each protocol offers step-by-step laboratory instructions, lists of the necessary equipments and required biological and/or chemical substances [4–7]. A biological or chemical laboratory contains various pieces of equipment used for performing such protocols and, as shown in Fig. 1.1, the engineering aspect of LoC design is aiming to embed all these components in a single chip for single-purpose applications.

1.1.1 Main Objectives of LoC Systems Several clear advantages of this technology over conventional approaches, including portability, full automation, ease of operation, low sample consumption and fast assays time, make LoC suitable for many applications including:

1.1.1.1 Highly Throughput Screening To conduct an experiment, a researcher fills a well with the required biological or chemical analytes and keeps the sample in an incubator for some time to allowing the sample to react properly. Afterwards, any changes can be observed using a microscope. In order to quickly conduct millions of biochemical or pharmacological tests, the researchers will require an automated highly throughput screening (HTS) [8], comprised of a large array of wells, liquid handling devices (e.g., microchannel, micropump and microvalves [9–11]), a fully controllable incubator and an integrated sensor array, along with the appropriate readout system.

CMOS Telecom Data Converters Angel Rodríguez-

Vázquez, Fernando Medeiro, Edmond Janssens, 2013-03-09 CMOS Telecom Data Converters compiles the latest achievements regarding the design of high-speed and high-resolution data converters in deep submicron CMOS technologies. The four types of analog-to-digital converter architectures commonly found in this arena are covered, namely sigma-delta, pipeline, folding/interpolating and flash. For all these types, latest achievements regarding the solution of critical architectural and circuital issues are presented, and illustrated through

IC prototypes with measured state-of-the-art performances. Some of these prototypes are conceived to be employed at the chipset of newest generation wireline modems (ADSL and ADSL+). Others are intended for wireless transceivers. Besides analog-to-digital converters, the book also covers other functions needed for communication systems, such as digital-to-analog converters, analog filters, programmable gain amplifiers, digital filters, and line drivers.

Multi-Threshold CMOS Digital Circuits Mohab Anis, Mohamed Elmasry, 2012-12-06 This excellent survey of state-of-the-art techniques discusses the MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power, while maintaining high performance. It addresses the leakage problem in a number of designs for combinational, sequential, dynamic and current-steering logic.

Linear CMOS RF Power Amplifiers for Wireless Applications

Paulo Augusto Dal Fabbro, Maher Kayal, 2010-06-22 Advances in electronics have pushed mankind to create devices, ranging from - credible gadgets to medical equipment to spacecraft instruments. More than that, modern society is getting used to—if not dependent on—the comfort, solutions, and astonishing amount of information brought by these devices. One ?eld that has continuously bene?ted from those advances is the radio frequency integrated c- cuit (RFIC) design, which in its turn has promoted countless bene?ts to the mankind as a payback. Wireless communications is one prominent example of what the - vances in electronics have enabled and their consequences to our daily life. How could anyone back in the eighties think of the possibilities opened by the wireless local area networks (WLANs) that can be found today in a host of places, such as public libraries, coffee shops, trains, to name just a few? How can a youngster, who lives this

true WLAN experience nowadays, imagine a world without it? This book deals with the design of linear CMOS RF Power Amplifiers (PAs). The RF PA is a very important part of the RF transceiver, the device that enables wireless communications. Two important aspects that are key to keep the advances in RF PA design at an accelerated pace are treated: efficiency enhancement and frequency-tunable capability. For this purpose, the design of two different integrated circuits realized in a 0.11 μm technology is presented, each one addressing a different aspect. With respect to efficiency enhancement, the design of a dynamic supply RF power amplifier is treated, making up the material of Chaps. 2 to 4.

VLSI-SoC: System-on-Chip in the Nanoscale Era – Design, Verification and Reliability Thomas Hollstein, Jaan Raik, Sergei Kostin, Anton Tšertov, Ian O'Connor, Ricardo Reis, 2017-08-31 This book contains extended and revised versions of the best papers presented at the 24th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2016, held in Tallinn, Estonia, in September 2016. The 11 papers included in the book were carefully reviewed and selected from the 36 full papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the latest scientific and industrial results and developments as well as future trends in the field of System-on-Chip (SoC) Design.

Analog Filters in Nanometer CMOS Heimo Uhrmann, Robert Kolm, Horst Zimmermann, 2013-08-15 Starting from the basics of analog filters and the poor transistor characteristics in nanometer CMOS 10 high-performance analog filters developed by the authors in 120 nm and 65 nm CMOS are described extensively. Among them are gm-C filters, current-mode filters, and active filters for system-on-chip

realization for Bluetooth, WCDMA, UWB, DVB-H, and LTE applications. For the active filters several operational amplifier designs are described. The book, furthermore, contains a review of the newest state of research on low-voltage low-power analog filters. To cover the topic of the book comprehensively, linearization issues and measurement methods for the characterization of advanced analog filters are introduced in addition. Numerous elaborate illustrations promote an easy comprehension. This book will be of value to engineers and researchers in industry as well as scientists and Ph.D students at universities. The book is also recommendable to graduate students specializing on nanoelectronics, microelectronics or circuit engineering.

Interface Integrated Circuits, 1991

Smart Adaptive Systems on Silicon Maurizio Valle, 2004-10-18

Intelligent/smart systems have become common practice in many engineering applications. On the other hand, current low cost standard CMOS technology (and future foreseeable developments) makes available enormous potentialities. The next breakthrough will be the design and development of smart adaptive systems on silicon i.e. very power and highly size efficient complete systems (i.e. sensing, computing and actuating actions) with intelligence on board on a single silicon die. Smart adaptive systems on silicon will be able to adapt autonomously to the changing environment and will be able to implement intelligent behaviour and both perceptual and cognitive tasks. At last, they will communicate through wireless channels, they will be battery supplied or remote powered (via inductive coupling) and they will be ubiquitous in our every day life. Although many books deal with research and engineering topics (i.e. algorithms, technology, implementations, etc.) few of them try to bridge the gap

between them and to address the issues related to feasibility, reliability and applications. Smart Adaptive Systems on Silicon, though not exhaustive, tries to fill this gap and to give answers mainly to the feasibility and reliability issues. Smart Adaptive Systems on Silicon mainly focuses on the analog and mixed mode implementation on silicon because this approach is amenable of achieving impressive energy and size efficiency. Moreover, analog systems can be more easily interfaced with sensing and actuating devices.

Instacart for CMOs Kiri Masters, Stefan Jordev, 2021-03-10 With more than 500 major retailers like Albertsons, ALDI, Costco, and CVS now leveraging Instacart's fulfillment capabilities, retail brands are recognizing the huge potential of this channel to elevate their sales. As online grocery shopping continues to be a preferred buying channel, the race is on to meet shoppers where they want to transact. But as a relatively new channel, there is not yet the same level well-documented best practices that we see in more established marketplaces like Amazon. Instacart for CMOs offers the first and most comprehensive look into how other retail brands are operating and maximizing their Instacart sales channel. Dial in your marketing strategy on Instacart before your competitors wake up to the opportunity. Instacart for CMO's is a must read for anyone whose products are listed on Instacart. The book is full of clear, clean, well-written, and actionable advice. Kiri Masters is a true expert on retail marketplaces and knows exactly what it takes to win. -- Jason Retailgeek Goldberg, chief commerce strategy officer, Publicis In Instacart for CMOs, You'll learn: How Instacart has revolutionized online grocery delivery, and what this new state-of-play means for CPG brands. If investing in Instacart is the right move for your brand, and how Instacart should fit amongst your other digital channels. How

Chief Marketing Officers and other executives within leading grocery brands and startups alike are leveraging Instacart as a marketing channel. The book includes exclusive interviews with retail industry executives, discussing their Instacart strategies and insights. Demand generation on Instacart: how to use advertising and promotional tools to achieve meaningful results. Get a practitioner's perspective on making Instacart advertising budget and strategy decisions based on data. The sticky challenge that most brands face around Instacart's position in their P&L. How retailers view Instacart, and how their relationship might affect your brand. How to get the most out of Instacart's platform, as well as a preview into what is likely coming in the near future.

Efficient Design of Variation-Resilient Ultra-Low Energy Digital Processors Hans Reyserhove, Wim Dehaene, 2019-03-27 This book enables readers to achieve ultra-low energy digital system performance. The author's main focus is the energy consumption of microcontroller architectures in digital (sub)-systems. The book covers a broad range of topics extensively: from circuits through design strategy to system architectures. The result is a set of techniques and a context to realize minimum energy digital systems. Several prototype silicon implementations are discussed, which put the proposed techniques to the test. The achieved results demonstrate an extraordinary combination of variation-resilience, high speed performance and ultra-low energy.

Soft-Computing-Based Nonlinear Control Systems Design Singh, Uday Pratap, Tiwari, Akhilesh, Singh, Rajeev Kumar, 2018-02-09 A critical part of ensuring that systems are advancing alongside technology without complications is problem solving. Practical applications of problem-solving theories can model conflict and

cooperation and aid in creating solutions to real-world problems. *Soft-Computing-Based Nonlinear Control Systems Design* is a critical scholarly publication that examines the practical applications of control theory and its applications in problem solving to fields including economics, environmental management, and financial modelling. Featuring a wide range of topics, such as fuzzy logic, nature-inspired algorithms, and cloud computing, this book is geared toward academicians, researchers, and students seeking relevant research on control theory and its practical applications.

IEEE Region 10 International Conference on Electrical and Electronic Technology, 2001 This CD-ROM contains the conference proceedings from the 10th IEEE Region 10 International Conference on Electrical and Electronic Technology.

VLSI Memory Chip Design Kiyoo Itoh, 2013-04-17 A systematic description of microelectronic device design. Topics range from the basics to low-power and ultralow-voltage designs, subthreshold current reduction, memory subsystem designs for modern DRAMs, and various on-chip supply-voltage conversion techniques. It also covers process and device issues as well as design issues relating to systems, circuits, devices and processes, such as signal-to-noise and redundancy.

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