Avmonitor

G. Boulaye,T.R. Lewin

PHM - a programmable hardware monitor Craig S. Anderson,1989 This technical report describes a project undertaken by the graduate students in CS 568, Winter 1989, to design and implement a programmable hardware monitor. The goal of this project was to create a general purpose, highly programmable yet compact design for a hardware monitor to be implemented with custom Complementary Metal Oxide Semiconductor Very Large Scale Integration chips. The architecture is designed for easy extensibility and allows the monitor to be customized for use in a wide variety of computer systems, and to accommodate a broad range of applications. Although the project did not reach fabrication, a great deal of the layout was completed and simulated. The purpose of a hardware monitor is to detect and record hardware events in a computer system for the purposes of performance evaluation. A simple, and much used, approach to performance analysis is trace-driven simulation. Trace-driven simulation depends upon the collection of a trace of all addresses generated during the execution of one or more computer programs. The major problem of this approach is that a large amount of memory is required to store all the trace information. Additionally, methods for collecting address traces are often expensive and intrusive; the process of collecting the trace data may alter the behavior of the computer system while it is being traced. Keywords: Layout.

Lectures on the Measurement and Evaluation of the Performance of Computing Systems Saul Rosen,1976-01-01 The scope of the discussion is limited to certain types of general purpose computing systems whose characteristics involve multiprogramming, multiprocessors, communication lines serving many users, and on-line terminal systems. The goal is to gain a better understanding of computing systems through the techniques of measurement and performance evaluation.

Euro-Par 2004 Parallel Processing Marco Danelutto,Marco Vanneschi,Domenico Laforenza,2004-12-27 Euro-Par Conference Series Euro-Par is an annual series of international conferences dedicated to the p- motion and advancement of all aspectsof parallelcomputing. The major themes can be divided into the broad categories of hardware, software, algorithms and applications for parallel computing. The objective of Euro-Par is to provide a forum within which to promote the development of parallel computing both as an industrial technique and an academic discipline, extending the frontier of both the state of the art and the state of the practice. This is particularly - portant at a time when parallel computing is undergoing strong and sustained development and experiencing real industrial take-up. The main audience for, and participants at, Euro-Par are seen as researchers in academic departments, government laboratories and industrial organizations. Euro-Par's objective is to be the primary choice of such professionals for the presentation of new - sults in their speci?c areas. Euro-Par also targets applications demonstrating the e?ectiveness of parallelism. This year's Euro-Par conference was the tenth in the conference series. The previous Euro-Par conferences took place in Sto- holm, Lyon, Passau, Southampton, Toulouse, Munich, Manchester, Paderborn and Klagenfurt. Next year the conference will take place in Lisbon. Euro-Par has a permanent Web site hosting the aims, the organization structure details as well as all the conference history:http://www. europar. org.

Detection of Intrusions and Malware, and Vulnerability Assessment Clémentine Maurice,Leyla Bilge,Gianluca Stringhini,Nuno Neves,2020-07-07 This book constitutes the proceedings of the 17th International Conference on Detection of Intrusions and Malware, and Vulnerability Assessment, DIMVA 2020, held in Lisbon, Portugal, in June 2020.* The 13 full papers presented in this volume were carefully reviewed and selected from 45 submissions. The contributions were organized in topical sections named: vulnerability discovery and analysis; attacks; web security; and detection and containment. *The conference was held virtually due to the COVID-19 pandemic.

Performance-oriented Application Development for Distributed Architectures M. Gerndt,2002 Annotation This publication is devoted to programming models, languages, and tools for performance-oriented program development in commercial and scientific environments. The included papers have been written based on presentations given at the workshop PADDA 2001. The goal of the workshop was to identify common interests and techniques for performance-oriented program development in commercial and scientific environments. Distributed architectures currently dominate the field of highly parallel computing. Distributed architectures, based on Internet and mobile computing technologies, are important target architectures in the domain of commercial computing too. The papers in this publication come from the two areas: scientific computing and commercial computing.

NBS Special Publication ,1978

High-Performance Computing and Networking Marian Bubak,Roy Williams,Hamideh Afsarmanesh,Bob Hertzberger,2003-06-29 This book constitutes the refereed proceedings of the 8th International Conference on High-Performance Computing and Networking, HPCN Europe 2000, held in Amsterdam, The Netherlands, in May 2000. The 52 revised full papers presented together with 34 revised posters were carefully reviewed for inclusion in the book. The papers are organized in sections on problem solving environments, metacomputing, load balancing, numerical parallel algorithms, virtual enterprises and virtual laboratories, cooperation coordination, Web-based tools for tele-working, monitoring and performance, low-level algorithms, Java in HPCN, cluster computing, data analysis, and applications in a variety of fields.

Computer Performance Evaluation Computer Performance Evaluation Users Group,1975

A Survey of Remote Monitoring Gary J. Nutt,1978

Computational Science - ICCS 2001 Vassil N. Alexandrov,Jack J. Dongarra,Benjoe A. Juliano,Rene S. Renner,C.J.Kenneth Tan,2003-05-15 LNCS volumes 2073 and 2074 contain the proceedings of the International Conference on Computational Science, ICCS 2001, held in San Francisco, California, May 27-31, 2001. The two volumes consist of more than 230 contributed and invited papers that reflect the aims of the conference to bring together researchers and scientists from mathematics and computer science as basic computing disciplines, researchers from various application areas who are pioneering advanced application of computational methods to sciences such as physics, chemistry, life sciences, and engineering, arts and humanitarian fields, along with software developers and vendors, to discuss problems and solutions in the area, to identify new issues, and to shape future directions for research, as well as to help industrial users apply various advanced computational techniques.

PHM - a programmable hardware monitor Craig S. Anderson,Katherine J. Armstrong,Gaetano Borriello,1989

Computational Science — ICCS 2002 Peter M.A. Sloot,C.J. Kenneth Tan,Jack J. Dongarra,Alfons G. Hoekstra,2003-08-01 Computational Science is the scienti?c discipline that aims at the development and understanding of new computational methods and techniques to model and simulate complex systems. The area of application includes natural systems – such as biology, envir- mental and geo-sciences, physics, and chemistry – and synthetic systems such as electronics and ?nancial and economic systems. The discipline is a bridge b- ween 'classical' computer science – logic, complexity, architecture, algorithms – mathematics, and the use of computers in the aforementioned areas. The relevance for society stems from the numerous challenges that exist in the various science and engineering disciplines, which can be tackled by advances made in this ?eld. For instance new models and methods to study environmental issues like the quality of air, water, and soil, and weather and climate predictions through simulations, as well as the simulation-supported development of cars, airplanes, and medical and transport systems etc. Paraphrasing R. Kenway (R.D. Kenway, Contemporary Physics. 1994): 'There is an important message to scientists, politicians, and industrialists: in the future science, the best industrial design and manufacture, the greatest medical progress, and the most accurate environmental monitoring and forecasting will be done by countries that most rapidly exploit the full potential ofcomputational science'. Nowadays we have access to high-end computer architectures and a large range of computing environments, mainly as a consequence of the enormous s- mulus from the various international programs on advanced computing, e.g.

Performance Evaluation of Computer and Communication Systems Lorenzo Donatiello,Randolph Nelson,1993-09-15 This volume contains the complete set of tutorial papers presented at the 16th IFIP (International

Federation for Information Processing) Working Group 7.3 International Symposium on Computer Performance Modelling, Measurement and Evaluation, and a number of tutorial papers presented at the 1993 ACM (Association for Computing Machinery) Special Interest Group METRICS Conference on Measurement and Modeling of Computer Systems. The principal goal of the volume is to present an overview of recent results in the field of modeling and performance evaluation of computer and communication systems. The wide diversity of applications and methodologies included in the tutorials attests to the breadth and richness of current research in the area of performance modeling. The tutorials may serve to introduce a reader to an unfamiliar research area, to unify material already known, or simply to illustrate the diversity of research in the field. The extensive bibliographies guide readers to additional sources for further reading.

Minicomputers Peter C. Sanderson,2014-05-12 Minicomputers illustrate some of the potential pitfalls and benefits that can be gained if a minicomputer system is chosen wisely. This book is divided into five chapters—introduction, software, programming, selecting the minicomputer system, and applications. This text specifically discusses the advantages of the minicomputer, peripheral devices for minicomputer systems, arithmetical software, and operating systems. The nature of programming language, typical minicomputer assembly language functions, and feasibility study are also elaborated. This monograph likewise covers the preparation of tender, original equipment manufacturers (OEM) applications, and unusual applications of minicomputers. This publication is beneficial to all potential users such as scientists, research workers, process control engineers, and business managers, including educationalists concerned with computer education.

Database and Expert Systems Applications - DEXA 2021 Workshops Gabriele Kotsis,A Min Tjoa,Ismail Khalil,Bernhard Moser,Atif Mashkoor,Johannes Sametinger,Anna Fensel,Jorge Martinez-Gil,Lukas Fischer,Gerald Czech,Florian Sobieczky,Sohail Khan,2021-09-20 This volume constitutes the refereed proceedings of the workshops held at the 32nd International Conference on Database and Expert Systems Applications, DEXA 2021, held in a virtual format in September 2021: The 12th International Workshop on Biological Knowledge Discovery from Data (BIOKDD 2021), the 5th International Workshop on Cyber-Security and Functional Safety in Cyber-Physical Systems (IWCFS 2021), the 3rd International Workshop on Machine Learning and Knowledge Graphs (MLKgraphs 2021), the 1st International Workshop on Artificial Intelligence for Clean, Affordable and Reliable Energy Supply (AI-CARES 2021), the 1st International Workshop on Time Ordered Data (ProTime2021), and the 1st International Workshop on AI System Engineering: Math, Modelling and Software (AISys2021). Due to the COVID-19 pandemic the conference and workshops were held virtually. The 23 papers were thoroughly reviewed and selected from 50 submissions, and discuss a range of topics including: knowledge discovery, biological data, cyber security, cyber-physical system, machine learning, knowledge graphs, information retriever, data base, and artificial intelligence.

SCI: Scalable Coherent Interface Hermann Hellwagner,Alexander Reinefeld,2006-12-29 Scalable Coherent Interface (SCI) is an innovative interconnect standard (ANSI/IEEE Std 1596-1992) addressing the high-performance computing and networking domain. This book describes in depth one specific application of SCI: its use as a high-speed interconnection network (often called a system area network, SAN) for compute clusters built from commodity workstation nodes. The editors and authors, coming from both academia and industry, have been instrumental in the SCI standardization process, the development and deployment of SCI adapter cards, switches, fully integrated clusters, and software systems, and are closely involved in various research projects on this important interconnect. This thoroughly cross-reviewed state-of-the-art survey covers the complete hardware/software spectrum of SCI clusters, from the major concepts of SCI, through SCI hardware, networking, and low-level software issues, various programming models and environments, up to tools and application experiences.

Computer Architecture G. Boulaye,T.R. Lewin,2012-12-06 This book presents as formal papers nearly all of the lectures given at the NATO advanced summer institute on Computer Architecture held at St. Raphael, France from September 12th - 24th 1976. It was not possible to include an important paper by G. Amdahl on the 470V6 System, nor papers by Mde. A. Recoque on distributed processing, Messrs. A. Maison and G. Debruyne on LSI technology, and K. Bowden. Computer architecture is a very diverse and expanding subject, consequently it was decided to limit the scope of the School to five main subject areas. These were: specific computer architectures, language orientated machines, associative processing, computer networks and specification and design methods. In addition an overall emphasis was placed on distributed and parallel processing and the need for an integrated hardware-software approach to design. Though some introductory material is included, this book is primarily intended for workers in the field of computer science and engineering who wish to update themselves on current topics in computer architecture. The main work of the School is well reflected in the collected papers, but it is impossible to convey the benefits obtained from the discussion groups and the continuous dialogue that was maintained throughout the School. The Editors would like to acknowledge with thanks the support of the NATO Scientific Affairs Division, who financed the School, and the European Research Office of the U.S. Army and the National Science Foundation for providing travel grants.

On-line Monitoring Systems and Computer Tool Interoperability Thomas Ludwig,2003 Ludwig (Institut fur Informatik, Ruprecht-Karls-Universitat Heidelberg, Germany) and Miller (computer science, U. of Wisconsin, US) present five papers examining the construction and methodology of tools for debugging and performance analysis in parallel programs. After a review of the past decade's work in debuggers and performance analyzers, papers look a tool infrastructure, an operational tool environment for multi-thread and multi-process debugging and execution visualization, multi-execution performance tuning, and the specification of performance properties of parallel applications using compound events. Annotation : 2004 Book News, Inc., Portland, OR (booknews.com).

Dependable Computing for Critical Applications 3 Carl E. Landwehr,Brian Randell,Luca Simoncini,2013-06-29 This volume contains the papers presented. at the Third IFIP International Working Conference on Dependable Computing for Critical Applications, sponsored by IFIP Working Group 10.4 and held in Mondello (Sicily), Italy on September 14-16, 1992. System developers increasingly apply computers where they can affect the safety and security of people and equipment. The Third IFIP International Working Conference on Dependable Computing for Critical Applications, like its predecessors, addressed various aspects of computer system dependability, a broad term defined as the degree of trust that may justifiably be placed in a system's reliability, availability, safety, security, and performance. Because the scope of the conference was so broad, we hope the presentations and discussions will contribute to the integration of these concepts so that future computer-based systems will indeed be more dependable. The Program Committee selected 18 papers for presentation from a total of 7 4 submissions at a May meeting in Newcastle upon Tyne, UK. The resulting program represented a broad spectrum of interests, with papers from universities, corporations, and government agencies in eight countries. Much diligent work by the Program Committee and the quality of reviews from more than a hundred external referees from around the world, for which we are most grateful, significantly eased the production of this technical program.

Software Design for Resilient Computer Systems Igor Schagaev,Eugene Zouev,Kaegi Thomas,2019-07-09 This book addresses the question of how system software should be designed to account for faults, and which fault tolerance features it should provide for highest reliability. With this second edition of Software Design for Resilient Computer Systems the book is thoroughly updated to contain the newest advice regarding software resilience. With additional chapters on computer system performance and system resilience, as well as online resources, the new edition is ideal for researchers and industry professionals. The authors first show how the system software interacts with the hardware to tolerate faults. They analyze and further develop the theory of fault tolerance to understand the different ways to increase the reliability of a system, with special attention on the role of system software in this process. They further develop the general algorithm of fault tolerance (GAFT) with its three main processes: hardware checking, preparation for recovery, and the recovery procedure. For each of the three processes, they analyze the requirements and properties theoretically and give possible implementation scenarios and system software support required. Based on the theoretical results, the authors derive an Oberon-based programming language with direct support of the three processes of GAFT. In the last part of this book, they introduce a simulator, using it as a proof of concept implementation of a novel fault tolerant processor architecture (ERRIC) and its newly developed runtime system feature-wise and performance-wise. Due to the wide reaching nature of the content, this book applies to a

host of industries and research areas, including military, aviation, intensive health care, industrial control, and space exploration.

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Table of Contents Avmonitor

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