

Rvclock

Eby G. Friedman

Model-Based Methodologies for Pervasive and Embedded Software

Ricardo J. Machado, Rita Suzana Maciel, Julia Rubin, Goetz Botterweck, 2013-11-18 This book constitutes the thoroughly refereed post-conference proceedings of the 8th International Workshop on Model-Based Methodologies for Pervasive and Embedded Software, MOMPES 2012, held in Essen, Germany, in September 2012. The 7 revised full papers presented together with 1 short papers were carefully reviewed and selected from 16 submissions. The papers cover a large spectrum of topics including model-driven engineering, model analysis, runtime verification, modeling of reactive systems, variability modeling, and domain-specific languages.

Comprehensive Semiconductor Science and Technology

, 2011-01-28 Semiconductors are at the heart of modern living. Almost everything we do, be it work, travel, communication, or entertainment, all depend on some feature of semiconductor technology. Comprehensive Semiconductor Science and Technology, Six Volume Set captures the breadth of this important field, and presents it in a single source to the large audience who study, make, and exploit semiconductors. Previous attempts at this achievement have been abbreviated, and have omitted important topics. Written and Edited by a truly international team of experts, this work delivers an objective yet cohesive global review of the semiconductor world. The work is divided into three sections. The first section is concerned with the fundamental physics of semiconductors, showing how the electronic features and the lattice dynamics change drastically when systems vary from bulk to a low-dimensional structure and further to a nanometer size. Throughout this section there is an emphasis on the

full understanding of the underlying physics. The second section deals largely with the transformation of the conceptual framework of solid state physics into devices and systems which require the growth of extremely high purity, nearly defect-free bulk and epitaxial materials. The last section is devoted to exploitation of the knowledge described in the previous sections to highlight the spectrum of devices we see all around us. Provides a comprehensive global picture of the semiconductor world Each of the work's three sections presents a complete description of one aspect of the whole Written and Edited by a truly international team of experts

Hands-On Software Architecture with Golang

Jyotiswarup Raiturkar, 2018-12-07 Understand the principles of software architecture with coverage on SOA, distributed and messaging systems, and database modeling Key Features Gain knowledge of architectural approaches on SOA and microservices for architectural decisions Explore different architectural patterns for building distributed applications Migrate applications written in Java or Python to the Go language Book Description Building software requires careful planning and architectural considerations; Golang was developed with a fresh perspective on building next-generation applications on the cloud with distributed and concurrent computing concerns. Hands-On Software Architecture with Golang starts with a brief introduction to architectural elements, Go, and a case study to demonstrate architectural principles. You'll then move on to look at code-level aspects such as modularity, class design, and constructs specific to Golang and implementation of design patterns. As you make your way through the chapters, you'll explore the core objectives of architecture such as effectively managing complexity, scalability, and reliability of software systems. You'll also work through

creating distributed systems and their communication before moving on to modeling and scaling of data. In the concluding chapters, you'll learn to deploy architectures and plan the migration of applications from other languages. By the end of this book, you will have gained insight into various design and architectural patterns, which will enable you to create robust, scalable architecture using Golang. What you will learn Understand architectural paradigms and deep dive into MicroservicesDesign parallelism/concurrency patterns and learn object-oriented design patterns in GoExplore API-driven systems architecture with introduction to REST and GraphQL standardsBuild event-driven architectures and make your architectures anti-fragileEngineer scalability and learn how to migrate to Go from other languagesGet to grips with deployment considerations with CICD pipeline, cloud deployments, and so onBuild an end-to-end e-commerce (travel) application backend in GoWho this book is for Hands-On Software Architecture with Golang is for software developers, architects, and CTOs looking to use Go in their software architecture to build enterprise-grade applications. Programming knowledge of Golang is assumed.

CMOS and Beyond Tsu-Jae King Liu,Kelin

Kuhn,2015-02-05 Get up to speed with the future of logic switch design with this indispensable overview of the most promising successors to modern CMOS transistors. Learn how to overcome existing design challenges using novel device concepts, presented using an in-depth, accessible, tutorial-style approach. Drawing on the expertise of leading researchers from both industry and academia, and including insightful contributions from the developers of many of these alternative logic devices, new concepts are introduced and discussed from a range of different viewpoints, covering all the necessary theoretical

background and developmental context. Covering cutting-edge developments with the potential to overcome existing limitations on transistor performance, such as tunneling field-effect transistors (TFETs), alternative charge-based devices, spin-based devices, and more exotic approaches, this is essential reading for academic researchers, professional engineers, and graduate students working with semiconductor devices and technology.

Ozga v. Clock, 266 MICH 58 (1934) ,1934 70

Seven Databases in Seven Weeks Eric Redmond,Jim Wilson,2012-05-11 Data is getting bigger and more complex by the day, and so are the choices in handling that data. As a modern application developer you need to understand the emerging field of data management, both RDBMS and NoSQL. *Seven Databases in Seven Weeks* takes you on a tour of some of the hottest open source databases today. In the tradition of Bruce A. Tate's *Seven Languages in Seven Weeks*, this book goes beyond your basic tutorial to explore the essential concepts at the core each technology. Redis, Neo4J, CouchDB, MongoDB, HBase, Riak and Postgres. With each database, you'll tackle a real-world data problem that highlights the concepts and features that make it shine. You'll explore the five data models employed by these databases-relational, key/value, columnar, document and graph-and which kinds of problems are best suited to each. You'll learn how MongoDB and CouchDB are strikingly different, and discover the Dynamo heritage at the heart of Riak. Make your applications faster with Redis and more connected with Neo4J. Use MapReduce to solve Big Data problems. Build clusters of servers using scalable services like Amazon's Elastic Compute Cloud (EC2). Discover the CAP theorem and its implications for your distributed data. Understand the tradeoffs between consistency and availability, and when you can use them to your advantage.

Use multiple databases in concert to create a platform that's more than the sum of its parts, or find one that meets all your needs at once. *Seven Databases in Seven Weeks* will take you on a deep dive into each of the databases, their strengths and weaknesses, and how to choose the ones that fit your needs. What You Need: To get the most of of this book you'll have to follow along, and that means you'll need a *nix shell (Mac OSX or Linux preferred, Windows users will need Cygwin), and Java 6 (or greater) and Ruby 1.8.7 (or greater). Each chapter will list the downloads required for that database.

Processor Design Jari Nurmi, 2007-07-26 Here is an extremely useful book that provides insight into a number of different flavors of processor architectures and their design, software tool generation, implementation, and verification. After a brief introduction to processor architectures and how processor designers have sometimes failed to deliver what was expected, the authors introduce a generic flow for embedded on-chip processor design and start to explore the vast design space of on-chip processing. The authors cover a number of different types of processor core.

The ... International Conference on Distributed Computing Systems, 1995

Ciarcia's Circuit Cellar Steve Ciarcia, 1979 Offers Projects Such as a Computer Controlled Weather Station & a Text-to-Speech Synthesizer. Includes Schematics & Building Tips

High-Performance System Design Vojin G. Oklobdzija, 1999-07-12 This comprehensive collection of papers offers you practical information that can be used to develop high-performance digital system design. Specially written introductions by editor Vojin G. Oklobdzija precede each chapter to aid your understanding of the most

relevant topics in this advanced area of circuit design. Featured topics include: * Differential pass-transistor logic * High-speed circuits and design of high-performance systems * Advanced deep submicron circuits used in high-speed computers and digital circuits * Clocking and latch design essential to high-performance systems * Relationships between VLSI algorithms and implementation techniques HIGH PERFORMANCE SYSTEM DESIGN: Circuits and Logic is indispensable reading for circuit designers, practicing engineers, and students who want to master the basic principles underlying high-performance system design. This handy, single volume provides a useful reference to a collection of accumulated experience necessary for good, successful designs. Professors: To request an examination copy simply e-mail collegeadoption@ieee.org. Sponsored by: IEEE Solid-State Circuits Council/Society.

Enterprise Software Architecture and Design

Dominic Duggan, 2012-01-12 This book fills a gap between high-level overview texts that are often too general and low-level detail oriented technical handbooks that lose sight the big picture. This book discusses SOA from the low-level perspective of middleware, various XML-based technologies, and basic service design. It also examines broader implications of SOA, particularly where it intersects with business process management and process modeling. Concrete overviews will be provided of the methodologies in those fields, so that students will have a hands-on grasp of how they may be used in the context of SOA.

Multimedia Computing and Networking , 1996

Clock Distribution Networks in VLSI Circuits and Systems Eby G. Friedman, 1995 Improve the performance and reliability of synchronous digital integrated circuits with this anthology of key literature on the design and analysis

of clock distribution networks for VLSI based computer and signal processing systems. Beginning with an extensive tutorial overview and bibliography, this all in one source offers substantive coverage of the most relevant issues related to the design of clock distribution networks for application to high performance synchronous design. Related topics include clock skew; automated layout of clock nets; distributed buffer and interconnect delays; clock distribution design of structured custom VLSI circuits; wafer scale integration; systolic arrays; globally asynchronous, locally synchronous systems; microwave issues; low power clocking techniques; process insensitive circuits; deterministic and probabilistic delay models; system timing specifications; clock distribution networks of well known circuits and future research in clock distribution networks. The material presented in Clock Distribution Networks in VLSI Circuits and Systems will be valuable to anyone with an interest in synchronous integrated circuits, computer design, or signal processing implementation issues.

CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters Rudy J. van de Plassche, 2013-04-17 CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes in depth converter specifications like Effective Number of Bits (ENOB), Spurious Free Dynamic Range (SFDR), Integral Non-Linearity (INL), Differential Non-Linearity (DNL) and sampling clock jitter requirements. Relations between these specifications and practical issues like matching of components and offset parameters of differential pairs are derived. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes the requirements of input and signal reconstruction filtering in case a converter is applied into a signal processing system. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes design details of high-speed A/D and

D/A converters, high-resolution A/D and D/A converters, sample-and-hold amplifiers, voltage and current references, noise-shaping converters and sigma-delta converters, technology parameters and matching performance, comparators and limitations of comparators and finally testing of converters.

Switched-Current Signal Processing and A/D Conversion Circuits Bengt E. Jonsson, 2013-04-18

Switched-Current Signal Processing and A/D Conversion Circuits: Design and Implementation describes the design and implementation of switched-current (SI) circuits with emphasis on signal processing and data-conversion applications. The work includes theoretical analysis, high-level and circuit-level simulation results as well as measurement results from a few of the author's circuit implementations. An extensive overview of the SI field of research is also given. The book contains an extensive overview of the switched-current field of research, and can therefore be used as a quick-reference to the field. The description of each design example has been organized to describe the entire design flow from system level design and simulation, to circuit simulation, layout and measurement as accurately as possible. Thus it is possible to follow each step in the design process. Switched-Current Signal Processing and A/D Conversion Circuits: Design and Implementation is an invaluable reference for researchers and circuit designers working with one-chip mixed-signal system solutions, and low-voltage analog CMOS design. It will also be appreciated by anyone requiring a quick overview of what has been done in the SI field.

Modelling of Multiphysics Systems Peeter Joot, This document is based on my lecture notes for the Fall 2014, University of Toronto Modeling of Multiphysics course (ECE1254H), taught by Professor P. Triverio. Official course

description: “The course deals with the modeling and simulation of physical systems. It introduces the fundamental techniques to generate and solve the equations of a static or dynamic system. Special attention is devoted to complexity issues and to model order reduction methods, presented as a systematic way to simulate highly-complex systems with acceptable computational cost. Examples from multiple disciplines are considered, including electrical/electromagnetic engineering, structural mechanics, fluid-dynamics. Students are encouraged to work on a project related to their own research interests.”

Topics: • Automatic generation of system equations (Tableau method, modified nodal analysis). • Solution of linear and nonlinear systems (LU decomposition, conjugate gradient method, sparse systems, Newton-Raphson method). • Solution of dynamical systems (Euler and trapezoidal rule, accuracy, stability). • Model order reduction of linear systems (proper orthogonal decomposition, Krylov methods, truncated balanced realization, stability/dissipativity enforcement). • Modeling from experimental data (system identification, the Vector Fitting algorithm, enforcement of stability and dissipativity).

Control Techniques for Power Converters with Integrated Circuit

Wen-Wei Chen, Jiann-Fuh Chen, 2017-12-28 This book offers an overview of power electronic applications in the study of power integrated circuit (IC) design, collecting novel research ideas and insights into fast transient response to prevent the output voltage from dropping significantly at the undershoot. It also discusses techniques and training to save energy and increase load efficiency, as well as fast transient response and high efficiency, which are the most important factors for consumer products that implement power IC. Lastly, the book focuses on power electronics for system loop analysis

and optimal compensation design to help users and engineers implement their applications. The book is a valuable resource for university researchers, power IC R&D engineers, application engineers and graduate students in power electronics who wish to learn about the power IC design principles, methods, system behavior, and applications in consumer products.

Fundamental Approaches to Software

Engineering Gabriele Taentzer,2010-03-16 This book constitutes the refereed proceedings of the 13th International Conference on Fundamental Approaches to Software Engineering, FASE 2010, held in Paphos, Cyprus, in March 2010, as part of ETAPS 2010, the European Joint Conferences on Theory and Practice of Software. The 25 papers presented were carefully reviewed and selected from 103 submissions. The volume also contains one invited talk. The topics covered are model transformation, software evolution, graph transformation, modeling concepts, verification, program analysis, testing and debugging, and performance modeling and analysis.

Cryptographic Hardware and Embedded Systems

- **CHES 2005** Josyula R. Rao,2005-08-18 This book constitutes the refereed proceedings of the 7th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2005, held in Edinburgh, UK in August/September 2005. The 32 revised full papers presented were carefully reviewed and selected from 108 submissions. The papers are organized in topical sections on side channels, arithmetic for cryptanalysis, low resources, special purpose hardware, hardware attacks and countermeasures, arithmetic for cryptography, trusted computing, and efficient hardware.

Ternary Digital System A P Dhande,V T Ingole,V R Ghiye,2014-10-01 Ternary digital system is commonly

known as three valued digital system. Three valued logic is an elementary set of Multiple Valued Logic, which is introduced in the book at the beginning. The book provides a detail overview of every concept required for the design and applications of ternary circuits. It covers the basic concepts for ternary logic fundamentals, ternary logic gates, its logic gate truth tables, Boolean rules for ternary logic up to ternary logic families, function synthesis and minimization techniques and an applications like one trit T-ALU, Two trit T-ALU Slice, Ternary R-S and D memory elements and an analog to ternary converter for DSP application as a fundamental block are developed and simulated using EDA tool. Finally computer simulation using EDA (Electronic Design Automation) tools like Tanner, spice and VHDL is also illustrated. In the first half of 19 th century G.Boole have proposed the Algebra for two valued (Binary logic) system after that Shanon has expressed the behavior of electrical switches in terms of Boolean algebra and he paved the ramp to an industrial development that is recognized as initiating one of the most revolutionary economic changes ever. MVL is also known as Multi-Valued, Multiple-Valued or Many-Valued logic. Multi-Value logic is regarded as a switch with more than two states. Such as a 3- value switch with states '0', '1' and '2'. Or a 4-value switch with states '0', '1', '2' and '3'. In case of 3-Valued logic the term ternary logic is used & term quaternary logic for 4-Valued logic. Alexander (1964) showed that the most efficient radix for implementation of switching systems is the natural base ($e \approx 2.71828$), it seems likely that the best integral radix is 3 rather than 2. It should be noted that this book emphasis on Ternary logic with concepts and applications. The fundamental work on Multiple Valued Logic (MVL) System was done by E.L.Post in the beginning of 19 th centuries and based on that work P.C.Rosen Bloom

modeled the Algebra for MVL is called Post Algebra.

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