Line Module Prototype

J. Rix,S. Haas,J. Teixeira

Automated Analysis of Virtual Prototypes at the Electronic System Level Mehran Goli,Rolf Drechsler,2020-05-04 This book describes a set of SystemC-based virtual prototype analysis methodologies, including design understanding, verification, security validation, and design space exploration. Readers will gain an overview of the latest research results in the field of Electronic Design Automation (EDA) at the Electronic System Level (ESL). The methodologies discussed enable readers to tackle easily key tasks and applications in the design process.

Rapid Prototyping of Digital Systems James O. Hamblen,Michael D. Furman,2007-05-08 Rapid Prototyping of Digital Systems, Second Edition provides an exciting and challenging laboratory component for an undergraduate digital logic design class. The more advanced topics and exercises are also appropriate for consideration at schools that have an upper level course in digital logic or programmable logic. Design engineers working in industry will also want to consider this book for a rapid introduction to FPLD technology and logic synthesis using commercial CAD tools, especially if they have not had previous experience with the new and rapidly evolving technology. Two tutorials on the Altera CAD tool environment, an overview of programmable logic, and a design library with several easy-to-use input and output functions were developed for this book to help the reader get started quickly. Early design examples use schematic capture and library components. VHDL is used for more complex designs after a short introduction to VHDL-based synthesis. A coupon is included with the text for purchase of the new UP 1X board. The additional logic and memory in the UP 1X's FLEX 10K70 is useful on larger design projects such as

computers and video games. The second edition includes an update chapter on programmable logic, new robot sensors and projects, optional Verilog examples, and a meta assembler which can be used to develop assemble language programs for the computer designs in Chapters 8 and 13.

C and the 8051 Thomas W. Schultz,Thomas Schultz,2004 This totally reworked book combines two previous books with material on networking. It is a complete guide to programming and interfacing the 8051 microcontroller-family devices for embedded applications.

Enhanced Virtual Prototyping Vladimir Herdt,Daniel Große,Rolf Drechsler,2020-10-14 This book presents a comprehensive set of techniques that enhance all key aspects of a modern Virtual Prototype (VP)-based design flow. The authors emphasize automated formal verification methods, as well as advanced coverage-guided analysis and testing techniques, tailored for SystemC-based VPs and also the associated Software (SW). Coverage also includes VP modeling techniques that handle functional as well as non-functional aspects and also describes correspondence analyses between the Hardware- and VP-level to utilize information available at different levels of abstraction. All approaches are discussed in detail and are evaluated extensively, using several experiments to demonstrate their effectiveness in enhancing the VP-based design flow. Furthermore, the book puts a particular focus on the modern RISC-V ISA, with several case-studies covering modeling as well as VP and SW verification aspects.

Microcontroller Prototypes with Arduino and a 3D Printer Dimosthenis E. Bolanakis,2021-03-30 Microcontroller Prototypes with Arduino and a 3D Printer Discover a

complete treatment of microcomputer programming and application development with Arduino and 3D printers Microcontroller Prototypes with Arduino and a 3D Printer: Learn, Program, Manufacture delivers a comprehensive guide to learning microcontrollers that's perfectly suited to educators, researchers, and manufacturers. The book provides readers with a seasoned expert's perspective on the process of microcomputer programming and application development. Carefully designed and written example code and explanatory figures accompany the text, helping the reader fully understand and retain the concepts described within. The book focuses on demonstrating how to craft creative and innovative solutions in embedded systems design by providing practical and illustrative methods and examples. An accompanying website includes functioning and tested source code and learning exercises and the book relies on freeware development tools for the creation of firmware and software code, 3D printed enclosures, and debugging. It allows the reader to work with modern sensors and collect sensor data to a host PC for offline analysis. Readers will also benefit from the inclusion of: A thorough introduction to the art of embedded computers, including their interdisciplinarity, TPACK analysis, and the impact of microcontroller technology on the maker industry An exploration of embedded programming with Arduino, including number representation and special-function codes and C common language reference A discussion of hardware interfaces with the outside world, including digital pin interface, analog pin interface, UART serial interface, I2C, and SPI A treatment of sensors and data acquisition, including environmental measurements with Arduino Uno, orientation and motion detection with Teensy, gesture recognition with TinyZero, and color sensing with Micro:bit A variety of supplementary resources—including

source codes and examples—hosted on an accompanying website to be maintained by the author: www.mikroct.com. Perfect for researchers and undergraduate students in electrical and electronic engineering or computer engineering, Microcontroller Prototypes with Arduino and a 3D Printer: Learn, Program, Manufacture will also earn a place in the libraries of hardware engineers, embedded system designers, system engineers, and electronic engineers.

Enhanced Virtual Prototyping for Heterogeneous Systems Muhammad Hassan,Daniel Große,Rolf Drechsler,2022-09-01 This book describes a comprehensive combination of methodologies that strongly enhance the modern Virtual Prototype (VP)-based verification flow for heterogeneous systems-on-chip (SOCs). In particular, the book combines verification and analysis aspects across various stages of the VP-based verification flow, providing a new perspective on verification by leveraging advanced techniques, like metamorphic testing, data flow testing, and information flow testing. In addition, the book puts a strong emphasis on advanced coverage-driven methodologies to verify the functional behavior of the SOC as well as ensure its security. Provides an extensive introduction to the modern VP-based verification flow for heterogeneous SOCs; Introduces a novel metamorphic testing technique for heterogeneous SOCs which does not require reference models; Includes automated advanced data flow coverage-driven methodologies tailored for SystemC/AMS-based VPs; Describes enhanced functional coverage-driven methodologies to verify various functional behaviors of RF amplifiers.

Emerging Solutions for Future Manufacturing Systems Luis M. Camarinha-Matos,2006-01-04 Industries and particularly the manufacturing sector have been facing

difficult challenges in a context of socio-economic turbulence characterized by complexity as well as the speed of change in causal interconnections in the socio-economic environment. In order to respond to these challenges companies are forced to seek new technological and organizational solutions. In this context two main characteristics emerge as key properties of a modern automation system – agility and distribution. Agility because systems need not only to be flexible in order to adjust to a number of a-priori defined scenarios, but rather must cope with unpredictability. Distribution in the sense that automation and business processes are becoming distributed and supported by collaborative networks. Emerging Solutions for Future Manufacturing Systems includes the papers selected for the BASYS'04 conference, which was held in Vienna, Austria in September 2004 and sponsored by the International Federation for Information Processing (IFIP).

   Power Converters for Medium Voltage Networks Md. Rabiul Islam,Youguang Guo,Jianguo Zhu,2014-09-15 This book examines a number of topics, mainly in connection with advances in semiconductor devices and magnetic materials and developments in medium and large-scale renewable power plant technologies, grid integration techniques and new converter topologies, including advanced digital control systems for medium-voltage networks. The book's individual chapters provide an extensive compilation of fundamental theories and in-depth information on current research and development trends, while also exploring new approaches to overcoming some critical limitations of conventional grid integration technologies. Its main objective is to present the design and implementation processes for medium-voltage converters, allowing the direct grid

integration of renewable power plants without the need for step-up transformers.

Advances in Artificial Intelligence **-- IBERAMIA 2012** Juan Pavón,Néstor D. Duque-Méndez,Rubén Fuentes Fernández,2012-11-15 This book constitutes the refereed proceedings of the 13th Ibero-American Conference on Artificial Intelligence, IBERAMIA 2012, held in Cartagena de Indias, Colombia, in November 2012. The 75 papers presented were carefully reviewed and selected from 170 submissions. The papers are organized in topical sections on knowledge representation and reasoning, information and knowledge processing, knowledge discovery and data mining, machine learning, bio-inspired computing, fuzzy systems, modelling and simulation, ambient intelligence, multi-agent systems, human-computer interaction, natural language processing, computer vision and robotics, planning and scheduling, AI in education, and knowledge engineering and applications.

Virtual Prototyping J. Rix,S. Haas,J. Teixeira,2016-01-09 A virtual prototype is a major interim step towards the creation of a virtual environment. This book explores the simulation, interaction, concepts and tools of virtual prototypes and environments. It provides a mixture of state-of-the-art, advanced research and industrial papers.

Rapid Prototyping of Application Specific Signal Processors Mark A. Richards,Anthony J. Gadient,Geoffrey A. Frank,1997-02-28 Rapid Prototyping of Application Specific Signal Processors presents leading-edge research that focuses on design methodology, infrastructure support and scalable architectures developed by the 150 million dollar DARPA United States Department of Defense RASSP Program. The contributions to this edited work include an introductory overview chapter that explains the origin, concepts and

status of this effort. The RASSP Program is a multi-year DARPA/Tri-Service initiative intended to dramatically improve the process by which complex digital systems, particularly embedded signal processors, are designed, manufactured, upgraded and supported. This program was originally driven by military applications for signal processing. The requirements of military applications for real-time signal processing are typically more demanding than those of commercial applications, but the time gap between technology employed in advanced military prototypes and commercial products is narrowing rapidly. The research on methodologies, infrastructure and architectures presented in this book is applicable to commercial signal processing systems that are in design now, or will be developed before the end of the decade. Rapid Prototyping of Application Specific Signal Processors is a valuable reference for developers of embedded digital systems, particularly systems engineers for signal processing systems (such as digital TV, biomedical image processing systems and telecommunications) and for military contractors who are developing signal processing systems. This book will also be of interest to managers who are charged with responsibility for creating and maintaining environments and infrastructures for developing large embedded digital systems. The chief value for managers will be the defining of methods and processes that reduce development time and cost.

Beam Test Calorimeter Prototypes for the CMS Calorimeter Endcap Upgrade Thorben Quast,2022-01-24 In order to cope with the increased radiation level and the challenging pile-up conditions at High Luminosity-LHC, the CMS collaboration will replace its current calorimeter endcaps with the High Granularity Calorimeter (HGCAL) in the mid

2020s. This dissertation addresses two important topics related to the preparation of the HGCAL upgrade: experimental validation of its silicon- based design and fast simulation of its data. Beam tests at the DESY (Hamburg) and the CERN SPS beam test facilities in 2018 have been the basis for the design validation. The associated experimental infrastructure, the algorithms deployed in the reconstruction of the recorded data, as well as the respective analyses are reported in this thesis: First, core components of the silicon-based prototype modules are characterised and it is demonstrated that the assembled modules are functional. In particular, their efficiency to detect minimum ionising particles (MIPs) traversing the silicon sensors is found to be more than 98% for most of the modules. No indication of charge sharing between the silicon pads is observed. Subsequently, the energy response is calibrated in situ using the beam test data. Equalisation of the different responses among the readout channels is achieved with MIPs hereby deploying the HGCAL prototype as a MIP-tracking device. The relative variation of the inferred calibration constants amounts to 3% for channels on the same readout chip. The calibration of the time-of-arrival information is performed with an external time reference detector. With it, timing resolutions of single cells including the full prototype readout chain around 60ps in the asymptotic high energy limit are obtained. The calorimetric performance of the HGCAL prototype is validated with particle showers induced by incident positrons and charged pions. For electromagnetic showers, the constant term in the relative energy resolution is measured to be (0.52 ± 0.08) %, whereas the stochastic term amounts to (22.2 ± 0.3)% $\sqrt{\text{GeV}}$. This result is in good agreement with the calorimeter simulation with GEANT4. The prototype's positioning resolution of the shower axis, after subtracting the contribution

from the delay wire chambers in the beam line used as reference, is found to be below 0.4 mm at 300 GeV. At the same energy, the angular resolution in the reconstruction of the electromagnetic shower axis in this prototype is measured to be less than 5mrad. The analysis of the hadronic showers in this thesis makes use state-of-the- art machine-learning methods that exploit the calorimeter's granularity. It is indicated that the energy resolution may be improved using software compensation and also that the separation of electromagnetic and charged pion-induced showers in the calorimeter may benefit from such methods. The measurements of the hadronic showers are adequately reproduced by GEANT4 simulation. Altogether, the obtained results from the analysis of the beam test data in this thesis are in agreement with the full functionality of the silicon-based HGCAL design. The final part of this thesis provides a proof of principle that generative modelling based on deep neural networks in conjunction with the Wasserstein distance is a suitable approach for the fast simulation of HGCAL data: Instead of sequential simulation, a deep neural network-based generative model generates all calorimeter energy depositions simultaneously. This genera t or network is optimised throu gh an adversarial training process using a critic network guided by the Wasserstein distance. The developed framework in this thesis is applied to both GEANT4- simulated electromagnetic showers and to positron data from the beam tests. Ultimately, this fast simulation approach is up to four orders of magnitude faster than sequential simulation with GEANT4. It is able to produce realistic calorimeter energy depositions from electromagnetic showers, incorporating their fluctuations and correlations when converted into typical calorimeter observables.

Telecommunications ,1987

Mantech Journal ,1976

Design and Prototyping for Drupal Dani Nordin,2011-12-15 Learn strategies for sketching, wireframing, and designing effective layouts; Break down a Drupal layout to understand its basic components; Understand Drupal's theme layer, and what to look for in a base theme; Work with the 960 grid system to facilitate efficient wireframing and theming; Manage Drupal markup, including the code generated by the powerful Views module; Use LessCSS to organize CSS and help you theme your site more efficiently--P. [4] of cover.

FPGA Prototyping by SystemVerilog Examples Pong P. Chu,2018-05-30 A hands-on introduction to FPGA prototyping and SoC design This is the successor edition of the popular FPGA Prototyping by Verilog Examples text. It follows the same "learning-by-doing" approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping. The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC (system on a chip) framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RT (register transfer) level modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an introductory text, the examples are developed in a rigorous manner, and the derivations follow the strict design guidelines and coding practices used for large, complex digital systems. The book is completely updated and uses the SystemVerilog language, which "absorbs" the Verilog language. It presents the hardware design in the SoC context and

introduces the hardware-software co-design concept. Instead of treating examples as isolated entities, the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software "programmability" and develop complex and interesting embedded system projects. The new edition: Adds four general-purpose IP cores, which are multi-channel PWM (pulse width modulation) controller, I2C controller, SPI controller, and XADC (Xilinx analog-to-digital converter) controller. Introduces a music synthesizer constructed with a DDFS (direct digital frequency synthesis) module and an ADSR (attack-decay-sustain-release) envelope generator. Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit, a test-pattern generator, an OSD (on-screen display) controller, a sprite generator, and a frame buffer. Provides a detailed discussion on blocking and nonblocking statements and coding styles. Describes basic concepts of software-hardware co-design with Xilinx MicroBlaze MCS soft-core processor. Provides an overview of bus interconnect and interface circuit. Presents basic embedded system software development. Suggests additional modules and peripherals for interesting and challenging projects. FPGA Prototyping by SystemVerilog Examples makes a natural companion text for introductory and advanced digital design courses and embedded system courses. It also serves as an ideal self-teaching guide for practicing engineers who wish to learn more about this emerging area of interest.

Cases on Electronic Commerce Technologies and Applications Khosrow-Pour, D.B.A., Mehdi,2006-04-30 Electronic commerce technologies and applications have changed the way information technology is used in business and society, allowing organizations

worldwide to expand their market reach and their customer service. Cases on Electronic Commerce Technologies and Applications presents a wide range of real-life cases that describe the successful and unsuccessful adoption of e-commerce, e-business, e-government, mobile commerce, and Web services technologies. This collection provides significant insight on the successful implementation of these areas.

How We Learn, how We Remember Leon N. Cooper,1995 Leon Cooper's somewhat peripatetic career has resulted in work in quantum field theory, superconductivity, the quantum theory of measurement as well as the mechanisms that underly learning and memory. He has written numerous essays on a variety of subjects as well as a highly regarded introduction to the ideas and methods of physics for non-physicists. Among the many accolades, he has received (some deserved) one he likes specially is the comment of an anonymous reviewer who characterized him as ?a nonsense physicist?.This compilation of papers presents the evolution of his thinking on mechanisms of learning, memory storage and higher brain function. The first half proceeds from early models of memory and synaptic plasticity to a concrete theory that has been put into detailed correspondence with experiment and leads to the very current exploration of the molecular basis for learning and memory storage. The second half outlines his efforts to investigate the properties of neural network systems and to explore to what extent they can be applied to real world problems.In all this collection, hopefully, provides a coherent, no-nonsense, account of a line of research that leads to present investigations into the biological basis for learning and memory storage and the information processing and classification properties of neural systems.

US Black Engineer & IT ,1987

FSM-based Digital Design using Verilog HDL Peter Minns,Ian Elliott,2008-04-30 As digital circuit elements decrease in physical size, resulting in increasingly complex systems, a basic logic model that can be used in the control and design of a range of semiconductor devices is vital. Finite State Machines (FSM) have numerous advantages; they can be applied to many areas (including motor control, and signal and serial data identification to name a few) and they use less logic than their alternatives, leading to the development of faster digital hardware systems. This clear and logical book presents a range of novel techniques for the rapid and reliable design of digital systems using FSMs, detailing exactly how and where they can be implemented. With a practical approach, it covers synchronous and asynchronous FSMs in the design of both simple and complex systems, and Petri-Net design techniques for sequential/parallel control systems. Chapters on Hardware Description Language cover the widely-used and powerful Verilog HDL in sufficient detail to facilitate the description and verification of FSMs, and FSM based systems, at both the gate and behavioural levels. Throughout, the text incorporates many real-world examples that demonstrate designs such as data acquisition, a memory tester, and passive serial data monitoring and detection, among others. A useful accompanying CD offers working Verilog software tools for the capture and simulation of design solutions. With a linear programmed learning format, this book works as a concise guide for the practising digital designer. This book will also be of importance to senior students and postgraduates of electronic engineering, who require design skills for the embedded systems market.

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