### **Cube Links**

Keqin Li,Yi Pan,Si-Qing Zheng

**Inventive Communication and Computational Technologies** G. Ranganathan,Xavier Fernando,Fuqian Shi,2022-01-11 This book gathers selected papers presented at the Inventive Communication and Computational Technologies conference (ICICCT 2021), held on 25-26 June 2021 at Gnanamani College of Technology, Tamil Nadu, India. The book covers the topics such as Internet of things, social networks, mobile communications, big data analytics, bio-inspired computing, and cloud computing. The book is exclusively intended for academics and practitioners working to resolve practical issues in this area.

**Mobile Ad-hoc and Sensor Networks** Xiaohua Jia, Jie Wu, Yanxiang He, 2005-12-05 This book constitutes the refereed proceedings of the First International Conference on Mobile Ad-hoc and Sensor Networks, MSN 2005, held in Wuhan, China in December 2005. The volume also contains 12 papers of the MSN workshop on Modeling and the Security in the Next Generation Mobile Information Systems (MSNG 2005). The 112 revised full papers were carefully reviewed and selected from a total of 512 submissions. The papers address all current topical areas in mobile ad hoc and sensor networks such as network architecture and protocols, software platforms and development tools, self-organization and synchronization, routing and data dissemination, failure resilience and fault isolation, energy management, data, information, and signal processing, security and privacy, network planning, provisioning, and deployment, network modeling and performance evaluation, developments and applications, as well as integration with other systems.

**Distributed Autonomous Robotic Systems 4** L.E. Parker, George Bekey, J. Barhen, 2012-12-06 The Fifth International Symposium on Distributed Autonomous Robotic Systems (DARS 2000) dealt with new strategies to realize complex, modular, robust, and fault-tolerant robotic systems. Technologies, algorithms, and system architectures for distributed autonomous robotic systems were presented and discussed during the meeting. DARS 2000 was truly an international event, with participants represent ing eleven countries from Europe, Asia, and the Americas. All of the papers in this volume were presented at DARS 2000, and were selected on the basis of peer re views to ensure quality and relevance. These papers have the common goal of con tributing solutions to realize robust and intelligent multirobot systems. The topics of the symposium address a wide range of issues that are important in the development of decentralized robotic systems. These topics include architec tures, communication, biological inspirations, reconfigurable robots, localization, exploration and mapping, distributed sensing, multi robot motion coordination, tar get assignment and tracking, multirobot learning, and cooperative object transport. DARS clearly requires a broad area of interdisciplinary technologies related not only to robotics and computer engineering, but also to biology and psychology. The DARS symposium is the leading established conference on distributed au tonomous systems. The First, Second, and Third International Symposia on Distrib uted Autonomous Robotic Systems (DARS '92, DARS '94, and DARS '96) were held at the Institute of Physical and Chemical Research (RIKEN), Saitama, Japan.

**Optimal Cube-connected Cube Multiprocessors** Xian-He Sun, Institute for Computer Applications in Science and Engineering, 1993 Abstract: Many CFD (computational fluid dynamics) and other scientific applications can be partitioned into subproblems. However, in general the partitioned subproblems are very large. They demand high performance computing power themselves, and the solutions of the subproblems have to be combined at each time step. In this paper, the cube-connect cube (CCCube) architecture is studied. The CCCube architecture is an extended hypercube structure with each node represented as a cube. It requires fewer physical links between nodes than the hypercube, and provides the same communication support as the hypercube does on many applications. The reduced physical links can be used to enhance the bandwidth of the remanding links and, therefore, enhance the overall performance. The concept and the method to obtain optimal CCCubes, which are the CCCubes with a minimum number of links under a given total number of nodes, are proposed. The superiority of optimal CCCubes over standard hypercubes

has also been shown in terms of the link usage in the embedding of a binomial tree. A useful computation structure based on a semi-binomial tree for divide-and-conquer type of parallel algorithms has been identified. We have shown that this structure can be implemented in optimal CCCubes without performance degradation compared with regular hypercubes. The result presented in this paper should provide a useful approach to design of scientific parallel computers.

**OLAP Solutions** Erik Thomsen,2002-10-15 OLAP enables users to access information from multidimensional datawarehouses almost instantly, to view information in any way theylike, and to cleanly specify and carry out sophisticated calculations. Although many commercial OLAP tools and products arenow available, OLAP is still a difficult and complex technology tomaster. Substantially updated with expanded coverage of implementationmethods for data storage, access, and calculation; also, newchapters added to combine OLAP with data warehouse, mining, anddecision support tools Teaches the best practices for building OLAP models thatimprove business and organizational decision-making, completelyindependent of commercial tools, using revised case studies Companion Web site provides updates on OLAP standards andtools, code examples, and links to valuable resources

**Parallel Numerical Computation with Applications** Laurence Tianruo Yang,2012-12-06 Parallel Numerical Computations with Applications contains selected edited papers presented at the 1998 Frontiers of Parallel Numerical Computations and Applications Workshop, along with invited papers from leading researchers around the world. These papers cover a broad spectrum of topics on parallel numerical computation with applications; such as advanced parallel numerical and computational optimization methods, novel parallel computing techniques, numerical fluid mechanics, and other applications related to material sciences, signal and image processing, semiconductor technology, and electronic circuits and systems design. This state-of-the-art volume will be an up-to-date resource for researchers in the areas of parallel and distributed computing.

**Introduction to Parallel Processing** Behrooz Parhami,2006-04-11 THE CONTEXT OF PARALLEL PROCESSING The field of digital computer architecture has grown explosively in the past two decades. Through a steady stream of experimental research, tool-building efforts, and theoretical studies, the design of an instruction-set architecture, once considered an art, has been transformed into one of the most quantitative branches of computer technology. At the same time, better understanding of various forms of concurrency, from standard pipelining to massive parallelism, and invention of architectural structures to support a reasonably efficient and user-friendly programming model for such systems, has allowed hardware performance to continue its exponential growth. This trend is expected to continue in the near future. This explosive growth, linked with the expectation that performance will continue its exponential rise with each new generation of hardware and that (in stark contrast to software) computer hardware will function correctly as soon as it comes off the assembly line, has its down side. It has led to unprecedented hardware complexity and almost intolerable dev- opment costs. The challenge facing current and future computer designers is to institute simplicity where we now have complexity; to use fundamental theories being developed in this area to gain performance and ease-of-use benefits from simpler circuits; to understand the interplay between technological capabilities and limitations, on the one hand, and design decisions based on user and application requirements on the other.

Advances in Modelling, Animation and Rendering John Vince, Rae Earnshaw, 2002-06-27 Advances in computer technology and developments such as the Internet provide a constant momentum to design new techniques and algorithms to support computer graphics. Modelling, animation and rendering remain principal topics in the filed of computer graphics and continue to attract researchers around the world. This volume contains the papers presented at Computer Graphics International 2002, in July, at the University of Bradford, UK. These papers represent original research in computer graphics from around the world and cover areas such as: - Real-time computer animation - Image based rendering - Non photo-realistic

rendering - Virtual reality - Avatars - Geometric and solid modelling - Computational geometry - Physically based modelling - Graphics hardware architecture - Data visualisation - Data compression The focus is on the commercial application and industrial use of computer graphics and digital media systems.

PARALLEL AND DISTRIBUTED COMPUTING : ARCHITECTURES AND ALGORITHMS BASU, S. K. ,2016-01-02 This concise text is designed to present the recent advances in parallel and distributed architectures and algorithms within an integrated framework. Beginning with an introduction to the basic concepts, the book goes on discussing the basic methods of parallelism exploitation in computation through vector processing, super scalar and VLIW processing, array processing, associative processing, systolic algorithms, and dataflow computation. After introducing interconnection networks, it discusses parallel algorithms for sorting, Fourier transform, matrix algebra, and graph theory. The second part focuses on basics and selected theoretical issues of distributed processing. Architectures and algorithms have been dealt in an integrated way throughout the book. The last chapter focuses on the different paradigms and issues of high performance computing making the reading more interesting. This book is meant for the senior level undergraduate and postgraduate students of computer science and engineering, and information technology. The book is also useful for the postgraduate students of computer application.

<u>Computer Principles and Design in Verilog HDL</u> Yamin Li,Tsinghua University Press,2015-08-17 Uses Verilog HDL to illustrate computer architecture and microprocessor design, allowing readers to readily simulate and adjust the operation of each design, and thus build industrially relevant skills Introduces the computer principles, computer design, and how to use Verilog HDL (Hardware Description Language) to implement the design Provides the skills for designing processor/arithmetic/cpu chips, including the unique application of Verilog HDL material for CPU (central processing unit) implementation Despite the many books on Verilog and computer architecture and microprocessor design, few, if any, use Verilog as a key tool in helping a student to understand these design techniques A companion website includes color figures, Verilog HDL codes, extra test benches not found in the book, and PDFs of the figures and simulation waveforms for instructors

<u>Easy Beading Vol. 8</u>, 2012-05-29 Get all the great projects from the eighth year of BeadStyle magazine in Easy Beading Vol. 8! Beaders will love the up-to-the-minute designs collected from the colorful pages of BeadStyle's eighth season. Fast, fashionable projects using pretty pearls, sparkling crystals, gorgeous gemstones, mixed media, and more are just the beginning! Variations of many projects allow readers to develop their own design style. A thorough, illustrated Beader's Glossary and Basics section, paired with simple stringing and wirework techniques, guarantee great results right from the start!

Interconnections for Computer Communications and Packet Networks Roberto Rojas-Cessa,2016-11-03 This book introduces different interconnection networks applied to different systems. Interconnection networks are used to communicate processing units in a multi-processor system, routers in communication networks, and servers in data centers. Queuing techniques are applied to interconnection networks to support a higher utilization of resources. There are different queuing strategies, and these determine not only the performance of the interconnection network, but also the set of requirements to make them work effectively and their cost. Routing algorithms are used to find routes to destinations and directions in what information travels. Additional properties, such as avoiding deadlocks and congestion, are sought. Effective routing algorithms need to be paired up with these networks. The book will introduce the most relevant interconnection networks, queuing strategies, and routing algorithm. It discusses their properties and how these leverage the performance of the whole interconnection system. In addition, the book covers additional topics for memory management and congestion avoidance, used to extract higher performance from the interconnection network.

What Computing Is All About Jan L.A.van de Snepscheut, 2012-12-06 I have always been fascinated with engineering. From Roman bridges

and jumbo jets to steam engines and CD players, it is the privilege of the engineer to combine scientific insights and technical possibilities into useful and elegant products. Engineers get a great deal of satisfaction from the usefulness and beauty of their designs. Some of these designs have a major impact on our daily lives, others enable further scientific insights or shift limits of technology. The successful engineer is familiar with the scientific basis of the field and the technology of the components, and has an eye for the envisioned applications. For example, to build an airplane, one had better understand the physics of motion, the structural properties of alu minum, and the size of passengers. And the physics of motion requires a mastery of mathematics, in particular calculus. Computers are a marvel of modern engineering. They come in a wide variety and their range of applications seems endless. One of the charac teristics that makes computers different from other engineering products is their programmability. Dishwashers have some limited programming capa is not the key part of the device. Their essential part is some bility, but it enclosed space where the dishes are stored and flushed with hot water. Computers are embedded in many different environments, but in their case the programming capability is the essential part. All computers are programmed in more or less the same way.

**Routing, Flow, and Capacity Design in Communication and Computer Networks** Michal Pioro, Deep Medhi, 2004-07-21 In network design, the gap between theory and practice is woefully broad. This book narrows it, comprehensively and critically examining current network design models and methods. You will learn where mathematical modeling and algorithmic optimization have been under-utilized. At the opposite extreme, you will learn where they tend to fail to contribute to the twin goals of network efficiency and cost-savings. Most of all, you will learn precisely how to tailor theoretical models to make them as useful as possible in practice. Throughout, the authors focus on the traffic demands encountered in the real world of network design. Their generic approach, however, allows problem formulations and solutions to be applied across the board to virtually any type of backbone communication or computer network. For beginners, this book is an excellent introduction. For seasoned professionals, it provides immediate solutions and a strong foundation for further advances in the use of mathematical modeling for network design. Written by leading researchers with a combined 40 years of industrial and academic network design experience. Considers the development of design models for different technologies, including TCP/IP, IDN, MPLS, ATM, SONET/SDH, and WDM. Discusses recent topics such as shortest path routing and fair bandwidth assignment in IP/MPLS networks. Addresses proper multi-layer modeling across network layers using different technologies—for example, IP over ATM over SONET, IP over WDM, and IDN over SONET. Covers restoration-oriented design methods that allow recovery from failures of large-capacity transport links and transit nodes. Presents, at the end of each chapter, exercises useful to both students and practitioners.

Advances in Nuclear Science and Technology M. Becker,1992 This twenty-fifth volume in a distinguished series addresses a range of topics including: the difficult matter of questioning scientific hypotheses in court the use of Monte Carlo simulation to evaluate time-dependent development and to study system reliability in nuclear reactors of considerable complexity the genetic optimization algorith wavelet analysis ergonomic design of safer and more efficient plant control rooms.

*Enabling Technology for MEMS and Nanodevices* Henry Baltes, Oliver Brand, Gary K. Fedder, Christofer Hierold, Jan G. Korvink, Osamu Tabata, 2013-03-27 Microstructures, electronics, nanotechnology - these vast fields of research are growing together as the size gap narrows and many different materials are combined. Current research, engineering sucesses and newly commercialized products hint at the immense innovative potentials and future applications that open up once mankind controls shape and function from the atomic level right up to the visible world without any gaps. Sensor systems, microreactors, nanostructures, nanomachines, functional surfaces, integrated optics, displays, communications technology, biochips, human/machine interfaces, prosthetics, miniaturized medical and surgery equipment and many more opportunities are being explored. This new series, Advanced Micro & Nanosystems, provides cutting-edge reviews from top authors on technologies, devices and advanced

systems from the micro and nano worlds.

<u>Survivability and Traffic Grooming in WDM Optical Networks</u> Arun Somani,2006-01-12 This book provides coverage of survivability and traffic grooming; two key issues in modern optical networks.

Parallel Computing Using Optical Interconnections Keqin Li,Yi Pan,Si-Qing Zheng,1998-10-31 Advances in optical technologies have made it possible to implement optical interconnections in future massively parallel processing systems. Photons are non-charged particles, and do not naturally interact. Consequently, there are many desirable characteristics of optical interconnects, e.g. high speed (speed of light), increased fanout, high bandwidth, high reliability, longer interconnection lengths, low power requirements, and immunity to EMI with reduced crosstalk. Optics can utilize free-space interconnects as well as guided wave technology, neither of which has the problems of VLSI technology mentioned above. Optical interconnections can be built at various levels, providing chip-to-chip, module-to-module, board-to-board, and node-to-node communications. Massively parallel processing using optical interconnections poses new challenges; new system configurations need to be designed, scheduling and data communication schemes based on new resource metrics need to be investigated, algorithms for a wide variety of applications need to be developed under the novel computation models that optical interconnections permit, and so on. Parallel Computing Using Optical Interconnections is a collection of survey articles written by leading and active scientists in the area of parallel computing using optical interconnections. This is the first book which provides current and comprehensive coverage of the field, reflects the state of the art from high-level architecture design and algorithmic points of view, and points out directions for further research and development.

IAENG Transactions on Engineering Technologies Gi-Chul Yang, Sio-long Ao, Len Gelman, 2014-07-08 This book contains fifty-eight revised and extended research articles written by prominent researchers participating in the Advances in Engineering Technologies and Physical Science conference, held in London, U.K., 4-6 July, 2012. Topics covered include Applied and Engineering Mathematics, Computational Statistics, Mechanical Engineering, Bioengineering, Internet Engineering, Wireless Networks, Knowledge Engineering, Computational Intelligence, High Performance Computing, Manufacturing Engineering, and industrial applications. The book offers the state of art of tremendous advances in engineering technologies and physical science and applications, and also serves as an excellent reference work for researchers and graduate students working on engineering technologies and physical science and applications.

Advanced Optical Wireless Communication Systems Shlomi Arnon, John Barry, George Karagiannidis, Robert Schober, Murat Uysal, 2012-05-24 Combines theory with real-world case studies to give a comprehensive overview of modern optical wireless technology.

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